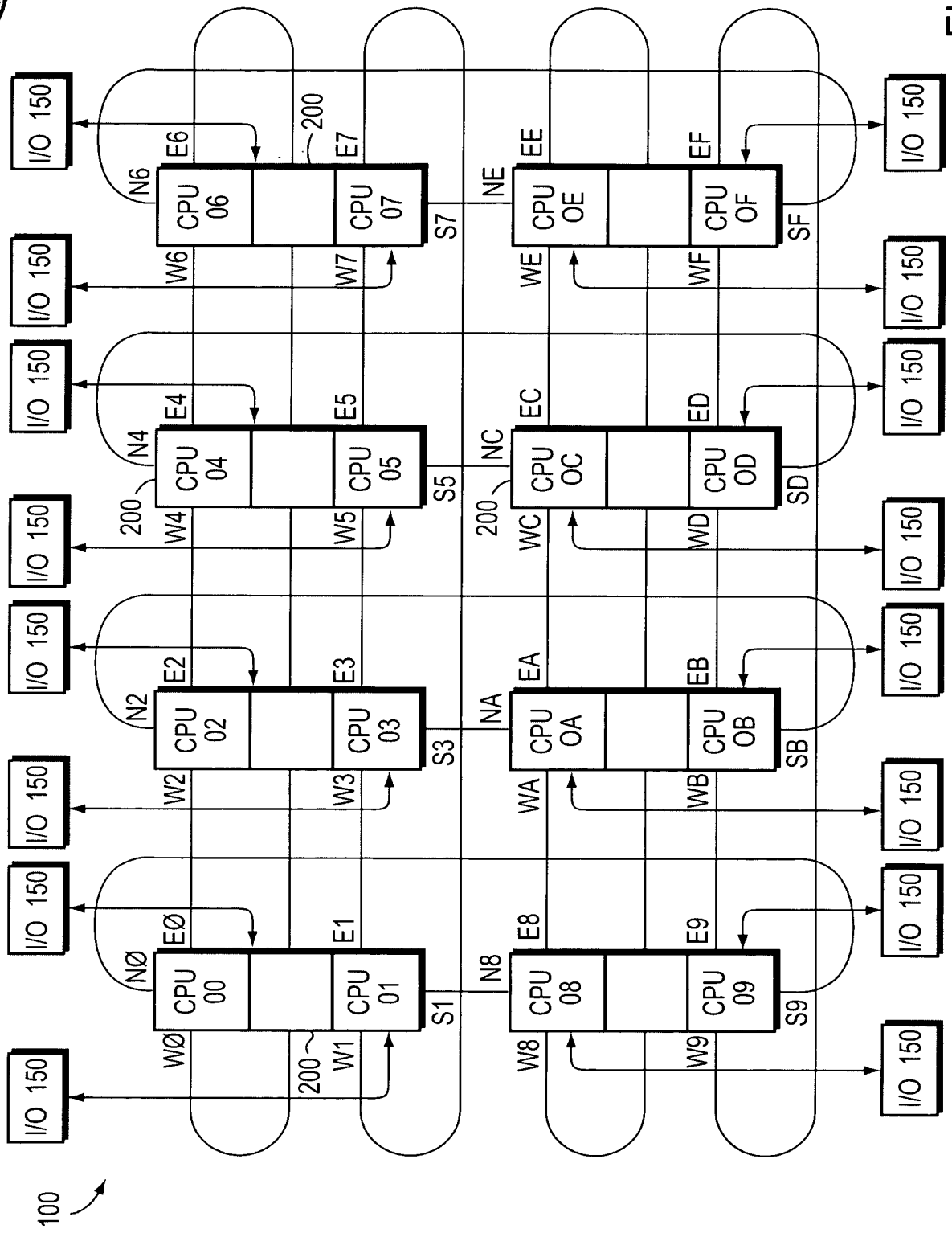


FIG. 1

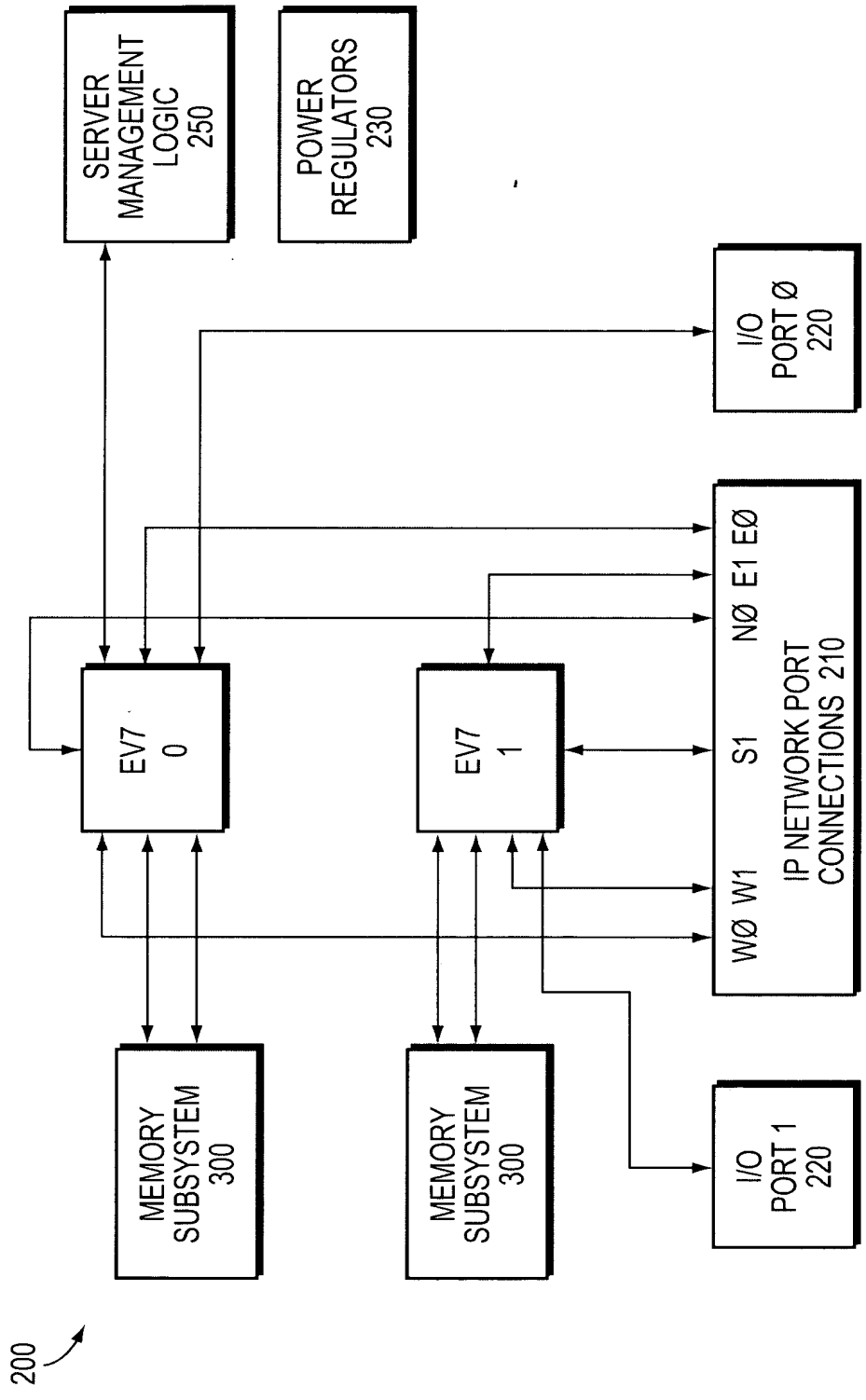

SERVER MANAGEMENT LOGIC 250
POWER REGULATORS 230
I/O PORT Ø 220
EV7 0
EV7 1
NØ E1 EØ
S1
WØ W1
IP NETWORK PORT CONNECTIONS 210
MEMORY SUBSYSTEM 300
MEMORY SUBSYSTEM 300
I/O PORT 1 220
200

FIG. 2

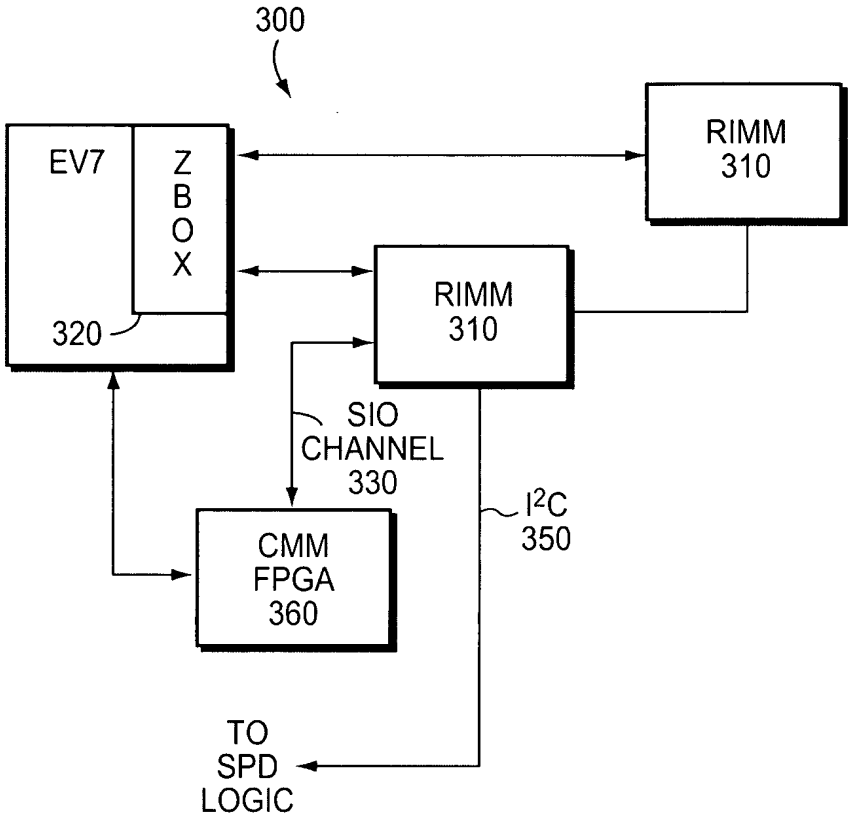

300
EV7
Z
B
O
X
320
RIMM
310
RIMM
310
SIO
CHANNEL
330
I2C
350
CMM
FPGA
360
TO
SPD
LOGIC

FIG. 3

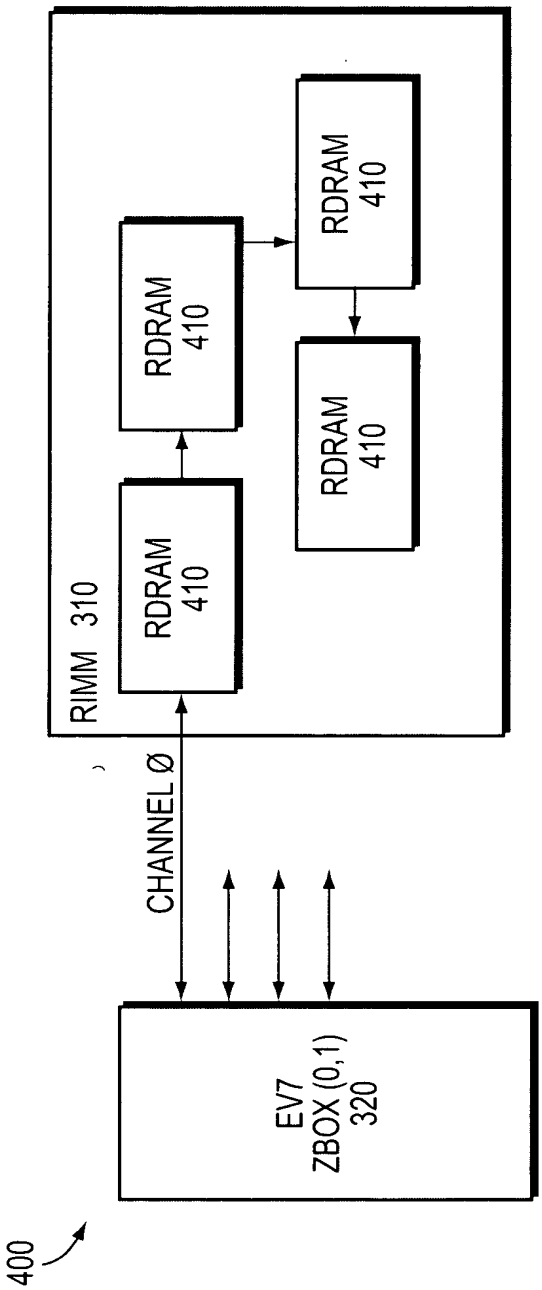

RIMM 310
RDRAM 410
RDRAM 410
RDRAM 410
RDRAM 410
CHANNEL Ø
EV7 ZBOX (0,1) 320
400

FIG. 4

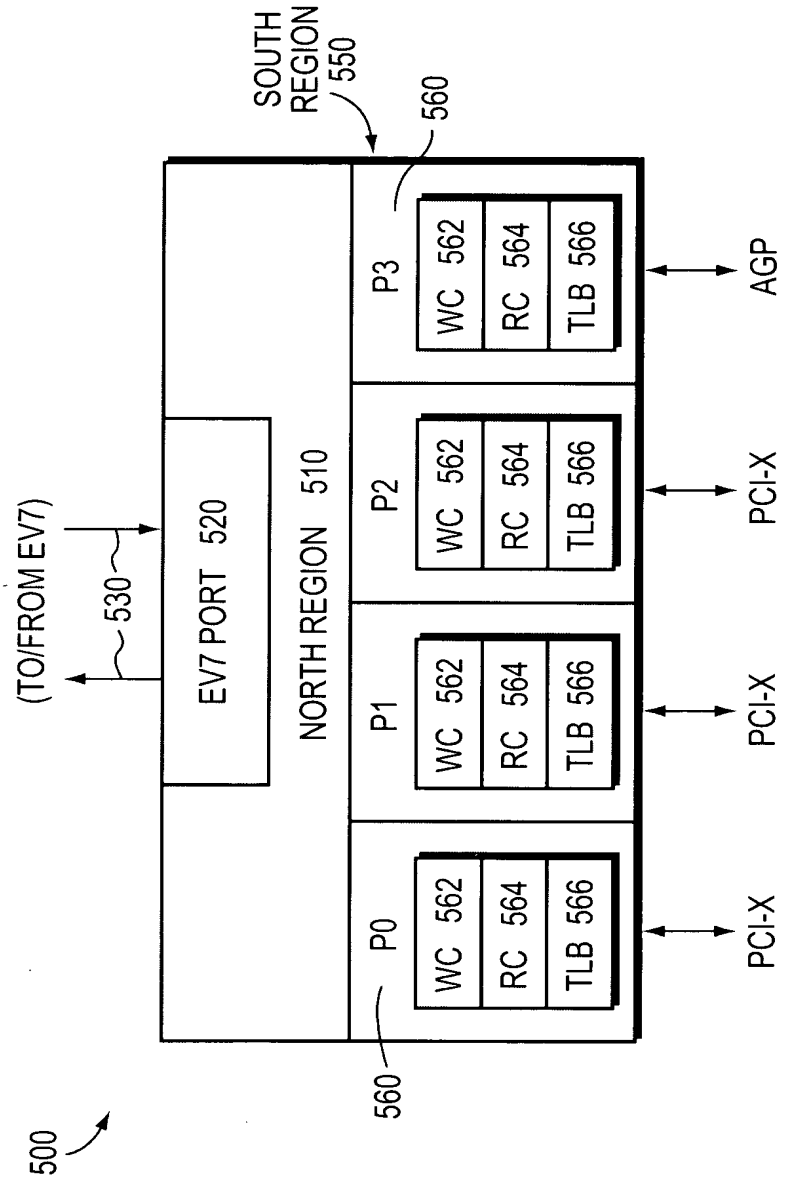

500
SOUTH REGION 550
NORTH REGION 510
EV7 PORT 520
530
(TO/FROM EV7)
560
560
P3
P2
P1
P0
WC 562
RC 564
TLB 566
AGP
PCI-X
PCI-X
PCI-X

FIG. 5

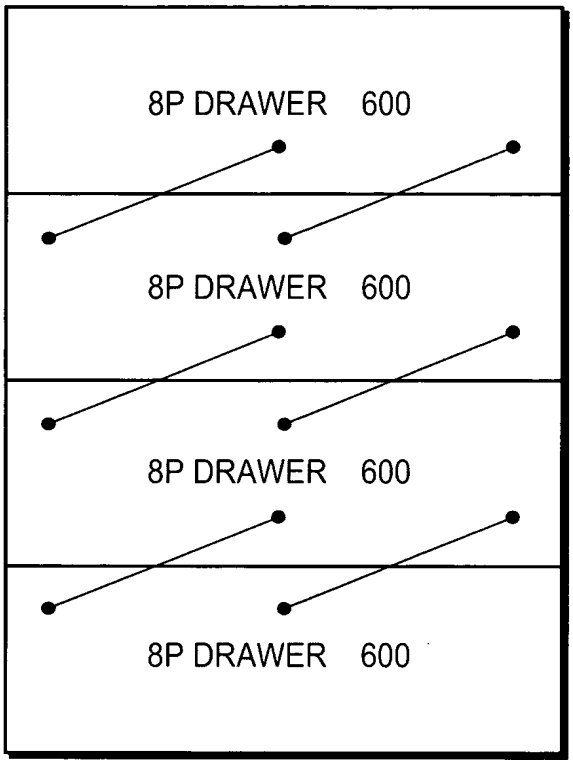


FIG. 6

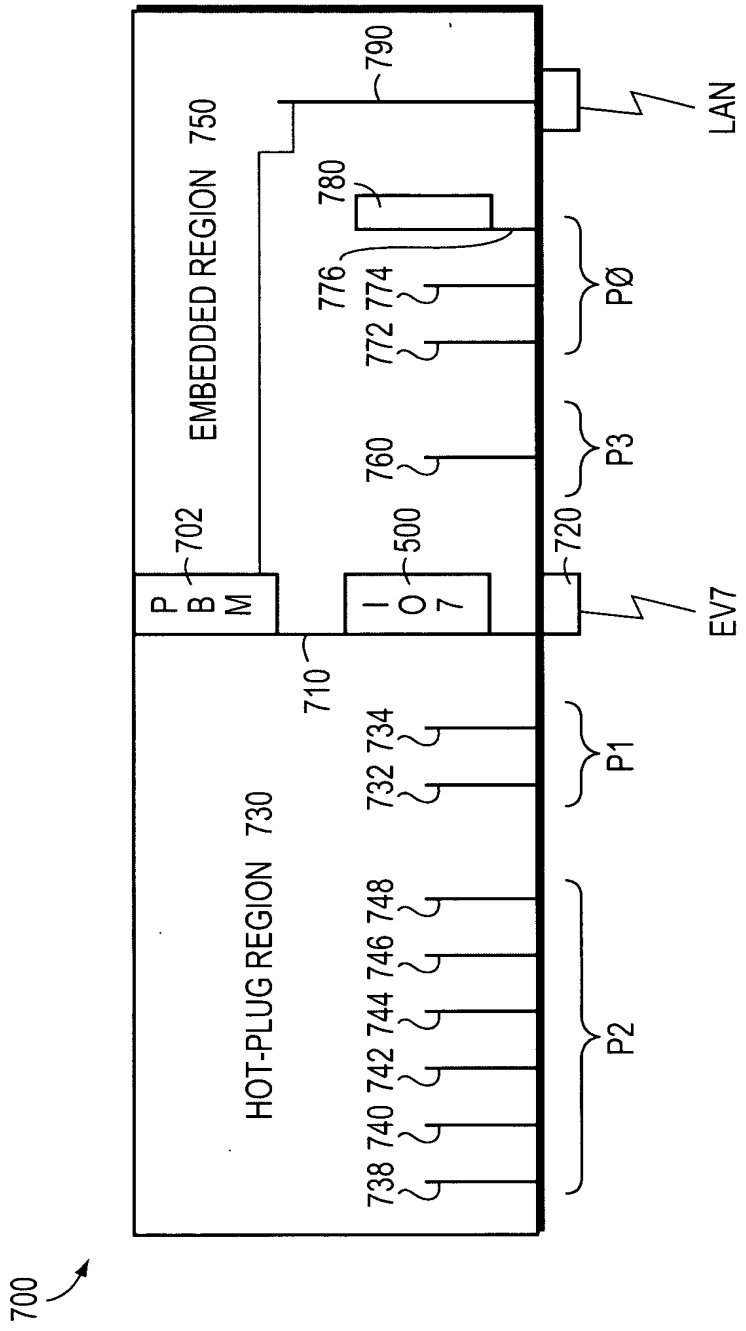

700
HOT-PLUG REGION 730
EMBEDDED REGION 750
PBM
702
710
IO7
500
720
EV7
LAN
790
780
776
774
772
760
738
740
742
744
746
748
732
734
P2
P3
PØ
P1

FIG. 7

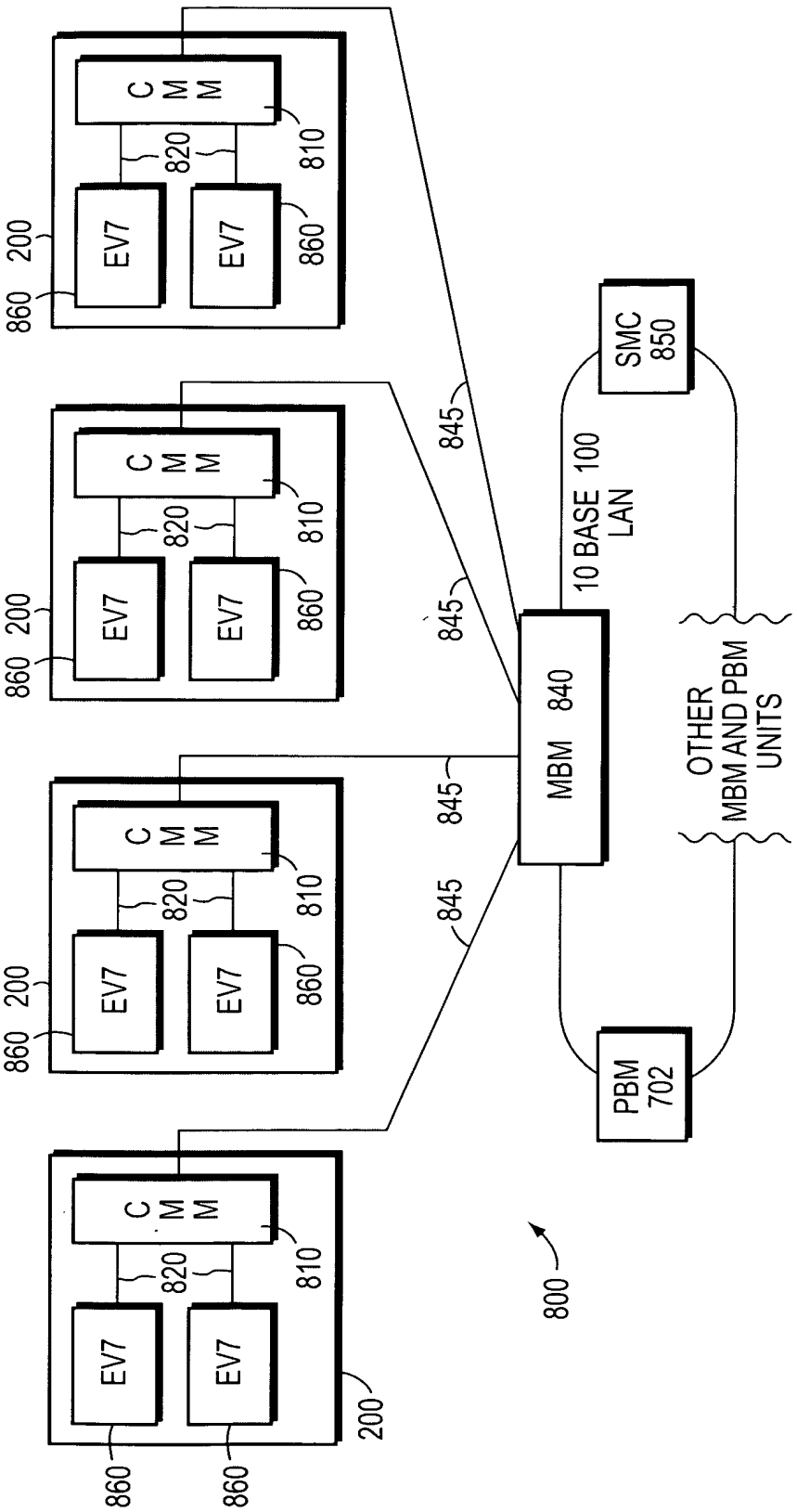

CMM
810
820
EV7
860
200
845
MBM 840
10 BASE 100 LAN
SMC 850
PBM 702
OTHER MBM AND PBM UNITS
800

FIG. 8

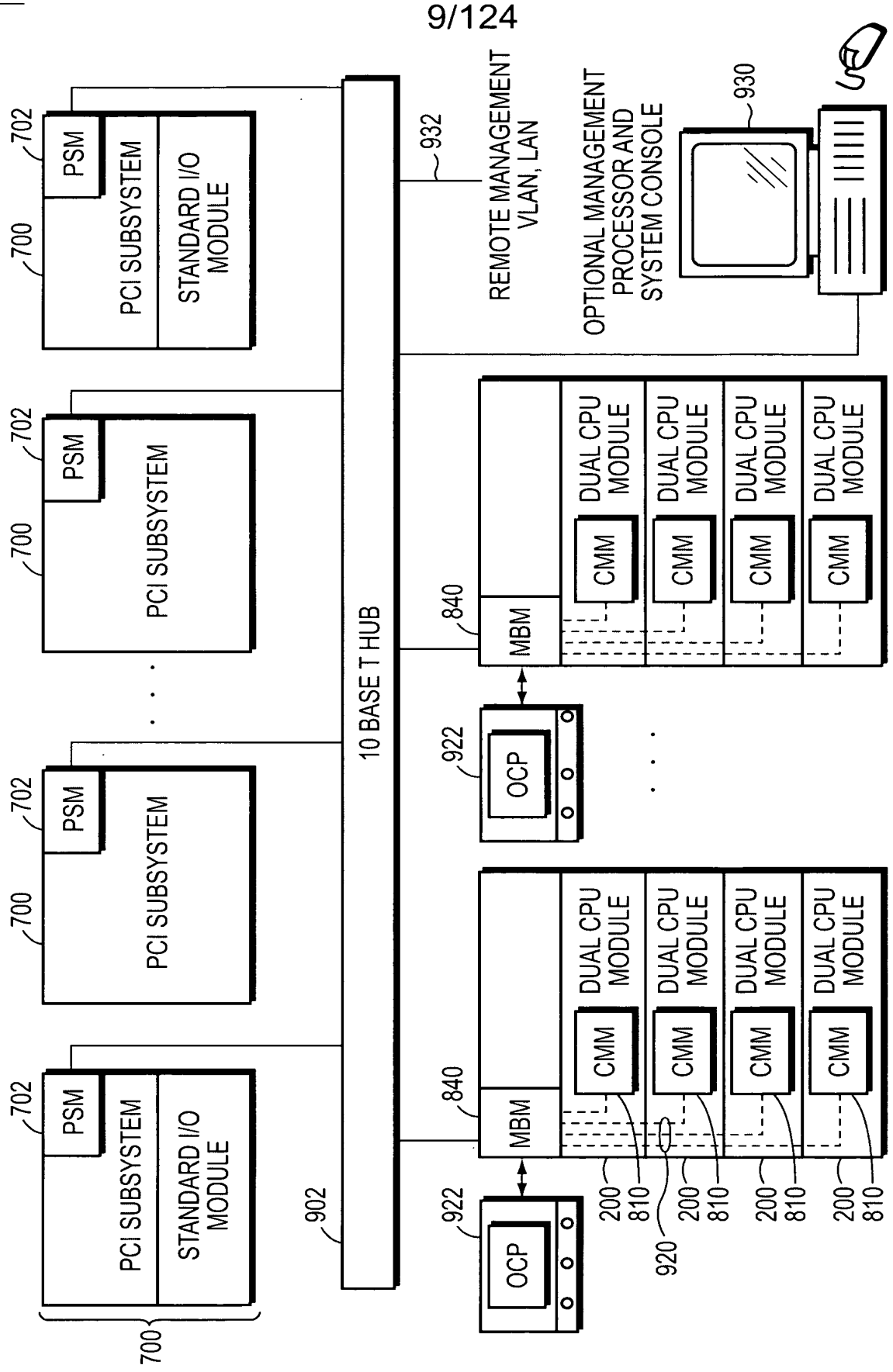

700
PCI SUBSYSTEM
STANDARD I/O MODULE
PSM
702
902
10 BASE T HUB
932
REMOTE MANAGEMENT VLAN, LAN
OPTIONAL MANAGEMENT PROCESSOR AND SYSTEM CONSOLE
930
840
MBM
DUAL CPU MODULE
CMM
922
OCP
904
200
810
920

FIG. 9

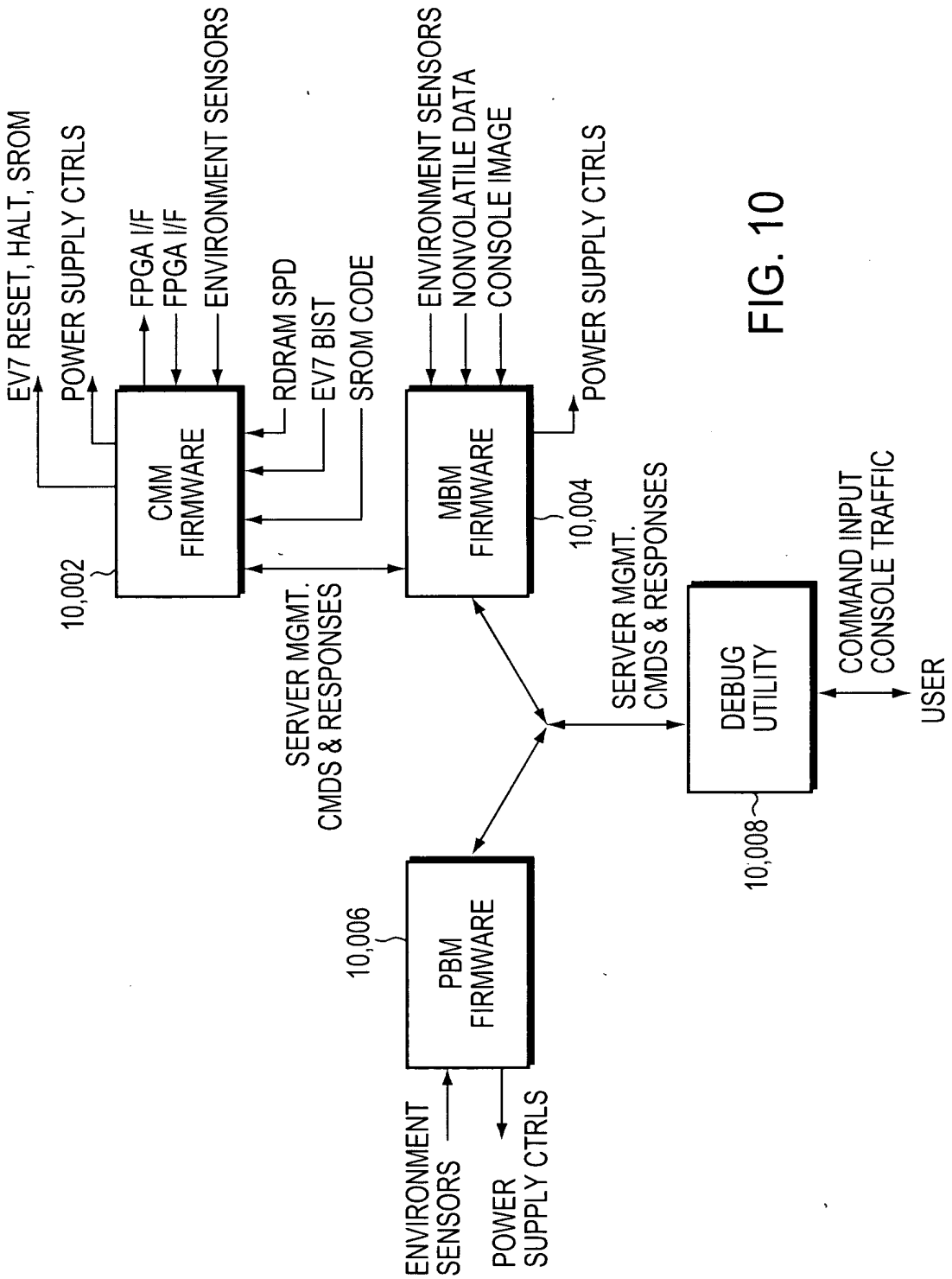

EV7 RESET, HALT, SROM
POWER SUPPLY CTRLS
FPGA I/F
FPGA I/F
ENVIRONMENT SENSORS
RDRAM SPD
EV7 BIST
SROM CODE
ENVIRONMENT SENSORS
NONVOLATILE DATA
CONSOLE IMAGE
POWER SUPPLY CTRLS
CMM FIRMWARE
10,002
SERVER MGMT. CMDS & RESPONSES
MBM FIRMWARE
10,004
SERVER MGMT. CMDS & RESPONSES
DEBUG UTILITY
COMMAND INPUT CONSOLE TRAFFIC
USER
10,008
PBM FIRMWARE
10,006
ENVIRONMENT SENSORS
POWER SUPPLY CTRLS

FIG. 10

COMMAND	DESCRIPTION
CONNECT	CONNECT A VIRTUAL CONSOLE SESSION TO AN SRM FIRMWARE INSTANCE
DISCONNECT	DISCONNECT A VIRTUAL CONSOLE SESSION FROM AN SRM FIRMWARE INSTANCE
POWER {OFF,ON} ITEM	CHANGE THE POWER STATE OF AN ITEM (CPU, 8P UNIT, I/O DRAWER)
HALT PROCESSOR	ISSUE A HALT TO AN EV7 PROCESSOR (SPECIFIED BY PROCESSOR ID)
RESET [PARTITION]	ISSUE A SYSTEM RESET OR A RESET TO THE SPECIFIED PARTITION
RESET PROCESSOR	ISSUE A RESET TO AN EV7 PROCESSOR (SPECIFIED BY PROCESSOR ID)
RESET {CMM, MBM PBM}	ISSUE A RESET TO ONE OF THE SERVER MANAGEMENT PROCESSORS
SET MANUFACTURING	SET SERIAL NUMBER, OTHER FRU DATA
SET PARTITION	DEFINE THE NUMBER OF PARTITIONS, ASSIGN PARTITIONABLE RESOURCES TO EACH PARTITION, DEFINE THE PARTITION PERMISSIONS.
SHOW CONFIG	SHOW THE ENTIRE SYSTEM CONFIGURATION
SHOW CPU {PROCESSOR}	SHOW DATA ON AN EV7 CPU (SPECIFIED BY PROCESSOR ID) OR ALL CPUs
SHOW LAN	SHOW THE NODES ON THE SERVER MANAGEMENT LAN
SHOW MEMORY	SHOW INFORMATION ON MEMORY CONFIGURATION
SHOW PARTITIONS	SHOW THE DEFINED PARTITION DATA
SHOW FRU	SHOW THE FLU DATA FOR THE SYSTEM FRUs
SHOW POWER	SHOW THE THERMAL AND VOLTAGE SENSOR DATA
SHOW ERROR	SHOW THE NON-VOLATILE SAVED ERROR STATE
CLEAR ERROR	CLEAR THE NON-VOLATILE SAVED ERROR STATE
UPDATE	UPDATE SYSTEM FIRMWARE
DATE	SET / SHOW THE SERVER MANAGEMENT TIME
EXAMINE / DEPOSIT	DISPLAY / MODIFY MEMORY
TEST PROCESSOR n	RUN THE TEST IDENTIFIED BY n ON THE SPECIFIED PROCESSOR ID
SET TEST SIGNAL PROCESSOR n	ASSERT CABLE TEST SIGNAL FOR PORT n (N, S, E, W, I/O) ON THE SPECIFIED PROCESSOR ID AND LIGHT THE CABLE LED.
CLEAR TEST SIGNAL PROCESSOR n	DE-ASSERT CABLE TEST SIGNAL FOR PORT n (N, S, E, W, I/O) ON THE SPECIFIED PROCESSOR ID AND EXTINGUISH THE CABLE LED.
CHECK TEST SIGNAL PROCESSOR n	TEST CABLE TEST SIGNAL FOR PORT n (N, S, E, W, I/O) ON THE SPECIFIED PROCESSOR ID.

EXTERNAL SERVER MANAGEMENT COMMANDS

FIG. 11

COMMAND	DESCRIPTION
PutChart	SEND A CHARACTER TO THE OPERATOR DISPLAY
GetChar	GET CHARACTER FROM THE OPERATOR KEYBOARD
SetTermInt	SET OPERATOR TERMINAL INTERRUPT SETTING
HELLO	ANNOUNCE THE PRESENT OF SERVER MANAGEMENT MEMBER
POLL	PROBE FOR THE PRESENCE OF A SPECIFIC SERVER MANAGEMENT MEMBER
NO-OP	NO OPERATION, USED FOR TESTING
SysError DATA	SYSTEM ERROR STATE INFORMATION TO BE SAVED
FRUError ID, DATA	STORE FRU ERROR DATA IN THE FRU SPECIFIED BY ID

INTERNAL SERVER MANAGEMENT COMMANDS

FIG. 12

CELL	DESCRIPTION
SECONDS	SECOND COUNT, 0-59, BINARY FORMAT
MINUTES	MINUTE COUNT, 0-59, BINARY FORMAT
HOURS	HOUR COUNT, 0-23, BINARY FORMAT
DAY	DAY OF THE MONTH, 1-31, BINARY FORMAT
MONTH	MONTH OF THE YEAR, 1-12, BINARY FORMAT
YEAR	YEAR, 0-99, BINARY FORMAT

BB_WATCH DATA

FIG. 13

MESSAGE	DESCRIPTION
CSB_READ	READ DATA ELEMENT FROM THE PMB
CSB_WRIT E	WRITE DATA ELEMENTS TO THE PMB
CSB_POLL	OBTAIN PMB STATUS

CSB MESSAGES

FIG. 14

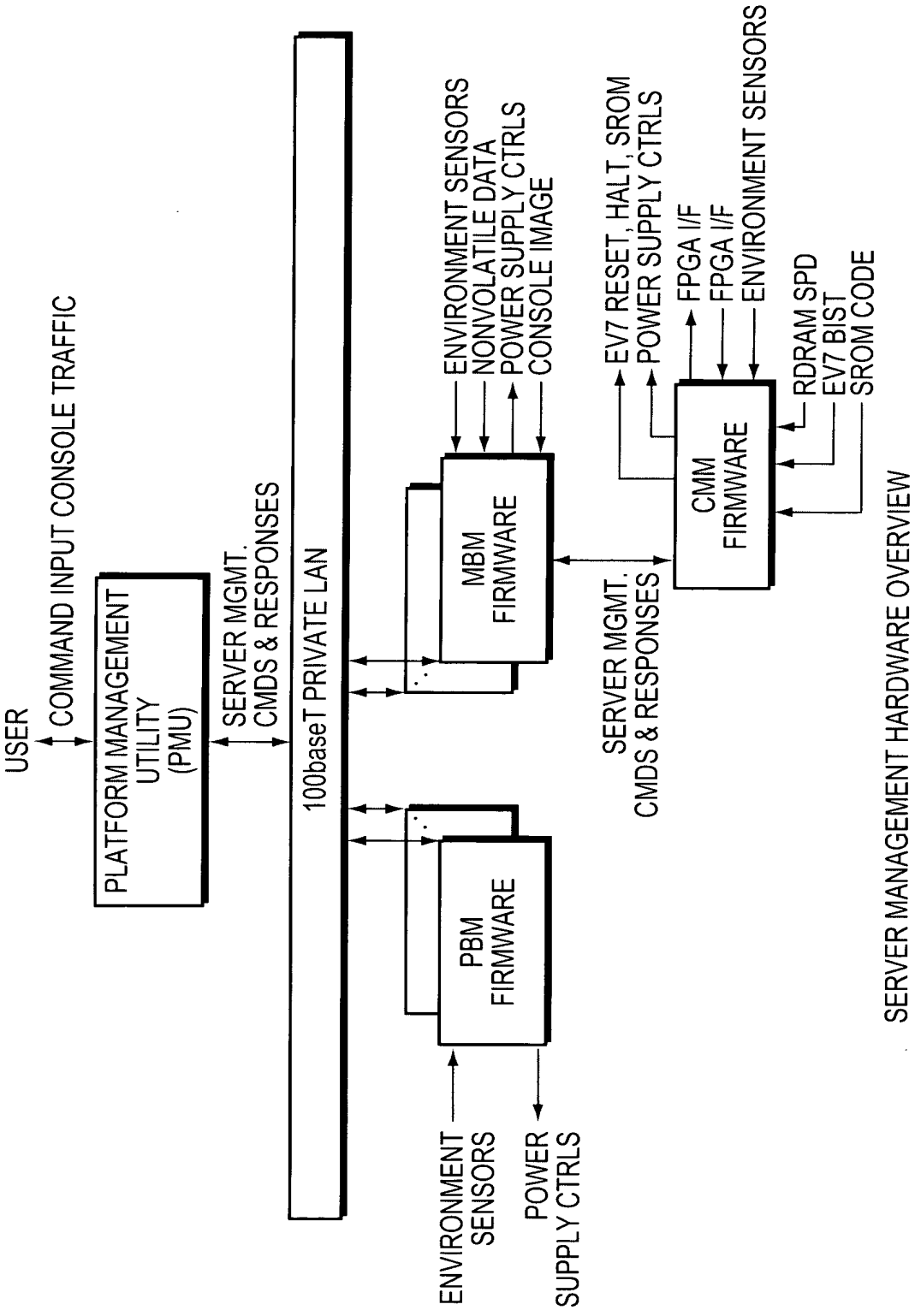
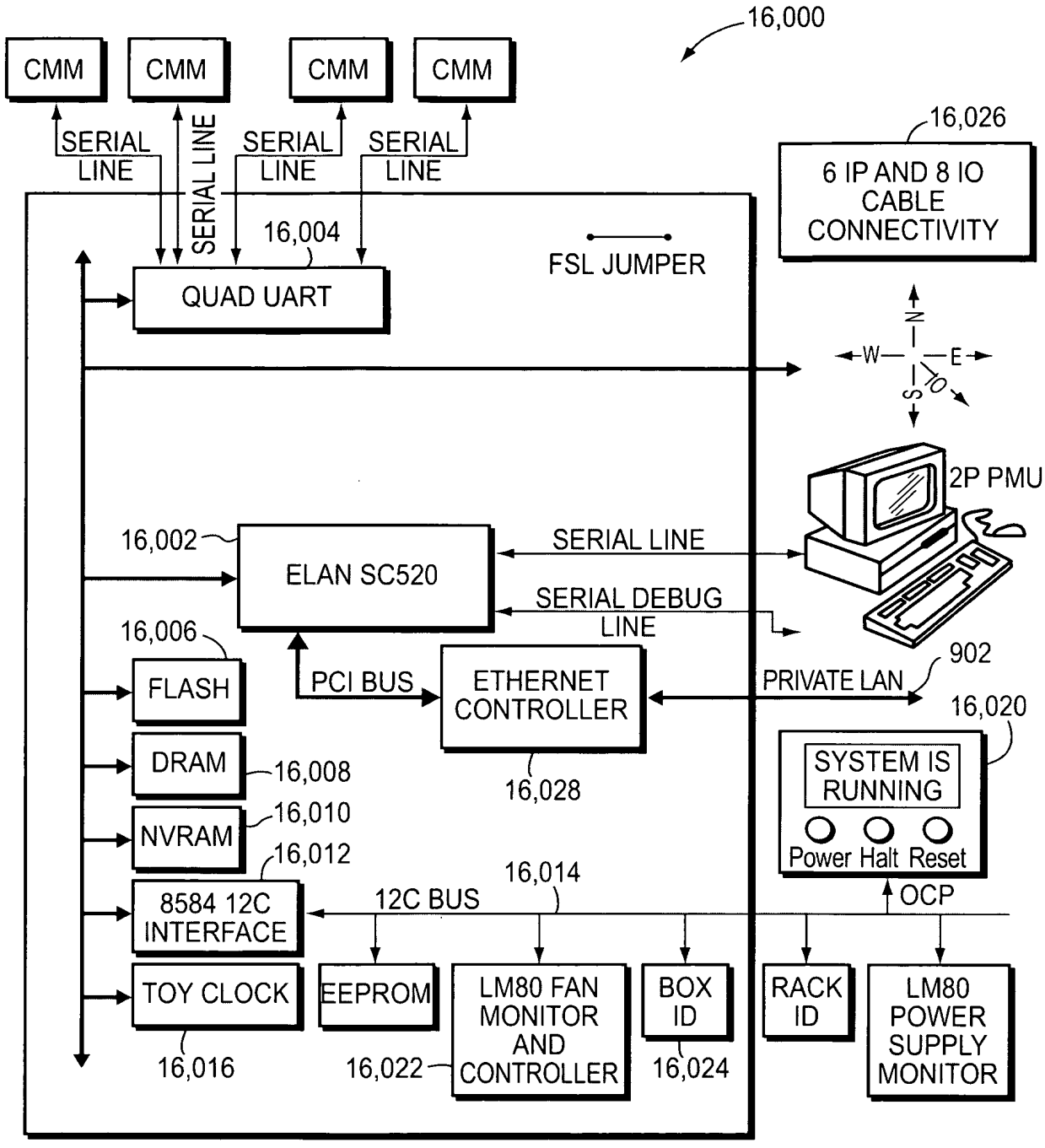

USER
COMMAND INPUT CONSOLE TRAFFIC
PLATFORM MANAGEMENT UTILITY (PMU)
SERVER MGMT. CMDS & RESPONSES
100baseT PRIVATE LAN
ENVIRONMENT SENSORS
NONVOLATILE DATA
POWER SUPPLY CTRLS
CONSOLE IMAGE
MBM FIRMWARE
PBM FIRMWARE
ENVIRONMENT SENSORS
POWER SUPPLY CTRLS
SERVER MGMT. CMDS & RESPONSES
EV7 RESET, HALT, SROM
POWER SUPPLY CTRLS
FPGA I/F
FPGA I/F
ENVIRONMENT SENSORS
CMM FIRMWARE
RDRAM SPD
EV7 BIST
SROM CODE
SERVER MANAGEMENT HARDWARE OVERVIEW

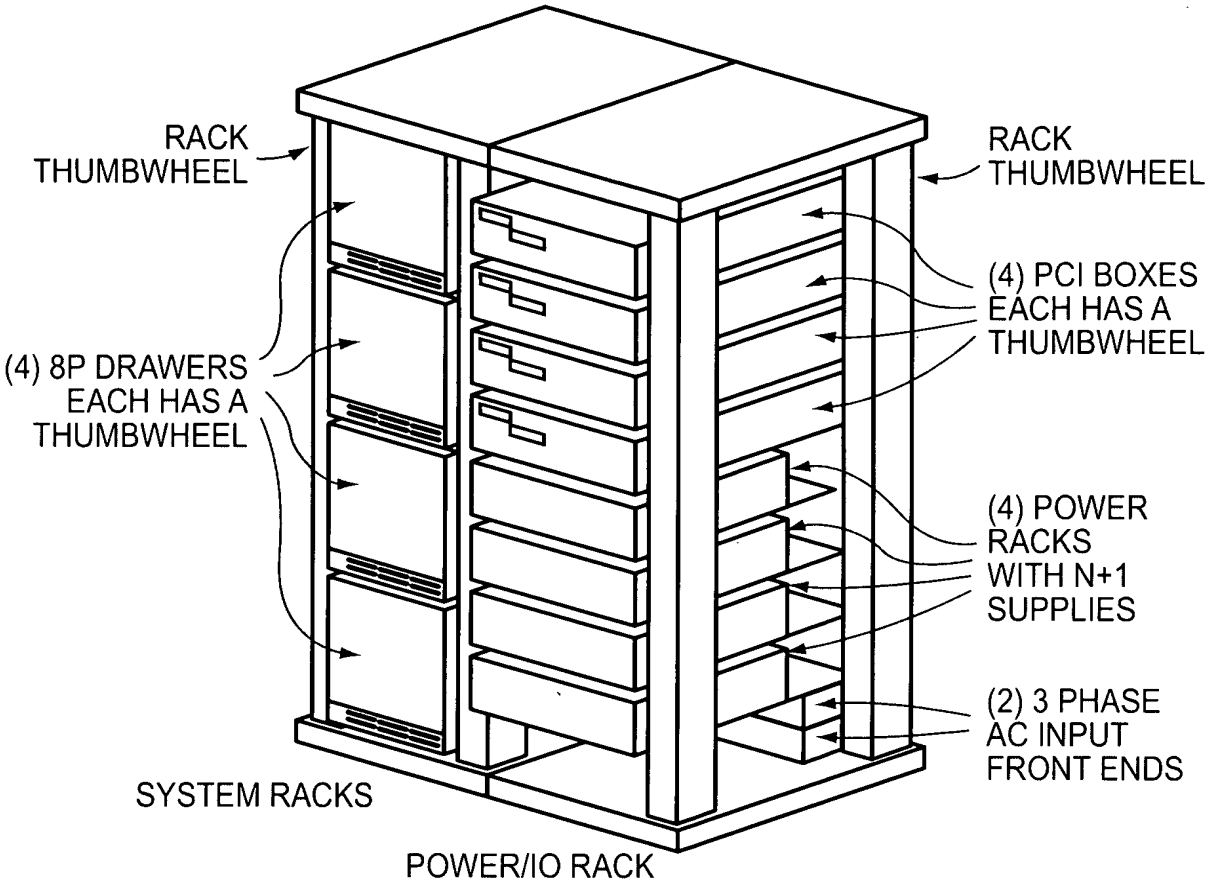
FIG. 15



MBM HARDWARE OVERVIEW

FIG. 16

32P MARVEL SYSTEM 2M RACK



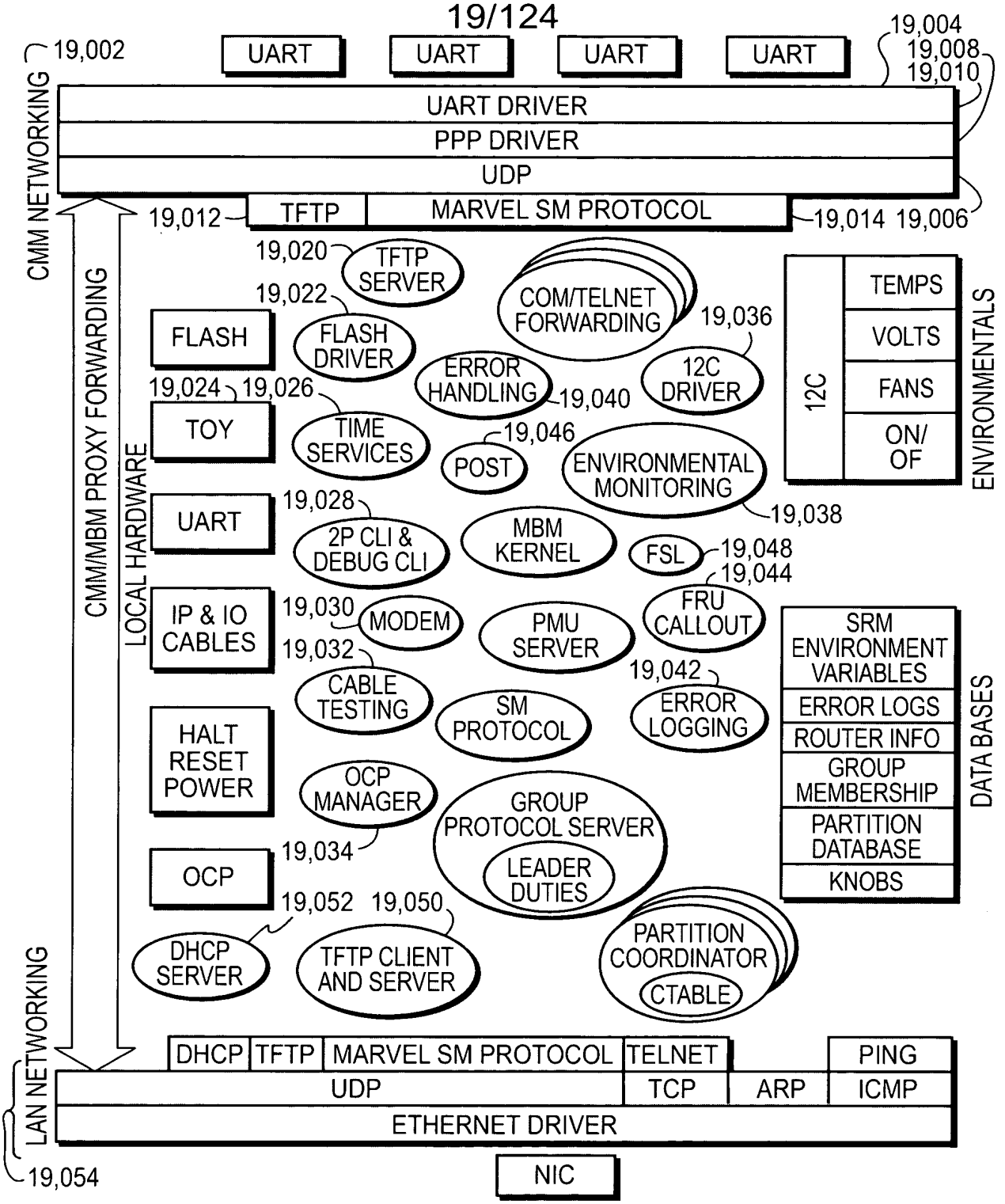
RACK AND BOX THUMBWHEEL SCHEME

FIG. 17

TASK	HOW MANY	WHERE
GROUP LEADER	1 PER MARVEL SYSTEM[1]	LOWEST MBM IN GROUP
PMU SERVER	1 PER MARVEL SYSTEM[1]	LOWEST MBM IN GROUP
PARTITION COORDINATOR	1 PER HARD PARTITION; MAX 8 PER MBM	MBM WITH LOWEST EV7 IN HARD PARTITION
TELNET SERVER	2 PER SUBPARTITION (COM 0, COM 1)	GRANDPARENT MBM OF PRIMARY EV7
DHCP	1 PER MARVEL SYSTEM[1]	LOWEST MBM IN GROUP

MBM TASK ATTRIBUTES

FIG. 18



MBM FIRMWARE OVERVIEW

FIG. 19

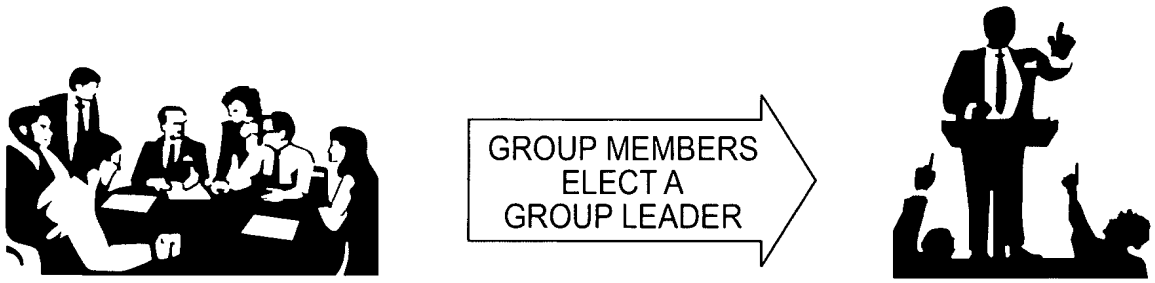


FIG. 20

GENERAL PHASE	GROUP CHANGES	SPECIFIC STEPS	NOTES
INDEPENDENT PROCESSING	UNAFFECTED	POST	
		H/W POLL STATUS AND STATE	HANDLE MBM HOT SWAP. POWER THAT IS ON IS LEFT ON, POWER THAT IS OFF IS LEFT OFF
GROUP FORMATION MAJORITY/ MINORITY REPLICATED DATA SYNC	NORMAL GROUP PROCESSING, NEW GROUP IS FORMED	NEW GROUP IS INITIATED. A GROUP FORMS AND A LEADER IS SELECTED.	
		IF THERE WAS NOT A PREVIOUS MAJORITY GROUP, THEN THE REPLICATED DATABASE IS MARKED INVALID.	
		IF THE NEW GROUP IS A MINORITY, MARK THE DATABASE READ-ONLY.	
		IF THE NEW GROUP IS MAJORITY, REQUEST THE DATABASE AND ANY PENDING UPDATES FROM ALL MEMBERS WHO WERE PREVIOUSLY JOINED TO THE MaxPrevMajorityGroup.	
		APPLY THE LONGEST LIST OF UPDATES TO THE CORRESPONDING DATABASE COPY AND SEND THE NEW INITIAL REPLICATED DATABASE TO ALL MEMBERS. ALL MEMBERS MARK THE DATABASE AS VALID	
		IF THE NEW GROUP IS A MAJORITY AND THERE WAS NO PREVIOUS MAJORITY GROUP, THEN CLEAR THE powerup_complete FLAG.	
		IF THE NEW GROUP IS A MAJORITY AND THE powerup_complete FLAG IS SET, THEN PROCEED TO THE HARDWARE INIT PHASE. ELSE, PROCEED TO THE OPERATIONAL PHASE.	MINORITY GROUPS REMAIN IN THIS PHASE
HARDWARE INIT	RETURN TO FORMING A NEW GROUP AND RERUN INIT	POLL CMMs TO DETERMINE CPU MODULE POPULATION DETAILS WITHIN EACH 8P BACKPLANE. THIS INITIALIZES THE LIST OF AVAILABLE RESOURCES.	
		THE LEADER COMPUTES A ROUTABLE CONFIGURATION FOR EACH PARTITION BASED UPON THE REQUESTED AND AVAILABLE CPU RESOURCES	

MARVEL SYSTEM POWERUP FLOW WITH GROUP RELATIONSHIP (PART 1)

FIG. 21A

GENERAL PHASE	GROUP CHANGES	SPECIFIC STEPS	NOTES
HARDWARE INIT	RETURN TO FORMING A NEW GROUP AND RERUN INIT	THE LEADER DECIDES TO POWER UP THE PARTITIONS. IT COMMANDS ALL MBMs, PBMs, AND CMMs TO POWER UP. UPON COMPLETION, THE LEADER OBTAINS THE RESULTS FROM EACH OF THESE COMMANDS AND ADJUSTS THE LIST OF AVAILABLE RESOURCES ACCORDINGLY.	
		THE PARTITION COORDINATORS START XSROM TESTING ON ALL CPUs FOR MEMORY.	
		THE PMU SERVER INITIATE IP CABLE TESTING BETWEEN 8P BACKPLANES AND I/O CRATES. THE RESULTS ARE USED TO RECALCULATE ROUTING AND ASSIGN I/O TO PARTITIONS.	
		THE PARTITION COORDINATORS INITIATE XSROM TESTS FOR I/O AND ROUTING. THE RESULTS ARE USED TO RECALCULATE ROUTING AND ADJUST THE LIST OF I/O RESOURCES.	
		THE PARTITION COORDINATORS INITIATE REMOTE MEMORY TESTING BETWEEN CPUs IN THE SAME PARTITIONS.	
		THE PARTITION COORDINATORS INITIATE INTERRUPT TESTING BETWEEN CPUs IN THE SAME PARTITIONS.	
		THE LEADER SETS THE powerup_complete FLAG TO TRUE AND PROCEEDS TO THE S/W LOAD PHASE.	
S/W LOAD (SRM+O/S)	NEW GROUPS ARE TREATED AS HOT-ADDS	THE PARTITION COORDINATORS ELECT A PRIMARY EV7 IN EACH PARTITION. THEY COMMAND ALL SECONDARIES TO SPINE ON RBOX_SCRATCH AND INITIATE LOADING OF THE SRM FIRMWARE ON THE PRIMARY.	
		THE SRM FIRMWARE COMMANDS THE SECONDARY CPUs TO JOIN BY WRITING RBOX_SCRATCH. THE PRIMARY EV7 COMPLETES ALL I/O INITIALIZATION.	
		IF THE SRM auto_action ENVIRONMENT VARIABLE IS SET TO BOOT, THE OPERATING SYSTEM BOOT IS ATTEMPTED ON THE PARTITION.	
OPERATIONAL	NEW GROUPS ARE TREATED AS HOT-ADDS	SERVER MANAGEMENT REQUESTS FROM THE PRIMARY EV7 IN EACH PARTITION ARE HANDLED BY THE CMMs/MBMs/PBMs. IF A NEW GROUP HAS CAUSED A CHANGE IN THE CPU, MEMORY, OR I/O RESOURCES, NOTIFY THE PRIMARY EV7 IN EACH AFFECTED PARTITION.	
		IF A NEW RESOURCE HAS BEEN PRE-ALLOCATED TO A PARTITION, THEN THE PARTITION COORDINATOR TAKES THE STEPS NECESSARY TO PROBE THE IP OR IO LINKS TO THE NEW RESOURCE (CPU OR I/O DRAWER).	

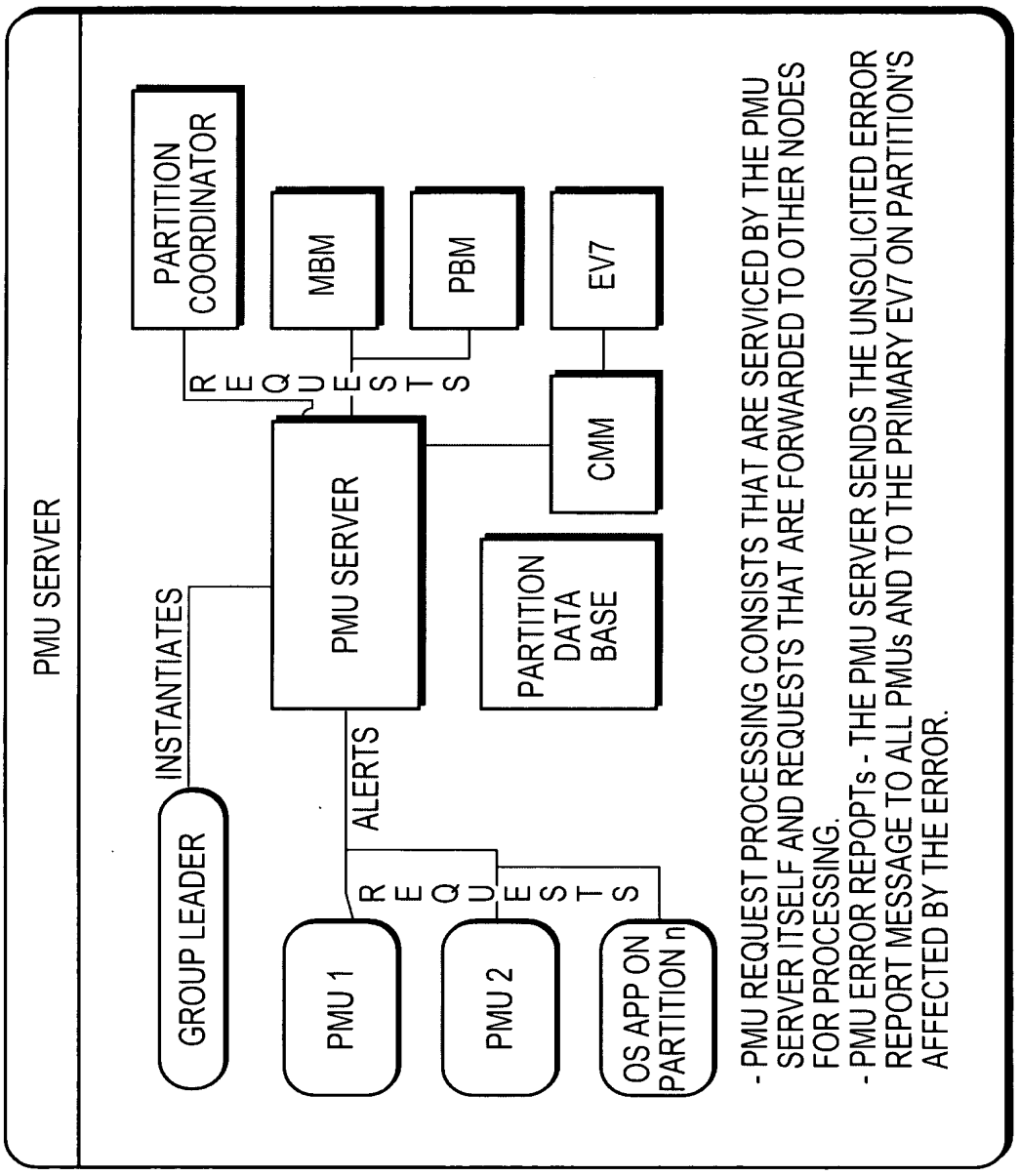
MARVEL SYSTEM POWERUP FLOW WITH GROUP RELATIONSHIP (PART 2)

FIG. 21B

CAPABILITY	VIA PRIVATE LAN	VIA FPGA
VIRTUAL CONSOLE TERMINAL ACCESS	YES	NO
FIRMWARE UPDATES	YES	NO
LOAD/DISABLE TEST FIRMWARE	YES	NO
LIVE CONFIGURATION CHANGE [1]	NO	YES
WRITING SRM ENVIRONMENT VARS [2]	NO	YES
UNSOLICITED NOTIFICATION OF ALERTS	YES	NO
STORE PCI SLOT INFORMATION [2]	NO	YES
ALL OTHERS	YES	YES

LAN VS FPGA PMU CAPABILITY MATRIX

FIG. 22



PMU SERVER BLOCK DIAGRAM

FIG. 23

COMMAND GROUP	COMMANDS RECEIVED	CLASS	PMU SERVER HANDLING METHOD
SYSTEM DISCOVERY	GET MBM/PBM CONFIGURATION	FORWARD	COMMANDS ARE FORWARDED TO THE MBM OR PBM ADDRESSED IN THE MESSAGE HEADER. RESPONSES ARE FORWARDED BACK TO THE PMU
	GET PCI SLOT INFO		
	GET PARTITION DATABASE	DIRECT	THE PMU SERVER DERIVES THE RESPONSE FROM HIS LOCAL COPY OF THE PARTITION DATABASE
	GET OWN PARTITION NUMBER		
	GET SYSTEM TOPOLOGY	DIRECT	THE PMU SERVER KEEPS TRACK OF THE APPLICATIONS MAKING THIS MULTI PHASE REQUEST UNTIL THE LAST ENTITY HAS BEEN REQUESTED. THE ENTITY NUMBER IN THE REQUEST IS USED TO INDEX INTO A LIST COMPOSED OF THE COMBINATION OF GROUP MEMBERS AND PARTITION DATABASE. THE IP ADDRESS, PARENT RELATIONSHIP AND PARTITION NUMBER IS DERIVED FROM THESE VALUES FOUND IN NVRAM. IF THE GROUP MEMBERS OR PARTITION DATABASE CHANGES BEFORE THE LAST ENTITY IS REQUESTED, AN ERROR RESPONSE IS RETURNED ON THE NEXT REQUEST.
	SET PCI SLOT INFO	FORWARD	THE PMU SERVER MUST ENSURE THAT THIS REQUEST IS COMING FROM AN EV7 AND NOT FROM THE PMU ON THE LAN. THESE PACKETS ARE DIRECTED TO THE PBM ASSOCIATED WITH THE SLOT.
PARTITION CONTROL	ALL COMMANDS OF GROUP	FORWARD	ALL COMMANDS IN THIS GROUP THAT CONTAIN A PARTITION NUMBER ARE FORWARDED TO THE MBM RUNNING THE APPROPRIATE PARTITION COORDINATOR. THE EXCEPTIONS ARE READ STATE OF OCP SWITCHES, OCP SWITCH ASSIGNMENT AND POWER ON/OFF COMMANDS THAT ARE SIMPLY FORWARDED TO THEIR DESTINATION.
EV7 SETUP	REQUEST EV7 START TEST	FORWARD	THIS COMMAND IS FORWARDED TO THE DESTINATION EV7

PMU SERVER RECEIVED COMMAND HANDLING

FIG. 24A

COMMAND GROUP	COMMANDS RECEIVED	CLASS	PMU SERVER HANDLING METHOD
CABLE TEST GROUP	GET CABLING CONFIGURATION	DIRECT	THE PMU SERVER RESPONDS WITH THE CONTENTS OF THE CABLE DATABASE THAT HE COMPOSED DURING INITIALIZATION OR WAS REQUESTED VIA RECONFIGURE CABLING.
	RETEST CABLE CONFIGURATION	COMPLEX	THE PMU SERVER RE-INITIATES THE TEST OF ALL IP AND IO CABLING MAKING USE OF THE COMMANDS GET MBM IP CABLING AND GET PBM IP CABLING.
	SET CABLE TEST SIGNAL GET CABLE TEST SIGNAL	COMPLEX	THE PMU SERVER ENSURES THAT THERE ARE NO OTHER ON-GOING CABLING REQUESTS AND FORWARDS THESE COMMANDS TO THE PBM OR MBM IN THE DESTINATION FIELD AND RETURNS THE RESPONSE TO THE PMU.
VIRTUAL CONSOLE	GET TELNET IP ADDRESS/PORT	DIRECT	PMU SERVER DETERMINES THE PRIMARY MBM's IP ADDRESS AND THE SOCKET PORT.
FIRMWARE LOAD AND UPGRADE, ENVIRON-MENTAL RETRIEVAL, FRU DATA, ERROR LOGGING, MISCEL-LANEOUS		FORWARD	ALL COMMANDS IN THESE GROUPS ARE FORWARDED TO THE CMM, PBM OR MBM IN THE DESTINATION FIELD AND THE RESPONSE RETURNED TO THE PMU.
DATA/TIME		FORWARD DIRECT	THE PMU SERVER ALLOWS BASE TIME GETS AND SETS FROM ALL PMUs BUT DELTA TIME SETS AND GETS CAN ONLY COME FROM PARTITION PRIMARY EV7s. THE REQUESTS ARE FORWARDED TO THE MBM BEING ADDRESSED.
MISCEL-LANEOUS	GET/SET KNOB READ/WRITE BLOCK DATA	FORWARD	THESE REQUESTS ARE FORWARDED TO THE DESTINATION FOR PROCESSING.

PMU SERVER RECEIVED COMMAND HANDLING

FIG. 24B

COMMAND GROUP	COMMANDS RECEIVED	PMU SERVER HANDLING METHOD
SYSTEM DISCOVERY	GET MBM/PBM CONFIGURATION	
	GET PARTITION DATABASE	
CABLE TESTING	SEND CABLE ID RECEIVE CABLE ID GET MBM IP CABLING GET PBM IP CABLING	THESE COMMANDS ARE ISSUED IN RESPONSE TO A RETEST CABLE CONFIGURATION REQUEST. THE PROCESS IS DISCUSSED IN SECTION ERROR! REFERENCE SOURCE NOT FOUND., ERROR! REFERENCE SOURCE NOT FOUND.
ERROR LOGGING GROUP	ERROR REPORTING	THE PMU SERVER KNOWS THE IP ADDRESS OF CLIENT PMUs AND DISTRIBUTES THE ALERTS TO EACH PMU.
MISCEL-LANEOUS	DISTRIBUTE DHCP LEASE DATA	THE DHCP SERVER RUNS ON THE PMU SERVER AND KEEPS TRACK OF THE DHCP CLIENTS. FOR FAILOVER PURPOSES, THIS DATA IS REPLICATED ON ALL NODES. SEE SECTION ERROR! REFERENCE SOURCE NOT FOUND. ERROR! BOOKMARK NOT DEFINED..

PMU SERVER ORIGINATING COMMANDS

FIG. 25

28/124

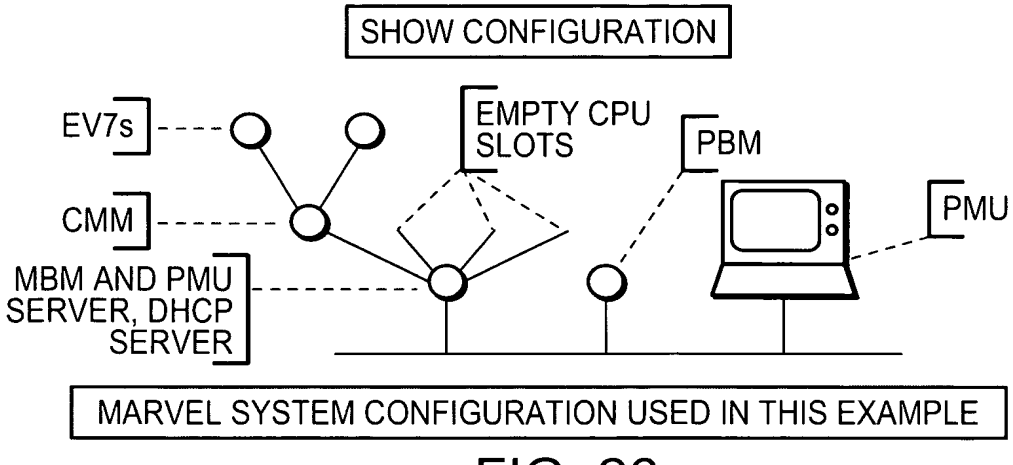


FIG. 26

PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMUs PC CONNECTS TO THE LAN AND REQUESTS AN IP ADDRESS
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE MBM IS RUNNING A DHCP SERVER AND PROVIDE IT A LEASE ON A DHCP ADDRESS.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU APPLICATION WHEN INITIALIZING ISSUES THE GET SYSTEM TOPOLOGY COMMAND STARTING AT 0. THE COMMAND IS ISSUED TO THE PMU SERVER WHICH IS A PREDETERMINED ADDRESS
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	ENTITY 0, THE DATA FOR THE MBM IS RETURNED.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	PMU SENDS A GET MBM CONFIGURATION TO THE MBM AND PICKS UP INFORMATION, LIKE THE RIMM POPULATION
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU SERVER TASK, FORWARDS THE COMMAND TO THE MBM SM PROTOCOL SERVICING TASK. IT RETURNS INFORMATION ON THE CPUs THAT IS HAS GATHERED PREVIOUSLY.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU ISSUES THE GET SYSTEM TOPOLOGY FOR ENTITY 1
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	ENTITY 1, THE CMM, IS RETURNED.

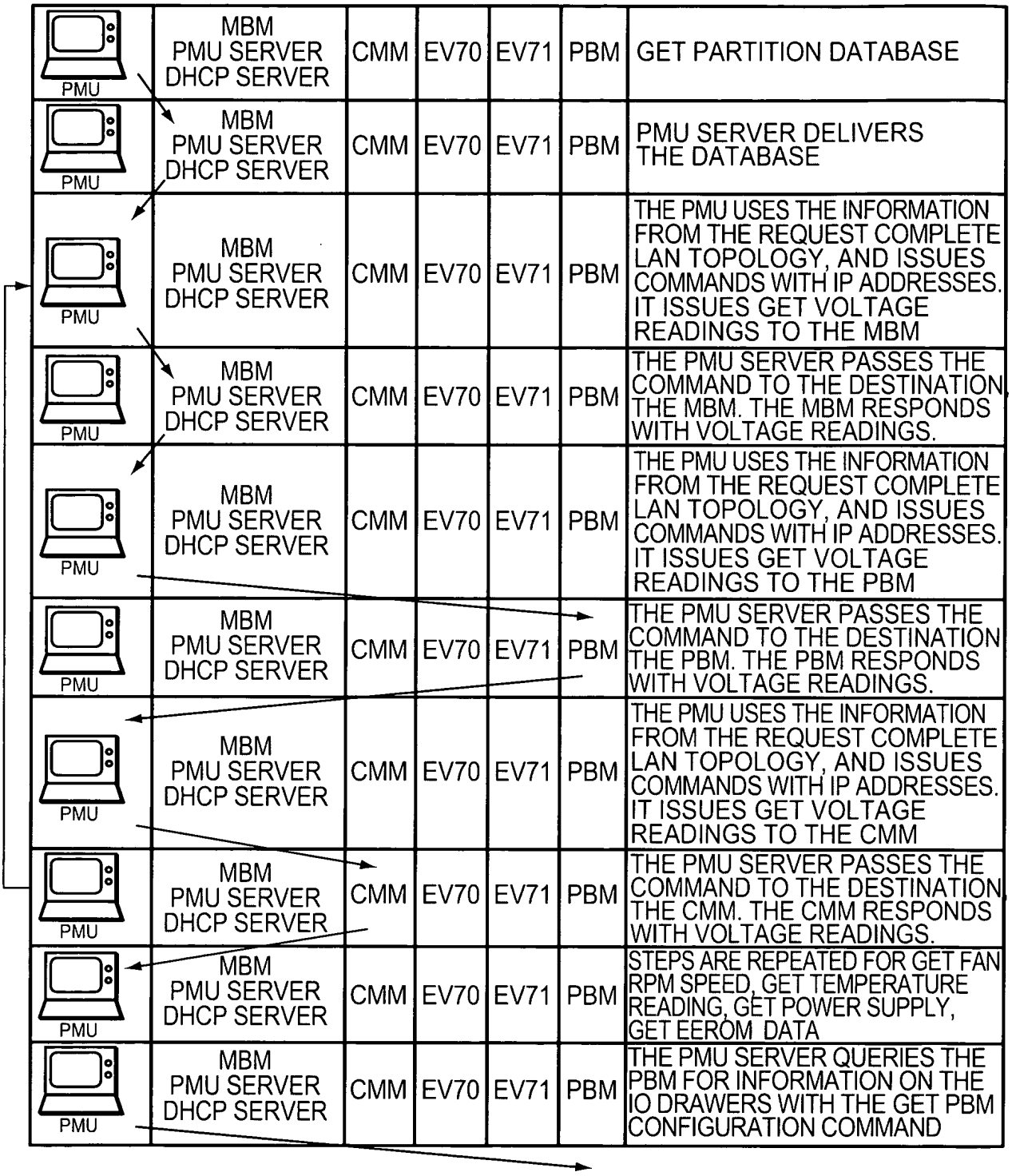
SHOW CONFIG FLOW DIAGRAM (PART 1)

FIG. 27

PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU ISSUES THE GET SYSTEM TOPOLOGY FOR ENTITY 2
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	ENTITY 2, THE DATA FOR THE EV7-0 IS RETURNED.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU ISSUES THE GET SYSTEM TOPOLOGY FOR ENTITY 3
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	ENTITY 3, THE DATA FOR THE EV7-1 IS RETURNED.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU ISSUES THE GET SYSTEM TOPOLOGY FOR ENTITY 4
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	ENTITY 4, THE DATA FOR THE PBM IS RETURNED.
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	THE PMU ISSUES A GET MBM IP CABLING TO THE PMU SERVER
PMU	MBM PMU SERVER DHCP SERVER	CMM	EV70	EV71	PBM	IP CABLING DATA IS RETURNED

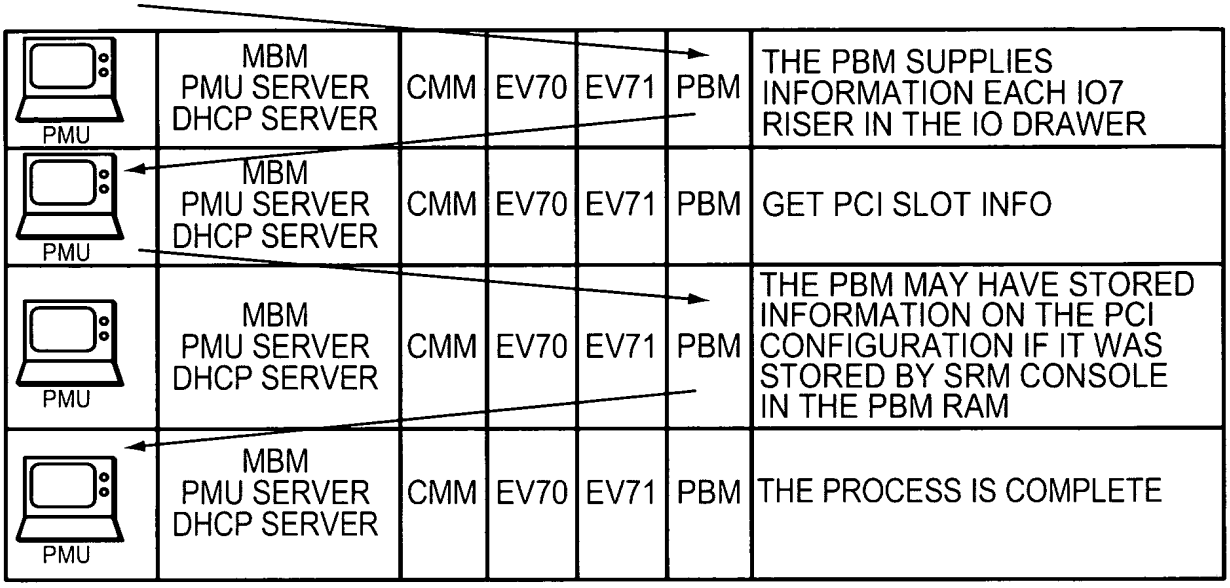
SHOW CONFIGURATION FLOW DIAGRAM (PART 2)

FIG. 28



SHOW CONFIGURATION FLOW DIAGRAM (PART 3)

FIG. 29A



SHOW CONFIGURATION FLOW DIAGRAM (PART 4)

FIG. 29B

Configuration of Marvel at: 5/22/00/ 08:30:05

MBM Rack 3-01 Part No= A1234567; Revision=32; Serial No=123456789 MFGDate040101 3 Errors

CMM 0 Part No=C1135; Revision=01; Serial No=11335577

EV70 - Partition 8/0; GTL Voltage= 1.5; 5V=4.9; Temp= 85F; Part No=E7-435; Revision=5; Serial No= 9876543; MFGDate=101100

EV71 - Partition 8/0; GTL Voltage= 1.5; 5V=5,0; Temp= 45F; Part No=E7-435; Revision=2; Serial No= 9876530; MFGDate=092100

RIMMO - 256MB; Part No=RA03-256; Revision=1;Serial No=000123456; MFGDate=070199

RIMMI - 256MB, Part No= RA03-256; Revision=1;Serial No= 000123457; MFGDate=070199

48V Power Suplies: 1-operational; 2 Missing; 3-Operational; 4-Off

FAN1= 340RPM(minimal 200):

PBM Rack 6- 02 Part No= B1234567; Revision=32; Serial No=1233456790 MFGDate 0401000 Errors

Power Supply 1 Operational;+5V = 5.0;-5V=5.1; 12V = 11.99;3.3.3V=3.1;Temp=98F; FAN1=286rpm(minimal=100); FAN2 =330rpm(minimal=100); Part No=PAA003; Revision=01;Serial No=SS2358

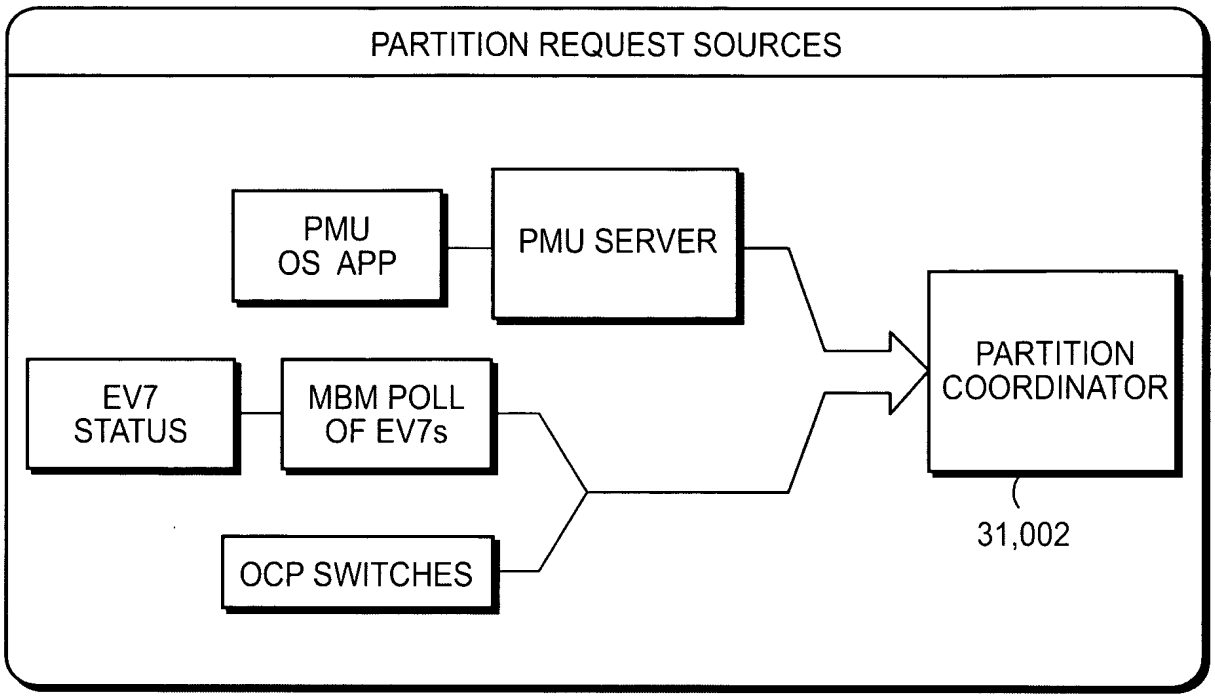
IO7Drawer - Partiotion 8; Part No=CPQ00I01; Revision=O7; Serial No=IO2345

PCI slot 3 - class=01;subclass=04;deviceid=0701;vendorid=0809;intpin=A;irq=5;

PCI slot 5 - class=02; subclass=03;deviced=0e00;vendorid=a3f2;intpin=D;irq=9

SHOW CONFIG SAMPLE OUTPUT

FIG. 30



PARTITION REQUEST SOURCES

FIG. 31

REQUESTS RECEIVED	CLASS	PARTITION STATES	HANDLING METHODS	REFERENCE FLOW
MOVE EV7s TO PARTITION REMOVE EV7s FROM PARTITION	COMPLEX	NOT RUNNING OS	CHECK THE PROPER CABLING OF A NEWLY FORMED HARD PARTITION OR ADDITIONS TO AN EXISTING PARTITION. DETERMINE MEMORY AND ROUTABILITY. DISTRIBUTE THE REQUESTS ON THE TRAIN TO UPDATE VOLATILE DATABASE.	
ASSIGN MEMORY OR IO TO SUB PARTITION	TRAINED			
SET PARTITION STATE ATTRIBUTES	TRAINED			
SWITCH PRIMARY EV7	COMPLEX	PARTITION RUNNING OS	DISTRIBUTE THE REQUESTS ON THE TRAIN TO UPDATE VOLATILE DATABASE.	
ASSIGN SUB PARTITION	TRAINED			
ASSIGN MEMORY OR IO TO SUB-PARTITION	TRAINED			
ADD EV7 OR DELETE EV7 TO/FROM RUNNING PARTITION	COMPLEX	PARTITION RUNNING OS	CABLE TESTING IS PERFORMED TO DETERMINE PROPER CONNECTIONS; A QUIESCE REQUEST IS MADE TO ALL EV7s IN THE PARTITION; NEW RCONFIG/CCONFIG REQUESTS ARE SENT TO ALL EV7s AND IF ACCEPTABLE CONTINUE IS SENT TO THE EV7. IF AN ERROR IS FOUND IN ROUTING NEW RCONFIG/CCONFIG COMPUTATIONS ARE ATTEMPTED AND SENT TO THE AFFECTED EV7s.	FIGURE 16
SAVE PARTITION ASSIGNMENT	TRAINED	DON'T CARE	SEND THE SAVE COMMAND ON THE TRAIN SO THAT ALL COPIES OF VOLATILE DATABASE FOR PARTITION CAN BE COPIED TO NON-VALATILE	FIGURE 9
RESET PARTITION	COMPLEX	PARTITION RESET IN PROGRESS OR PARTITION RUNNING OS	PULSE RESET ON ALL EV7s IN PARTITION, RELOAD SROM, XSROM ON ALL EV7, RECONFIGURE ROUTING, SEND RCONFIG/CCONFIG TO ALL EV7s AND IF NO ERRORS, LOAD SRM TO PRIMARY.	FIGURE 11
		PARTITION POWERED OFF	RETURN ERROR	

PARTITION COORDINATOR HANDLING OF REQUESTS (PART 1)

FIG. 32A

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REQUESTS RECEIVED	CLASS	PARTITION STATES	HANDLING METHODS	REFERENCE FLOW
POWER OFF PARTITION	COMPLEX		IF DUAL EV7 IN PARTITION OR EV7 IN FREE POOL, SEND POWER OFF EV7s TO EACH SUCH CMM IN PARTITION. POWER OFF PCI DRAWER ON PBMs THAT HAVE ALL IO7 RISERS IN PARTITION. IF POWER IS OFF FOR ALL CMMs IN A CABINET OF MBMs, POWER OFF THE POWER SUPPLIES IN THE CABINET.	
		PARTITION POWERED OFF OR REQUEST MADE TO SUB PARTITION	RETURN ERROR	
POWER ON PARTITION	COMPLEX	PARTITION RESET IN PROGRESS OR PARTITION RUNNING OS	RETURN ERROR	
		PARTITION POWERED OFF	TURN POWER ON TO ANY POWER SUPPLIES THAT ARE OFF AND UNDER CONTROL OF CABINETS THAT HAVE EV7s ASSIGNED TO OUR PARTITION. POWER UP PCI DRAWERS FOR PBMs THAT HAVE IO7 IN OUR PARTITION. POWER UP DUAL EV7s ON CMMs THAT HAVE EV7s IN OUR PARTITION. CONTINUE LIKE THE RESET PROCESS.	
HALT PARTITION	FORWARDED	PARTITION RESET IN PROGRESS OR PARTITION RUNNING OS	SEND HALT ON TO PRIMARY EV7 OF PARTITION.	
		PARTITION IN HALT STATE OR PARTITION POWER OFF	RETURN ERROR	
DISABLE HALT PARTITION	FORWARDED	PARTITION RESET IN PROGRESS OR PARTITION RUNNING OS	RETURN ERROR	
		PARTITION IN HALT STATE OR PARTITION POWER OFF	SEND HALT OFF TO PRIMARY EV7 OF PARTITION.	
STORE ENVI-RONMENT VARIABLES	TRAINED	PARTITION RUNNING OS	THIS CAUSES A DISTRIBUTION OF THE SRM ENVIRONMENT VARIABLES TO ALL PEERS	FIGURE 34
GET ENVI-RONMENT VARIABLES	DIRECT	PARTITION RUNNING OS	RETURN LOCAL COPY OF ALL SRM ENVIRONMENT VARIABLES	

PARTITION COORDINATOR HANDLING OF REQUESTS (PART 2)

FIG. 32B

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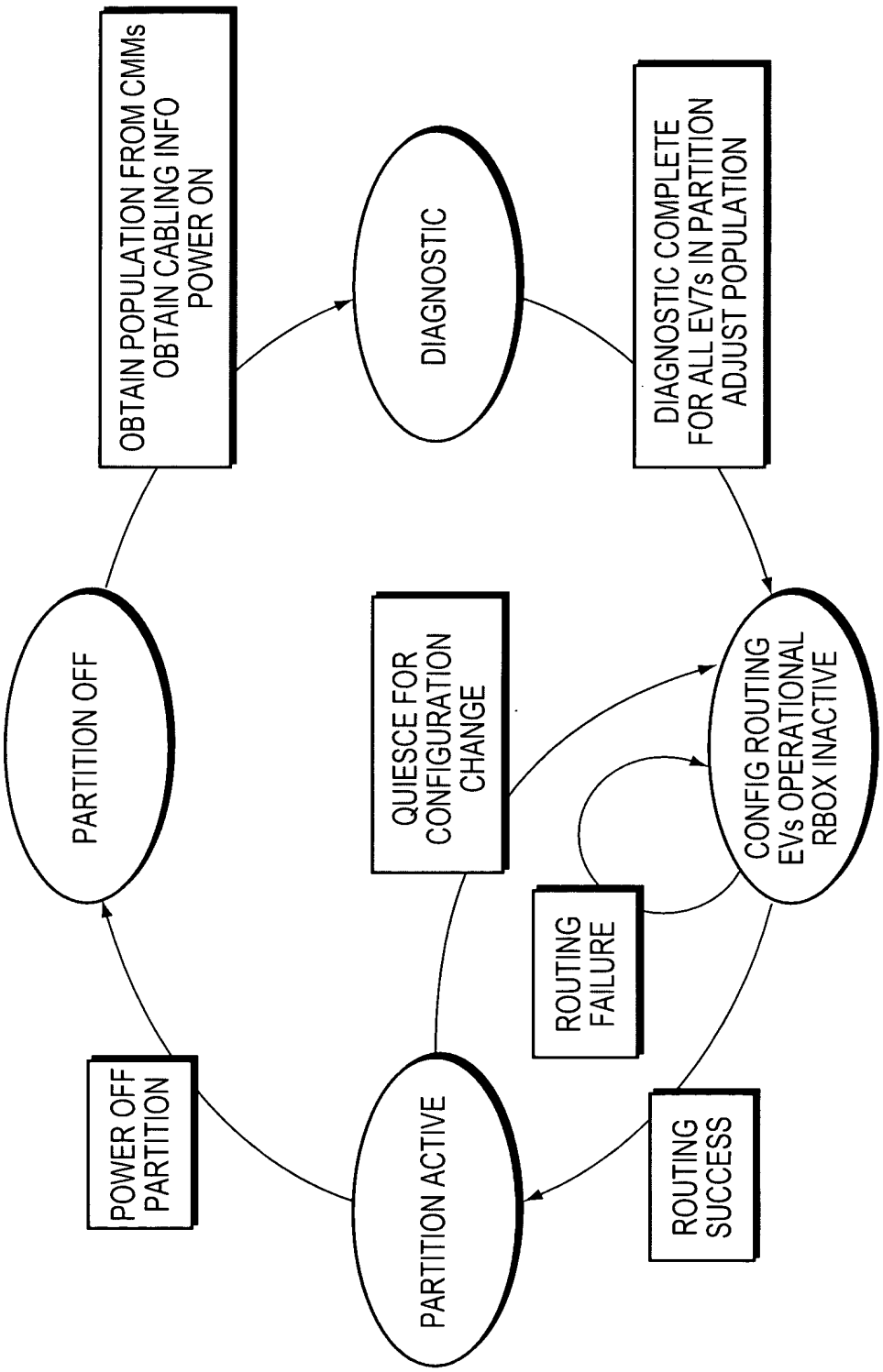
COMMANDS ISSUED	PARTITION STATES	HANDLING METHODS	REFERENCE FLOW
TRAIN FULL	ANY		
EV7 QUIESCE	PARTITION RESET IN PROGRESS	CMM FACILITATES	FIGURE 17, FIGURE 20
CONFIG RBOX/CBOX	PARTITION RESET IN PROGRESS	CMM FACILITATES	FIGURE 17, FIGURE 20
EV7 RESET	ANY	CMM FACILITATES	FIGURE 15
EV7 START TEST	PARTITION RESET IN PROGRESS	CMM FACILITATES	FIGURE 16, FIGURE 18
EV7 HALT	ANY	CMM FACILITATES	
LOAD IMAGE	PARTITION RESET IN PROGRESS	CMM FACILITATES	FIGURE 15, FIGURE 20
SET PARTITION STATE	PARTITION RESET IN PROGRESS	CMM FACILITATES	FIGURE 15, FIGURE 16, FIGURE 17, FIGURE 18, FIGURE 20, FIGURE 21
POWER ON/OFF	ANY	CMM, MBM, AND PBM FACILITATES	

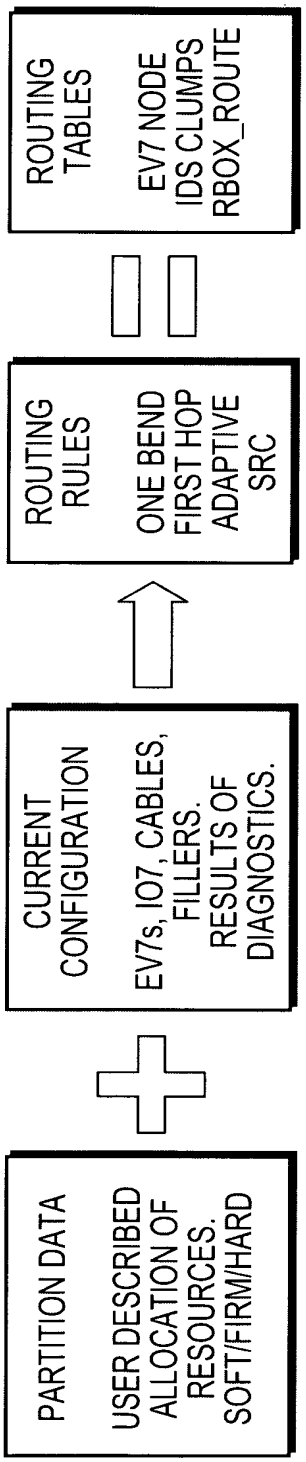
PARTITION COORDINATOR COMMANDS ISSUED

FIG. 33



PARTITION STATE DIAGRAM

FIG. 34



INPUTS AND OUTPUTS OF ROUTER ALGORITHM

FIG. 35

ROUTING GLOSSARY

PRIMARY DIMENSION: ONE OF THE EAST-WEST OR NORTH-SOUTH. THIS CHOICE IS THE SAME FOR ALL EV7s.

SECONDARY DIMENSION: THE OTHER WAY.

DIMENSION-ORDER ROUTING: THE SHORTEST PATH CONNECTING TWO NODES WHICH PROCEEDS FIRST ALONG THE PRIMARY DIMENSION AND THEN ALONG THE SECONDARY.

ADAPTIVE ROUTING: THE COLLECTION OF PATHS ADVANCING NODE-TO-NODE IN THE SAME PRIMARY AND SECONDARY DIRECTIONS AS THE DIMENSION-ORDER ROUTING. AT EACH INTERMEDIATE NODE IT MUST BE POSSIBLE TO ADVANCE IN EITHER DIRECTION UNTIL THE DIMENSION COORDINATE IN A DIRECTION MATCHES THAT OF THE DESTINATION.

INITIAL HOP: A ROUTING OPTION WHICH ALLOWS A HOP FROM THE SOURCE NODE IN ANY DIRECTION TO AN ADJACENT NODE. THIS OPTION ALLOWS SOME CONNECTION OF NODES IN IMPERFECT MESHES.

SRC ROUTING: ANOTHER DEADLOCK-FREE ROUTING METHOD IN WHICH TRAVEL PROCEEDS FIRST ALONG THE SECONDARY DIMENSION.

ROUTING GLOSSARY

FIG. 36

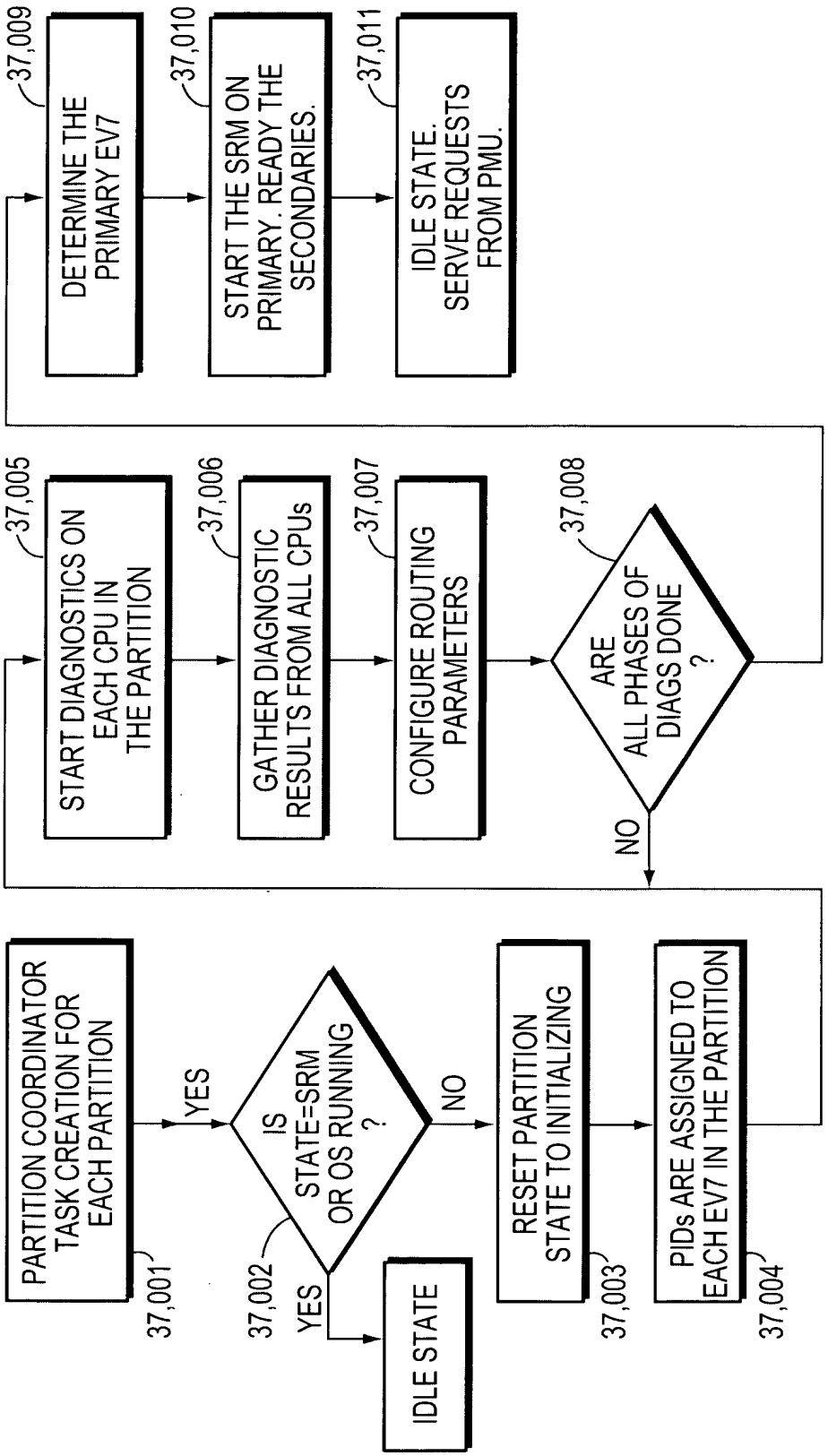
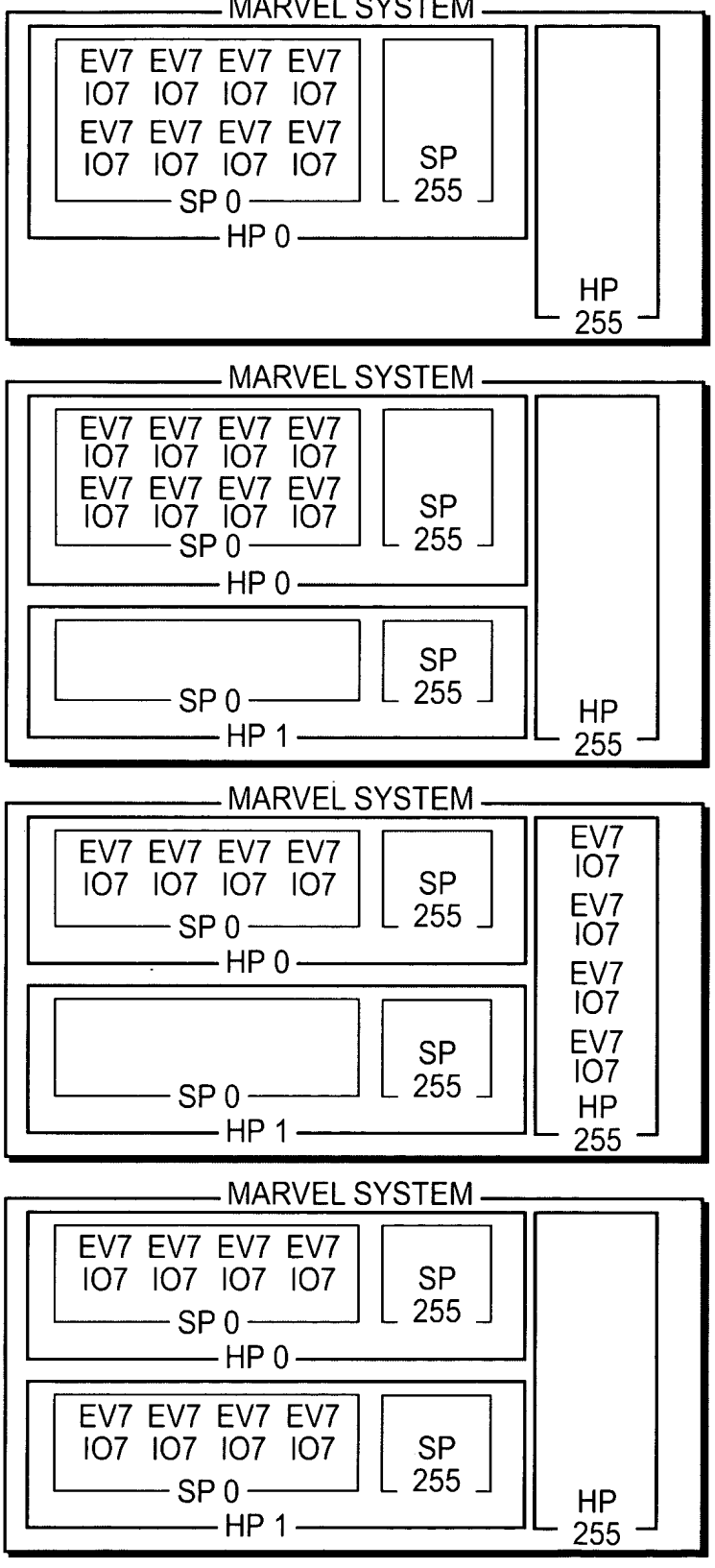
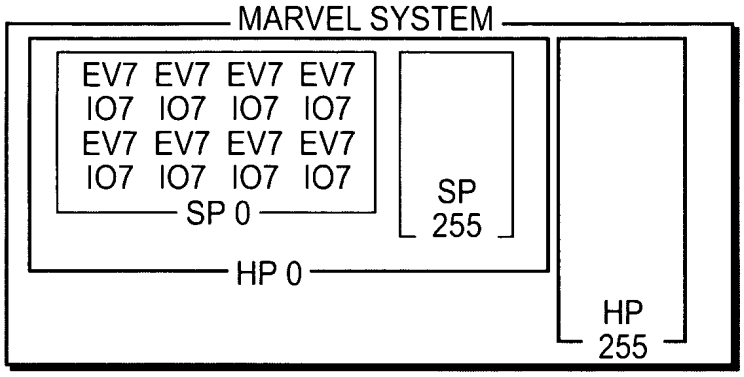

PARTITION COORDINATOR TASK CREATION FOR EACH PARTITION
37,001
IS STATE=SRM OR OS RUNNING ?
37,002
YES
NO
IDLE STATE
RESET PARTITION STATE TO INITIALIZING
37,003
PIDs ARE ASSIGNED TO EACH EV7 IN THE PARTITION
37,004
ARE ALL PHASES OF DIAGS DONE ?
37,008
NO
START DIAGNOSTICS ON EACH CPU IN THE PARTITION
37,005
GATHER DIAGNOSTIC RESULTS FROM ALL CPUs
37,006
CONFIGURE ROUTING PARAMETERS
37,007
DETERMINE THE PRIMARY EV7
37,009
START THE SRM ON PRIMARY. READY THE SECONDARIES.
37,010
IDLE STATE. SERVE REQUESTS FROM PMU.
37,011
PARTITION COORDINATION

FIG. 37

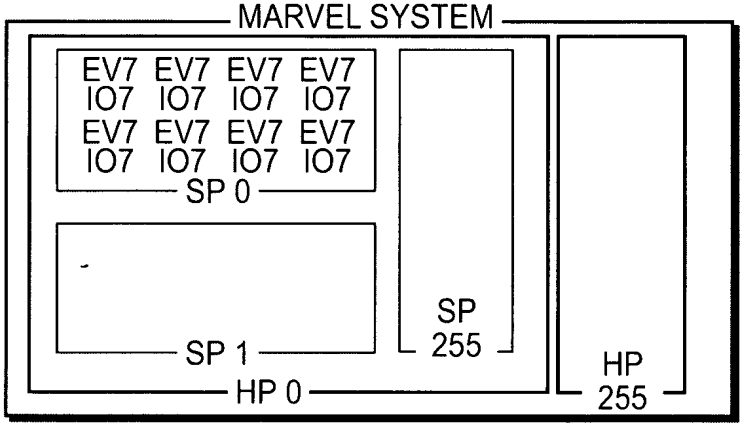


CREATING A HARD PARTITION BLOCK FLOW

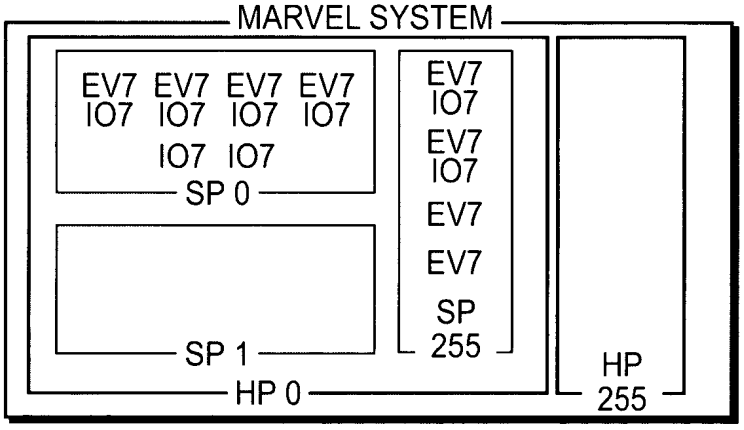
FIG. 38



THE INITIAL SYSTEM DEFAULTS ALL THE RESOURCES INTO ONE OPERATION.



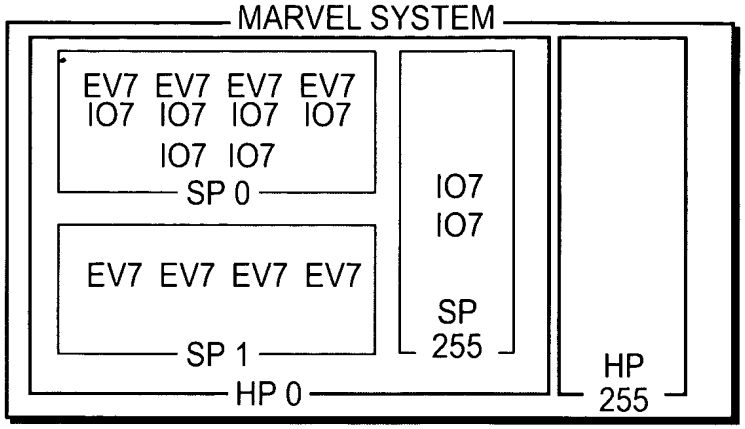
THE USER CREATED A 2nd SUBPARTITION (SP 1) WITHIN THE EXISTING HARD PARTITION (HP 0).



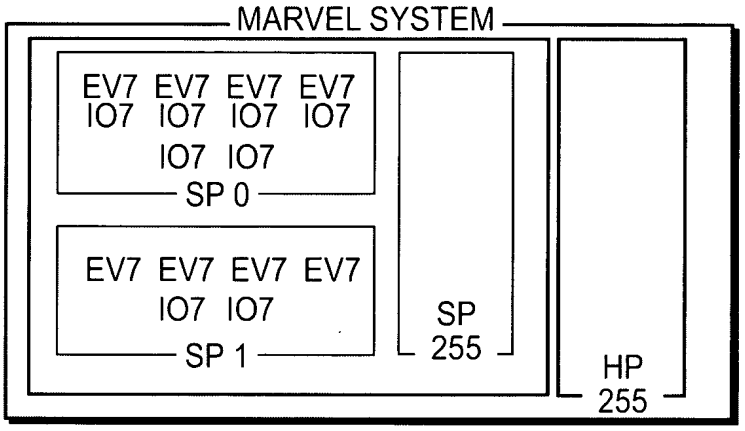
THE USER DELETES EV7s FROM SP 0, MIGRATING THEM INTO THE PARTITION FREE POOL (SP 255). UNLIKE MOVING THEM OUT OF THE HARD PARTITION, THE IO7s DO NOT STICK TO THEIR EV7s. THE USER ISSUES SEPARATE DELETE IO7 COMMANDS, AND IN THIS EXAMPLE, ONLY MIGRATES 2 IO7s.

CREATING A SUBPARTITION BLOCK FLOW (PART 1)

FIG. 39A



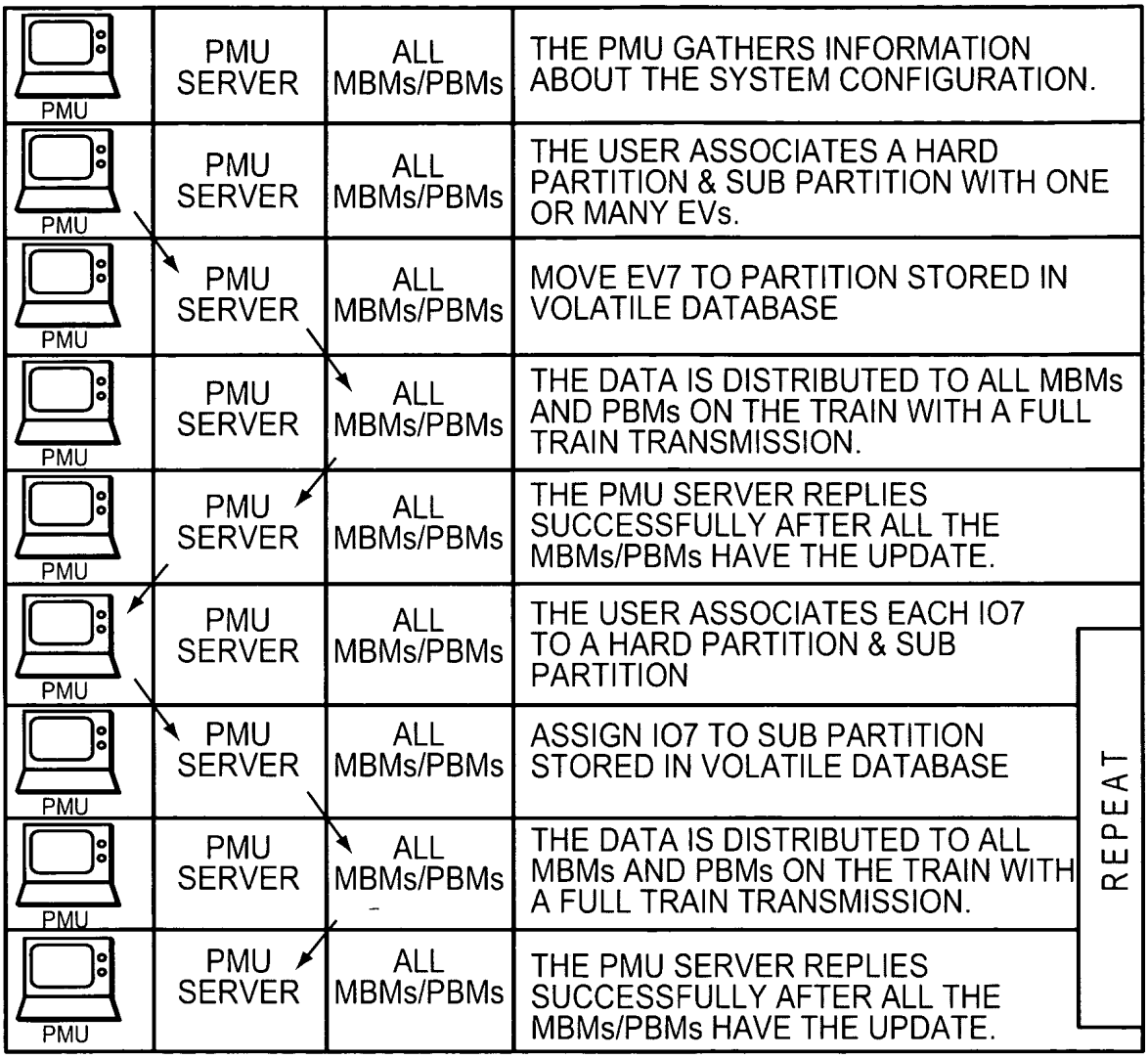
IN CONFIGURATION THE NEW SUBPARTITION (SP 1), EV7s ARE MOVED INTO THE PARTITION. AGAIN NOTE THE IO7s ARE ACTED UPON INDEPENDENT OF THE EV7.



TO GET THE IO7s INTO THE NEW SUBPARTITION, AN ASSIGN COMMAND IS USED.

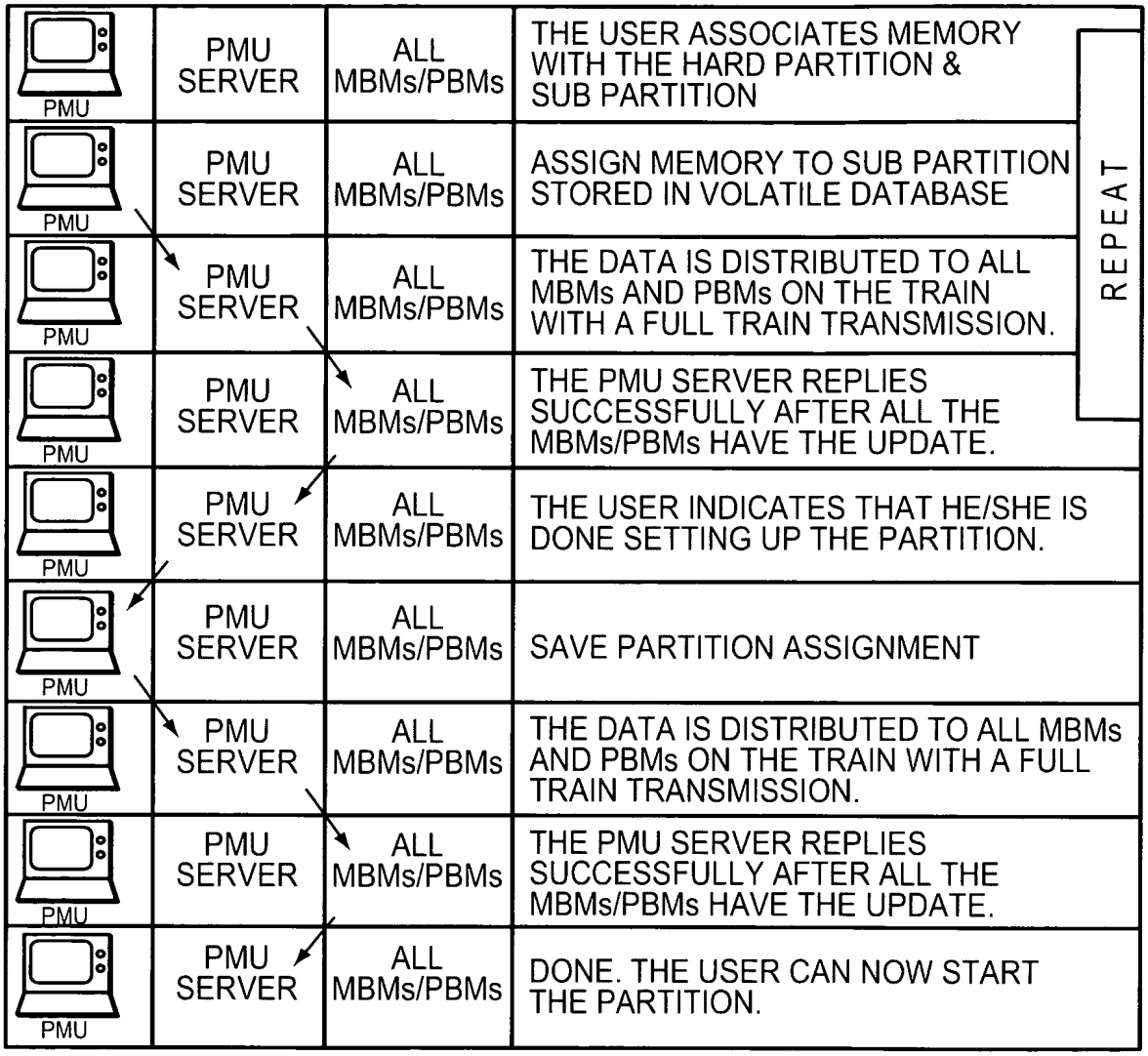
CREATING A SUBPARTITION BLOCK FLOW (PART 2)

FIG. 39B



CREATING A NEW PARTITION FLOW DIAGRAM (PART 1 OF 2)

FIG. 40



CREATING A NEW PARTITION FLOW DIAGRAM (PART 2 OF 2)

FIG. 41

RESET STATE

PMU	PMU SERVER	GROUP LEADER	MBM	CMMs	EV7s	AFTER A GROUP FORMATION, THE LEADER RECONCILES THE PARTITION DATABASE AND STARTS UP A PARTITION COORDINATOR FOR EACH PARTITION. THE PC IS STARTED ON THE LOWEST MBM ID IN THE PARTITION.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	THE PC GATHERS INFORMATION ABOUT THE CURRENT STATE OF THE PARTITION. EACH MBM IN THE PARTITION IS QUERIED FOR STATE OF THE CMMs WITH THE GET MBM CONFIGURATION REQUEST.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	EACH CMM IN THE PARTITION IS QUERIED WITH THE GET CMM STATE COMMAND.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	EACH CMM RETURNS THE STATUS OF THE EV7s IT CONTROLS.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	THE MBM ASSEMBLES THE DATA FROM ALL THE CMMs INTO THE REPLY TO THE GET MBM CONFIGURATION.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	EACH PBM IN THE PARTITION IS QUERIED FOR STATE OF THE CMMs WITH THE GET PBM CONFIGURATION REQUEST.
PMU	PMU SERVER	PART COORD	MBMs	CMMs	EV7s	THE PBMs REPLY WITH THE IO7 CONFIGURATION.

PARTITION START FLOW DIAGRAM (RESET STATE) (PART 1)

FIG. 42A

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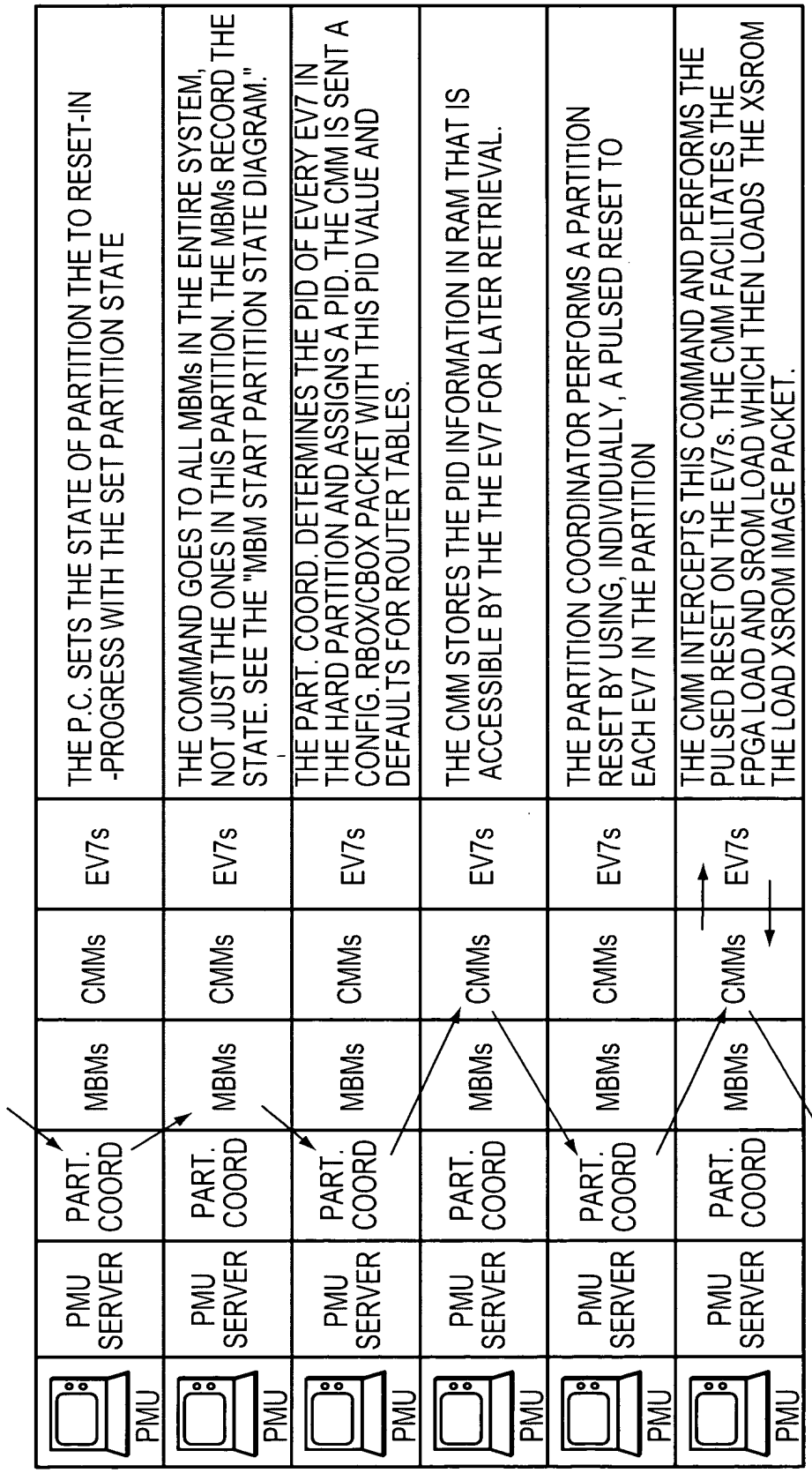
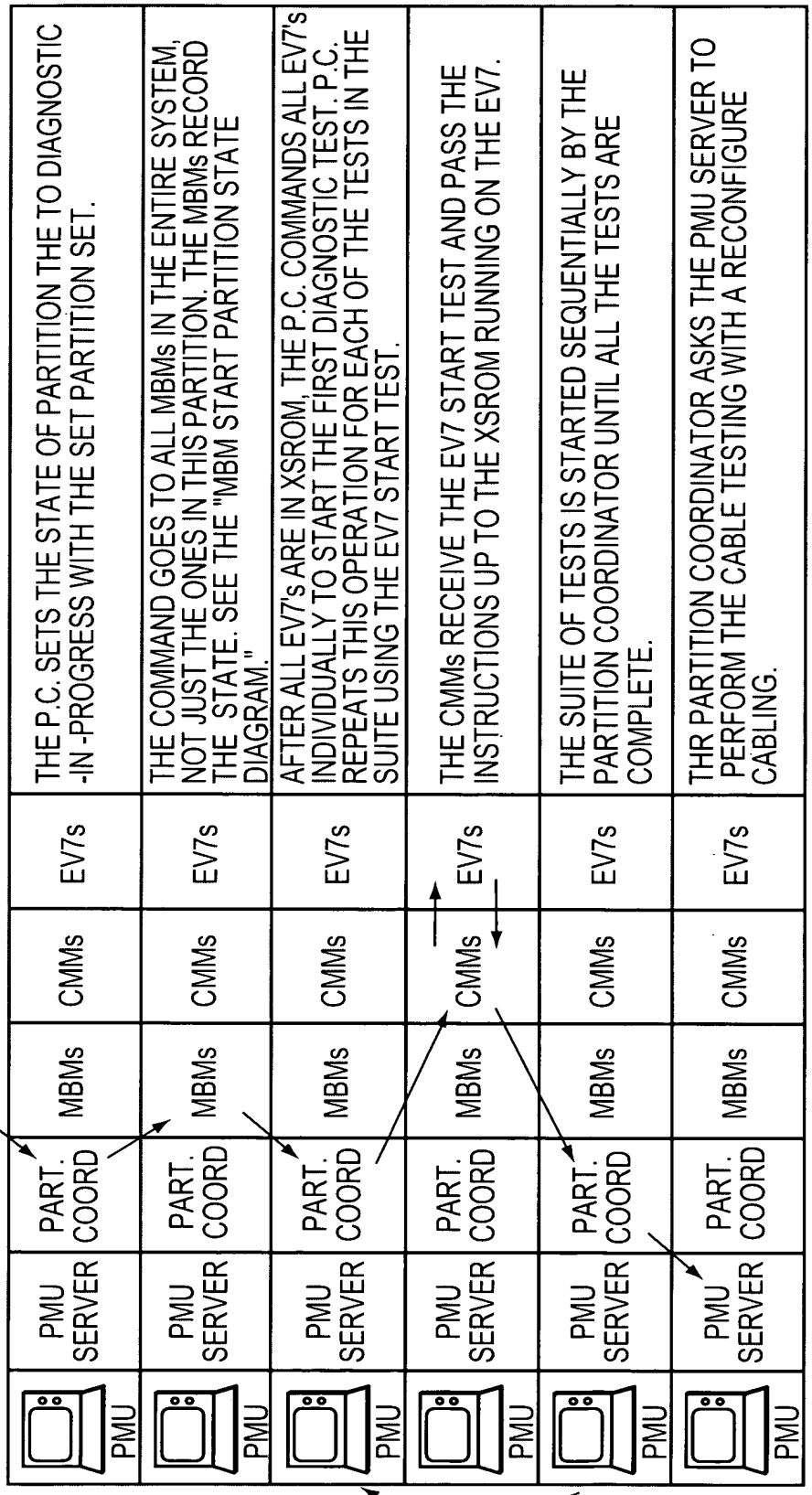


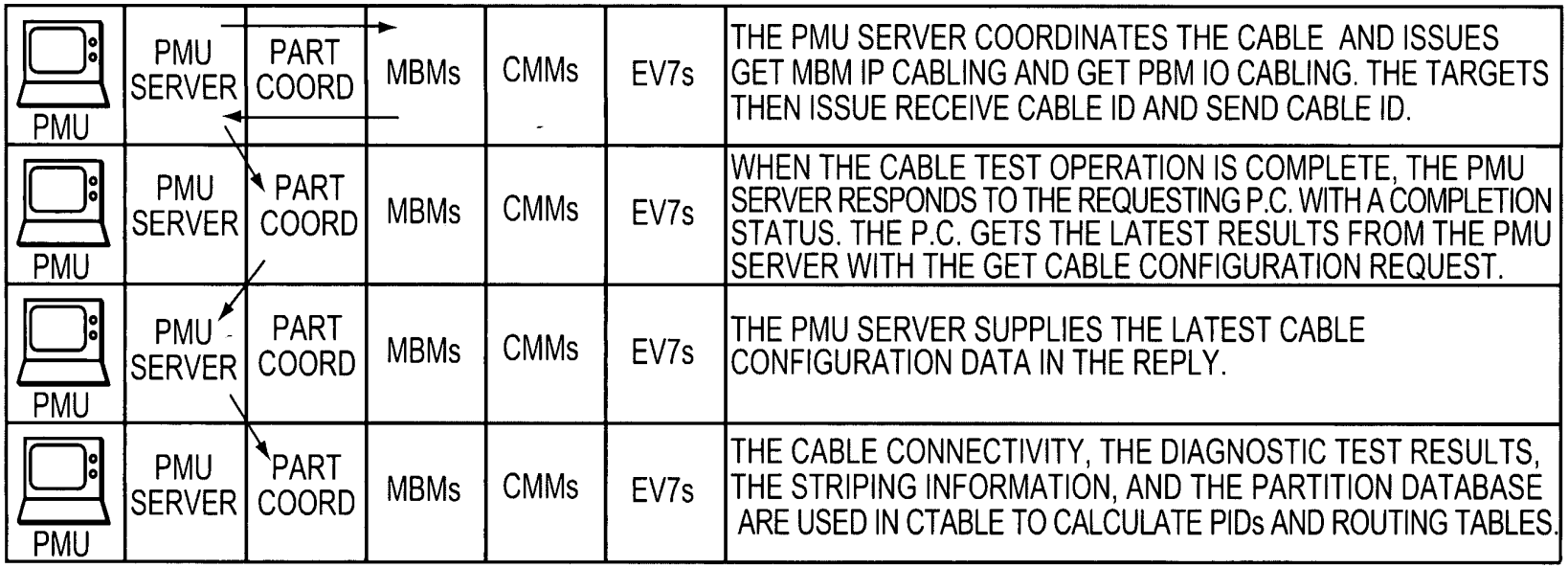
PARTITION START FLOW DIAGRAM (RESET STATE) (PART 2)

FIG. 42B



PARTITION START FLOW DIAGRAM, DIAGNOSTIC STATE (PART 1)

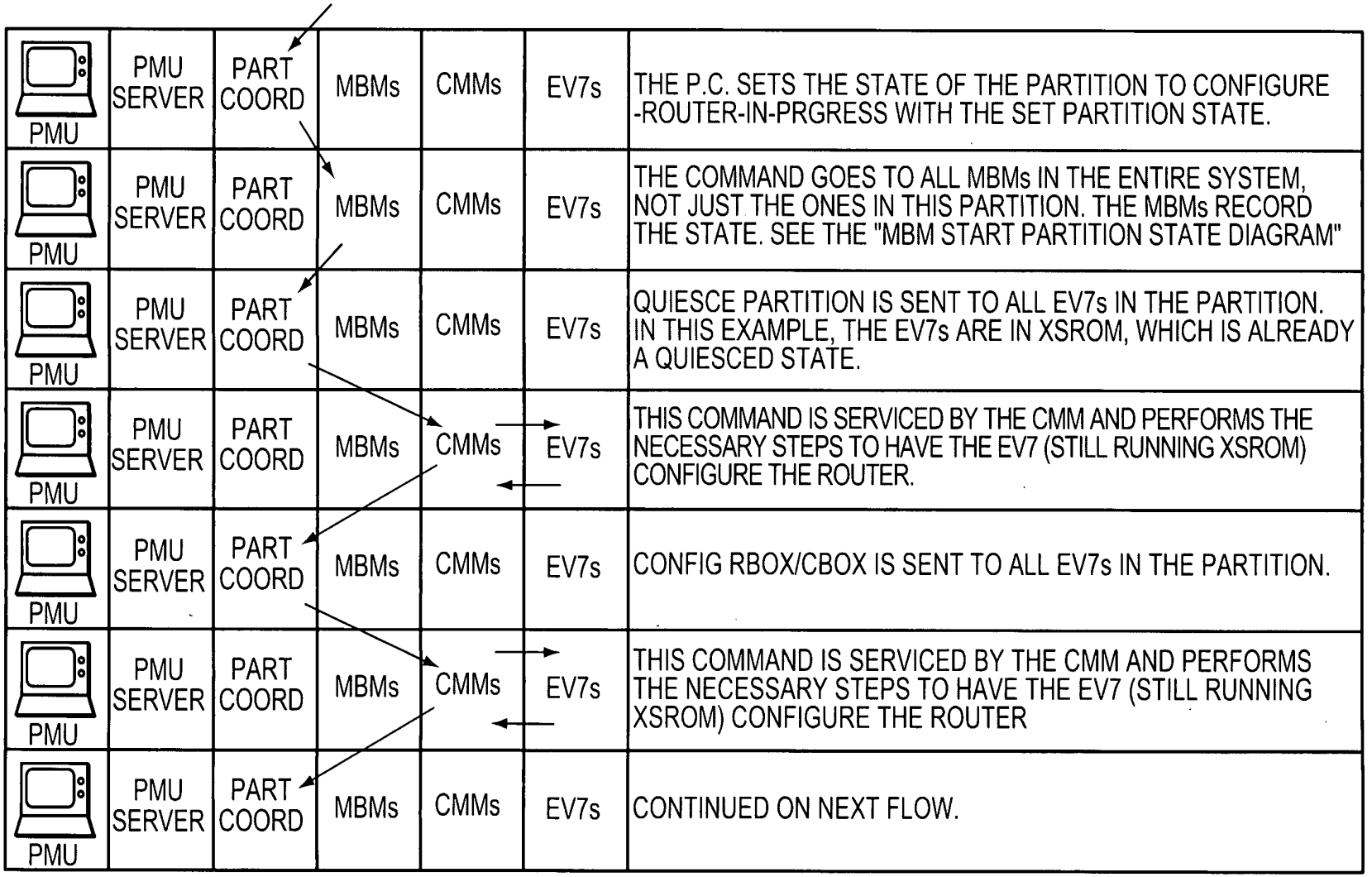
FIG. 43A



PARTITION START FLOW DIAGRAM, DIAGNOSTIC STATE (PART 2)

FIG. 43B

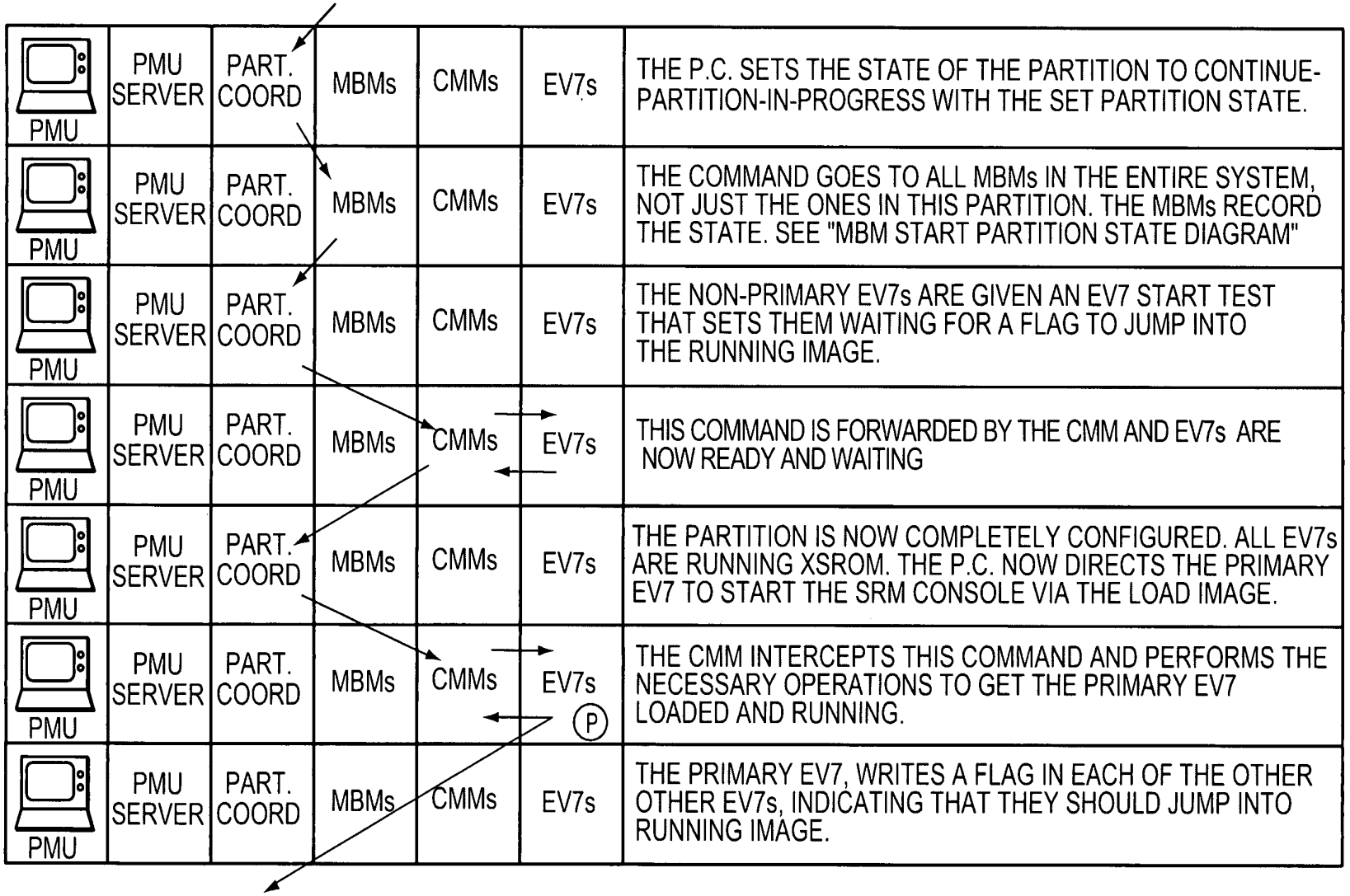
CONFIGURE ROUTER



PARTITION START FLOW DIAGRAM (CONFIGURE ROUTE)

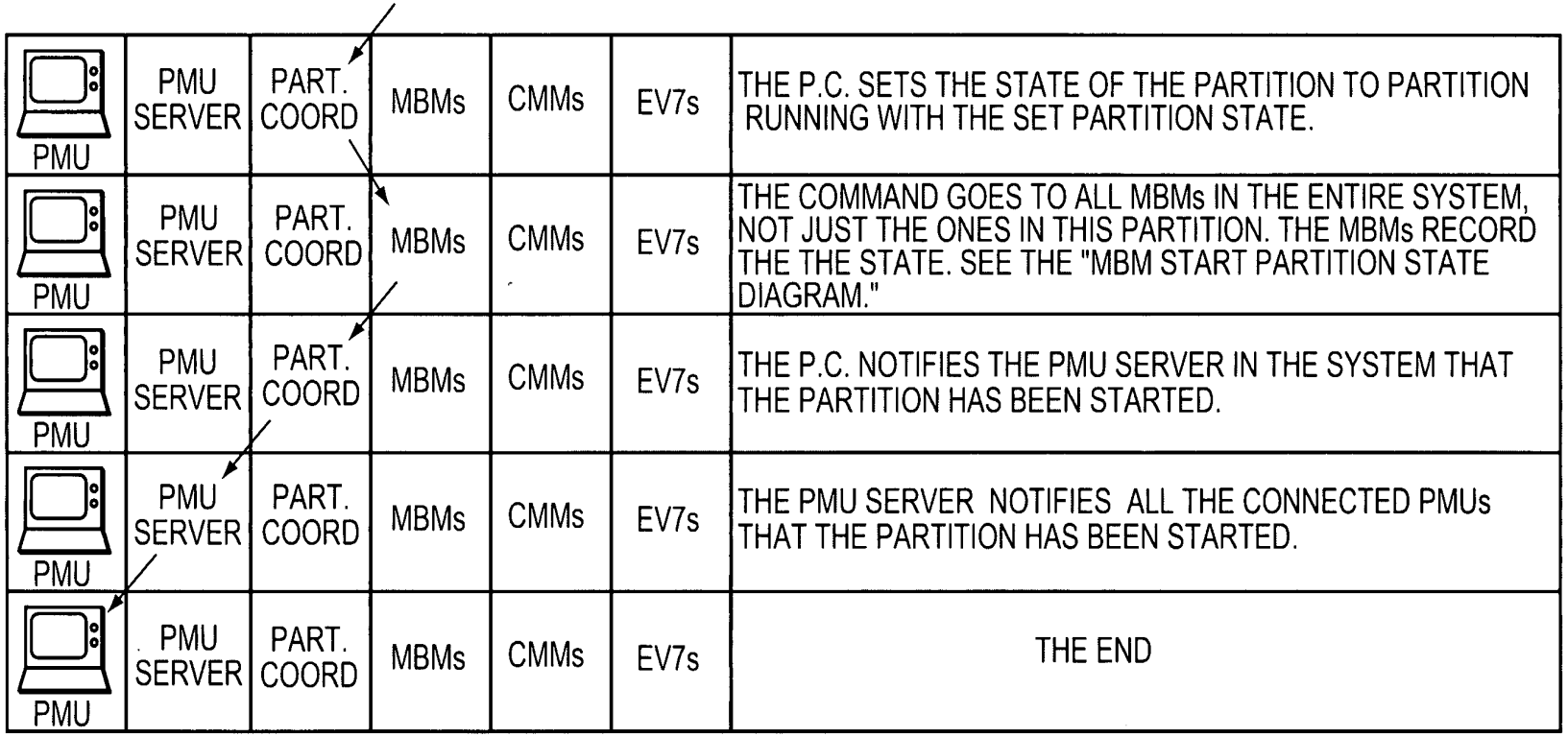
FIG. 44

CONTINUE PARTITION AND PARTITION RUNNING



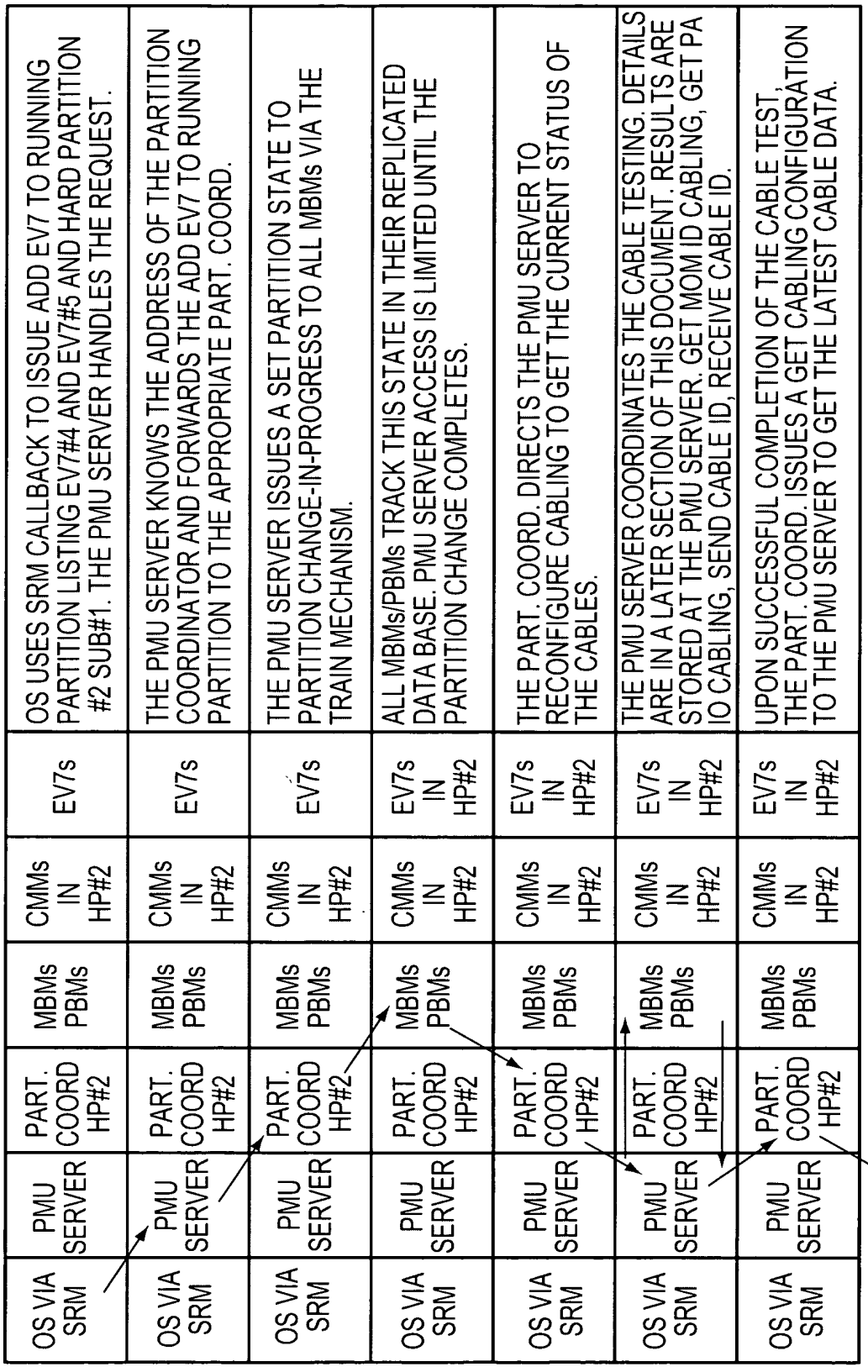
PARTITION START FLOW DIAGRAM (RUNNING) (PART 1)

FIG. 45A



PARTITION START FLOW DIAGRAM (RUNNING) (PART 2)

FIG. 45B



ADD EV7 FLOW DIAGRAM (PART 1)

FIG. 46A

OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	THE PMU SERVER RESPONDS WITH THE CABLE CONNECTIVITY DATA BASE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	THE PART. COORD VALIDATES THE CABLE CONNECTIVITY. IF OKAY ADD EV7 TO RUNNING PARTITION IS DISTRIBUTED TO ALL MBMs/PBMs ON THE LAN VIA THE TRAIN MECHANISM.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	ALL MBMs/PBMs MODIFY THEIR REPLICATED DATA BASE TO TRACK THIS CHANGE TO THE FREE POOL AND TO PARTITION#2
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7#4 EV7#5	PULSE EV7 RESET IS DIRECTED AT EV7#4 AND EV7#5 (COMMAND ISSENT TO THE CMM)
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7#4 EV7#5	EV7#4 AND EV7#5 ARE RESET. THE CMM FACILIATES THE SROM LOAD. THE XSROM IS STARTED WITH THE LOAD IMAGE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	CONTINUES ON NEXT FIGURE

ADD EV7 FLOW DIAGRAM (PART 2)

FIG. 46B

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OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	THE PART. COORD. CALCULATES THE ROUTABILITY OF THE PROPOSED NEW CONFIGURATION. IF THERE IS A ROUTING ERROR THE COMMAND FINISHES WITH ERROR. WHEN ROUTING IS SUCCESSFUL, A SET PARTITION STATE TO CONFIG-ROUTE-IN-PROGRESS.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	ALL MBMs RECEIVE THE SET PARTITION STATE VIA THE TRAIN MECHANISM AND SET THEIR DATA BASES TO INDICATE THAT HP#2 IS THE CONFIG-ROUTE-IN-PROGRESS STATE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	WHEN THE TRAIN COMPLETES, THE PART. COORD. COMMANDS EACH EV7 IN HARD PARTITION #2, INDIVIDUALLY, TO QUIESCE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	QUIESCE IS RECEIVED AT THE CMMs IN THE PARTITION AND THEY TAKE ACTIONS TO QUIESCE THE EV7. EV#4 AND EV#5 ARE IN XSROM AND DO NOT HAVE TO BE QUIESCED.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	CONFIG RBOX/CBOX IS SENT TO ALL EV7s IN THE PARTITION, WHICH INCLUDES EV#4 AND EV#5 WHICH ARE IN XSROM.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	CONFIG RBOX/CBOX IS RECEIVED AT THE CMMs AND THEY DIRECT THE EV7 TO CONFIG THE RBOX AND THE CBOX.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	SET PARTITION STATE TO CONTINUE-PARTITION-IN-PROGRESS. THIS IS DISTRIBUTED VIA THE TRAIN MECHANISM.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CMMs IN HP#2	EV7s IN HP#2	ALL MBMs/PBMs TRACK THE STATE OF THE PARTITION IN THEIR REPLICATED DATA BASE.

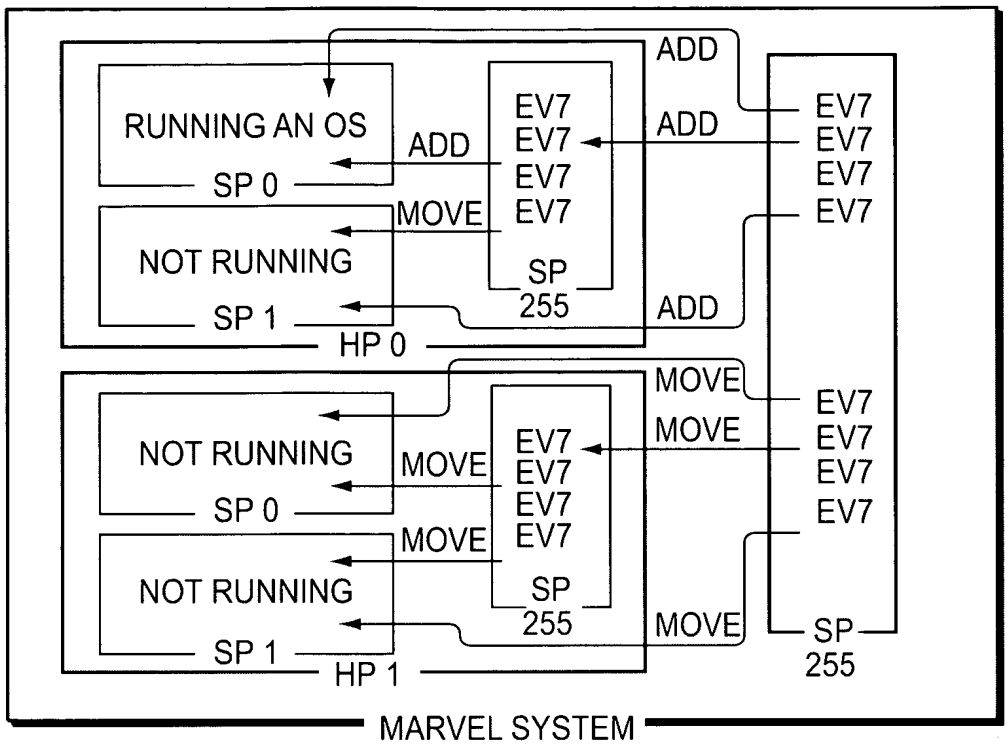
ADD EV7 FLOW DIAGRAM (PART 3)

FIG. 46C

OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	THE PMU SERVER RESPONDS WITH THE CABLE CONNECTIVITY DATA BASE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	ALL MBMs/PBMs TRACK THE STATE OF THE PARTITION IN THEIR REPLICATED DATA BASE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	SET PARTITION STATE TO PARTITION RUNNING IS ISSUED ON THE TRAIN.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	ALL MBMs/PBMs TRACK THE STATE OF THE PARTITION IN THEIR REPLICATED DATA BASE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	THE PART. COORD. NOTIFIES THE PMU SERVER THAT THE ADD EV7 TO PARTITION IS DONE.
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	THE PMU SERVER RESPONDS TO THE ORIGINAL ADD EV7 TO PARTITION .
OS VIA SRM	PMU SERVER	PART. COORD HP#2	MBMs PBMs	CCMs IN HP#2	EV7s IN HP#2	THE END

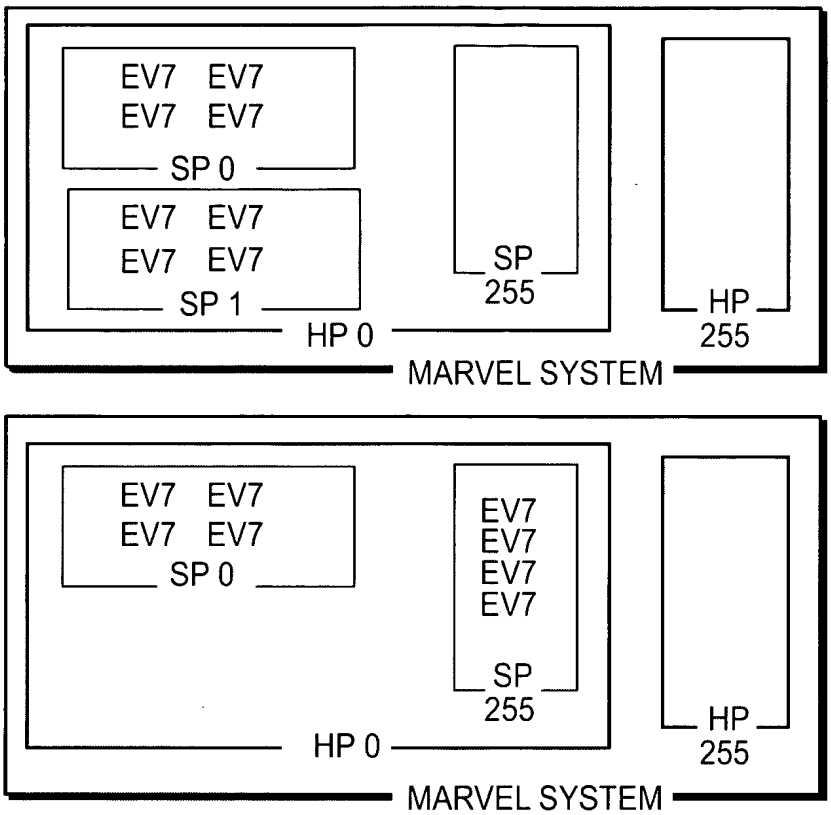
ADD EV7 FLOW DIAGRAM (PART 4)

FIG. 46D



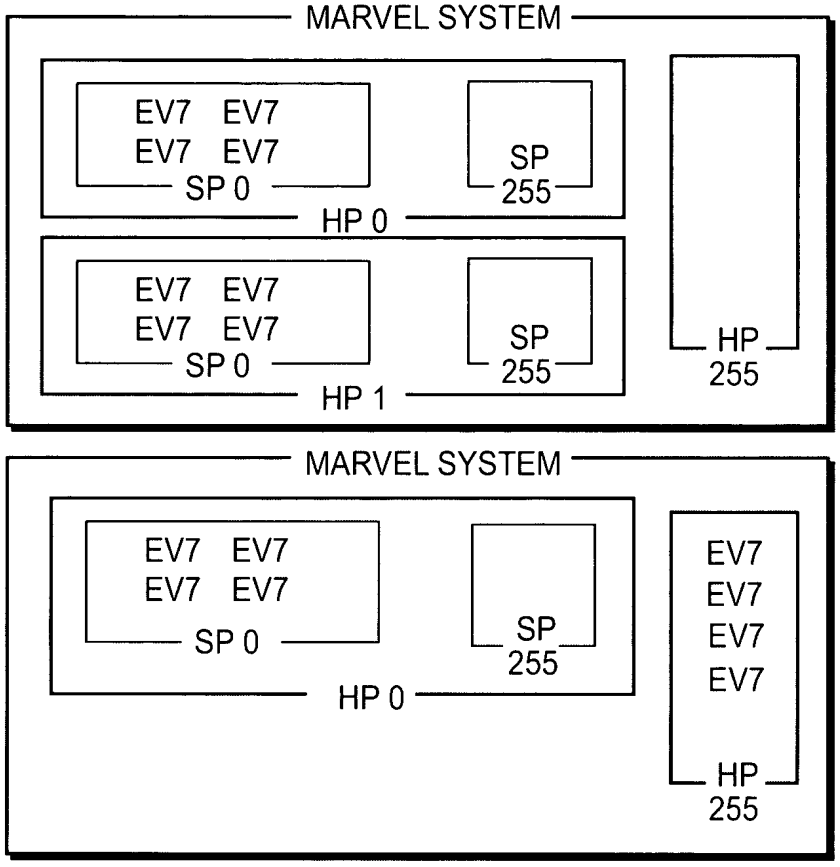
ADD VS MOVE

FIG. 47



DESTROYING A SOFT PARTITION

FIG. 48

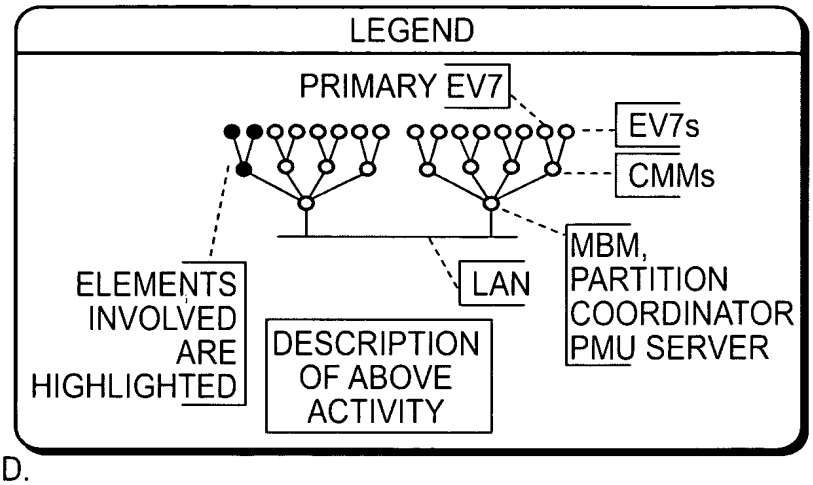


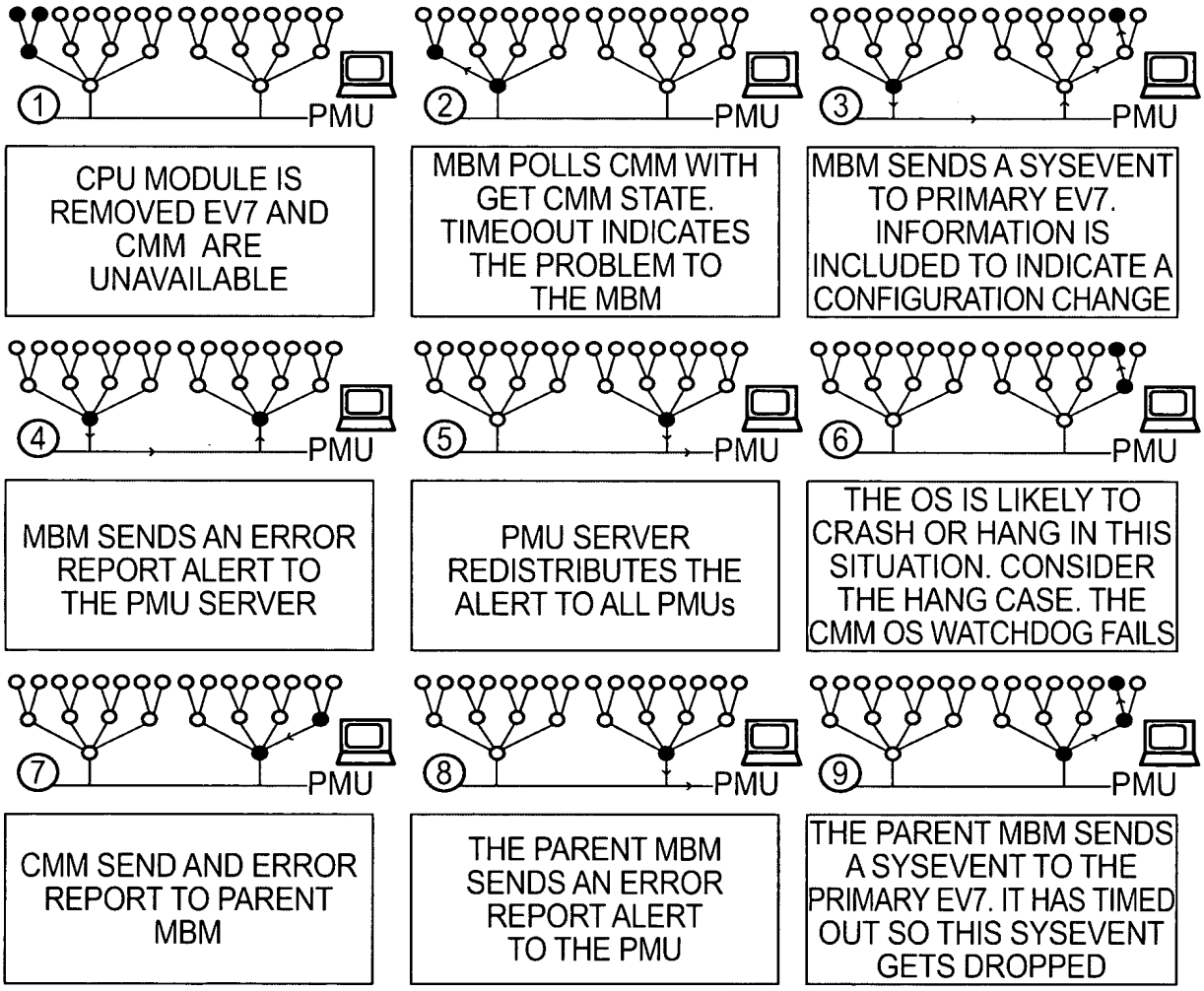
DESTROYING A HARD PARTITION

FIG. 49

THIS FLOW IS AN EXAMPLE OF SM PROTOCOL ACTIVITY WHEN A CPU MODULE FAILS. THE ENTIRE CONFIGURATION IS ONE PARTITION, AND THE FAILING CPU MODULE IS NOT THE PRIMARY.

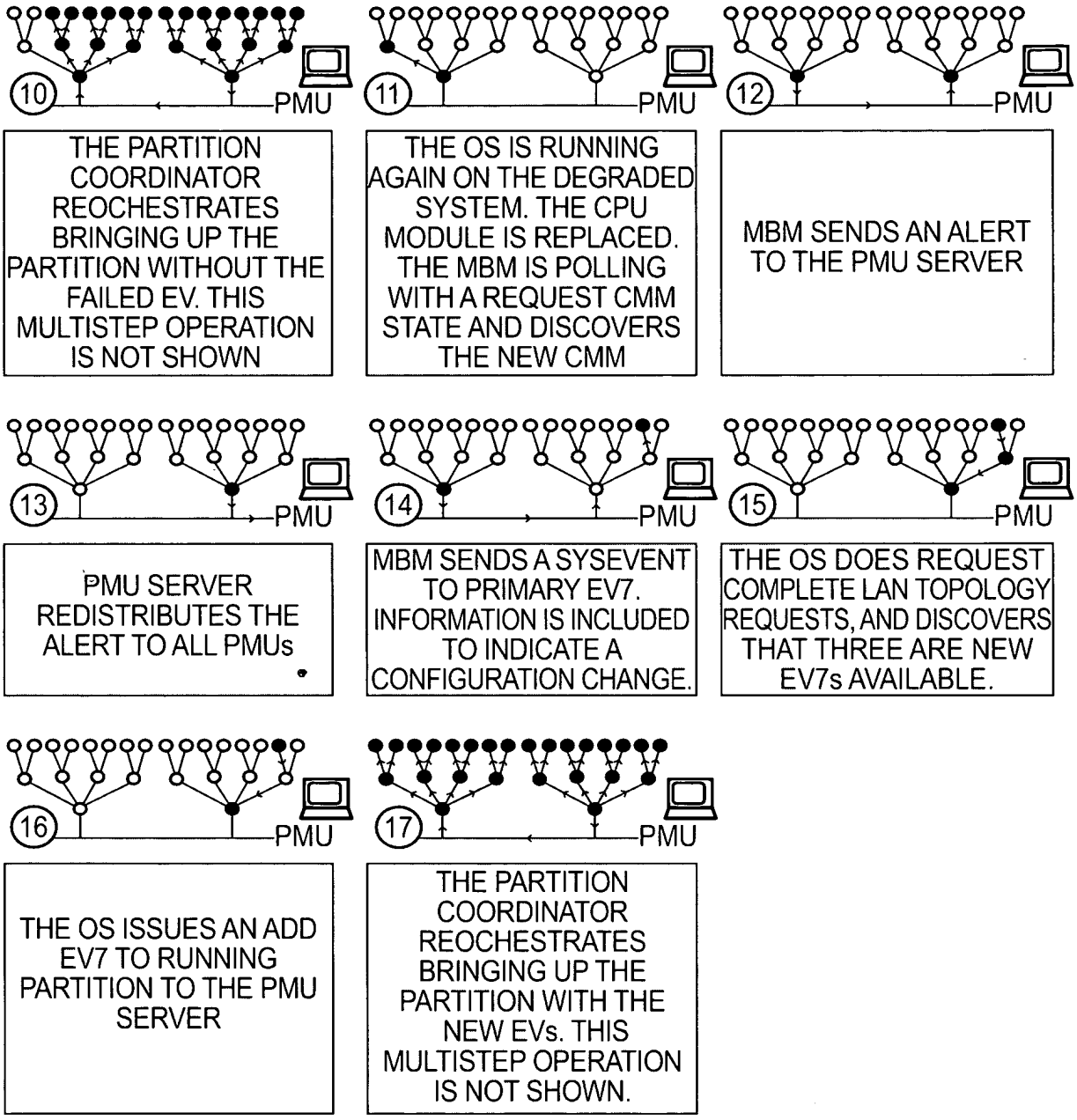
THE OPERATING SYSTEM DOES CRASH, AND IS RESTARTED. THE FAILED CPU MODULE IS REPLACED AND THE ORIGINAL COMPLETE CONFIGURATION IS RESTORED.





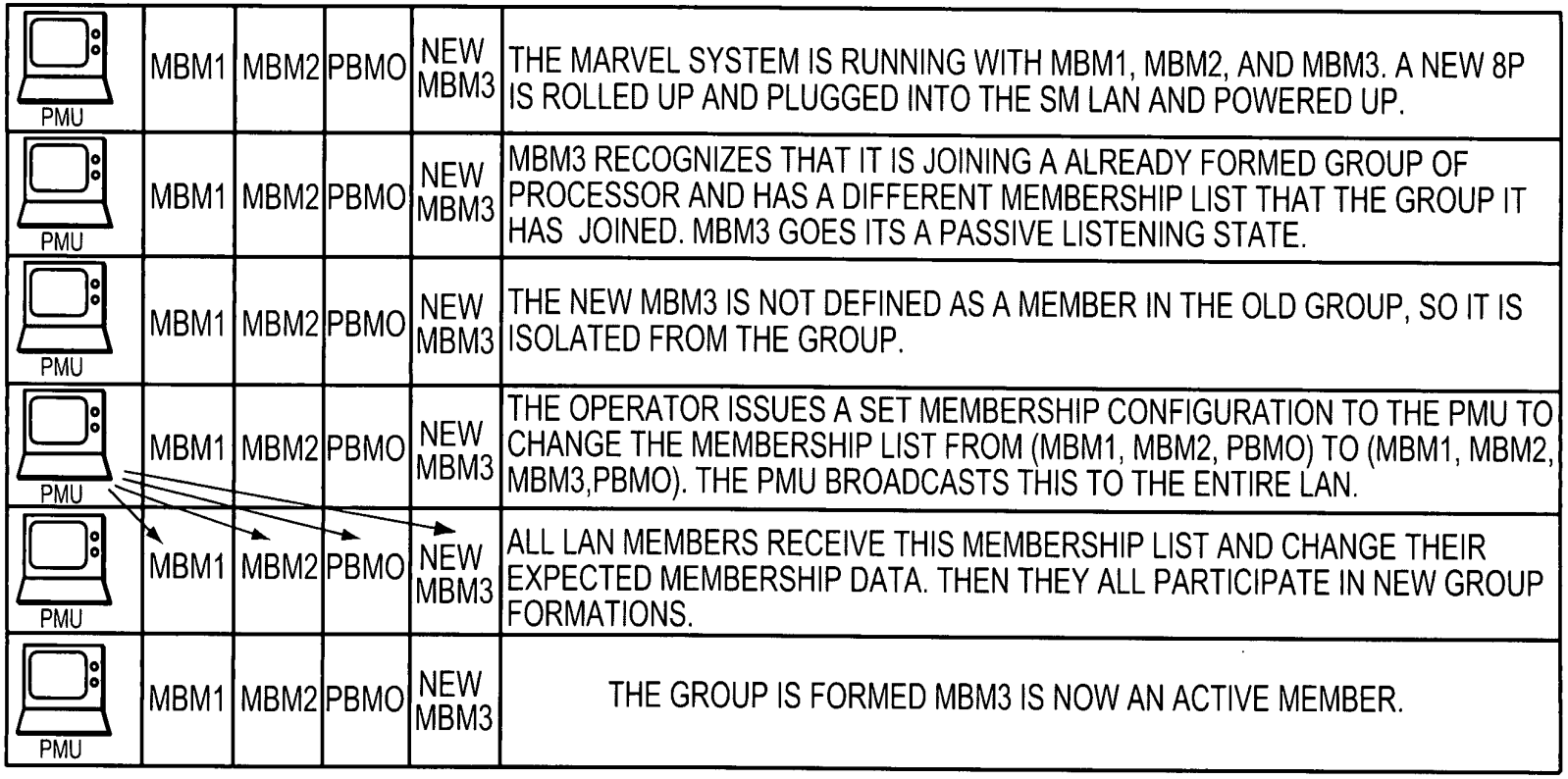
EV7 FAILYRE/REPLACE FLOW DIAGRAM (PART 1)

FIG. 50



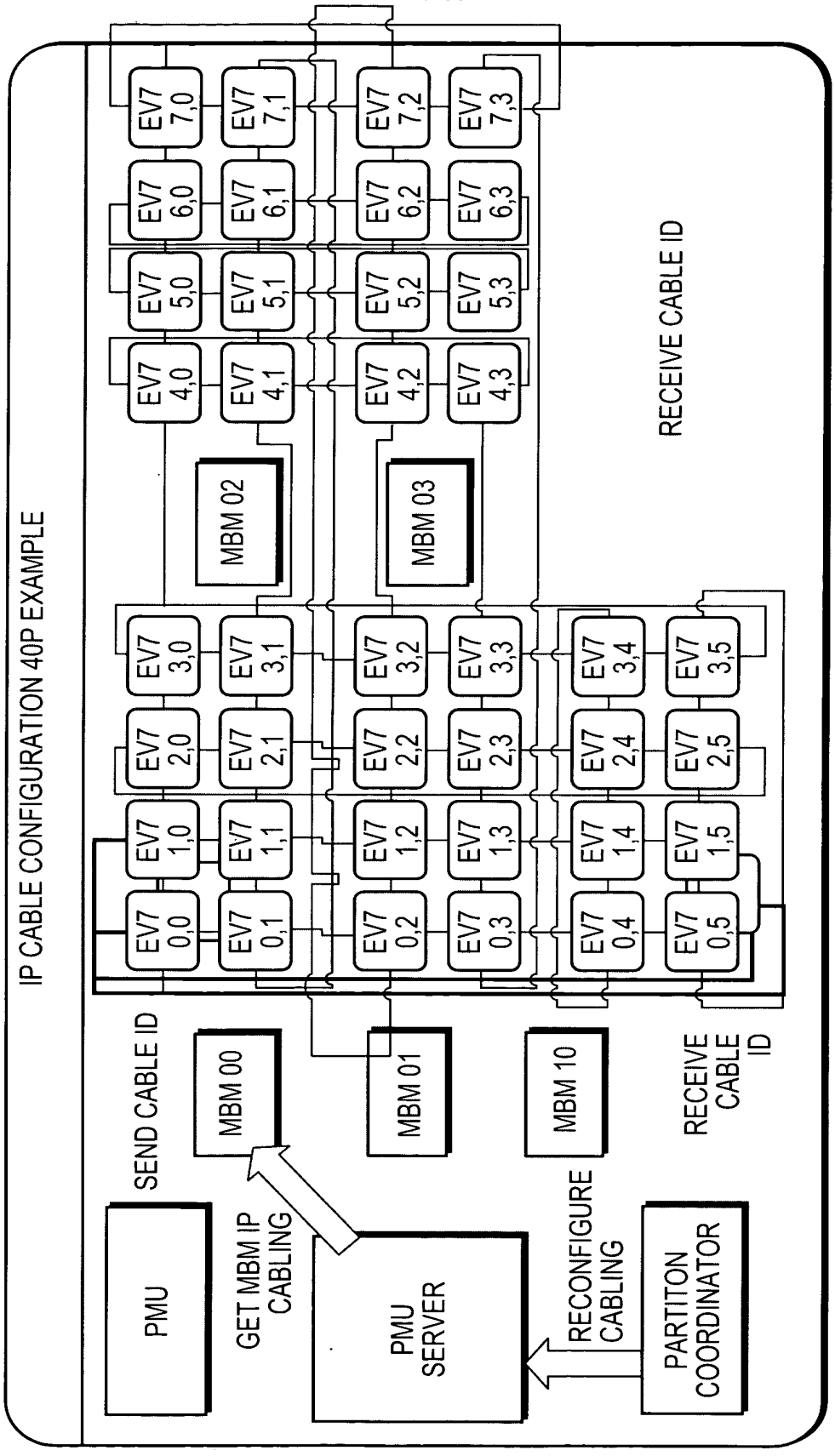
EV7 FAILYRE/REPLACE FLOW DIAGRAM (PART 2)

FIG. 51



SET MEMBERSHIP CONFIGURATION FLOW DIAGRAM

FIG. 52



IP CABLE CONFIGURATION BLOCK DIAGRAM

FIG. 53

THE EV7 IDS (x,y) ARE DETERMINED BY THE THUMB-WHEEL SETTING USING THE FOLLOWING ALGORITHM:
x (E,W COORDINATE) = (RACK NUMBer>>2)*8) + ((MBM NUMBER>>1)*4) + CMM NUMBER
y (N,S COORDINATE) = ((RACK NUMBER & 0x03)*4) = ((MBM NUMBER & 0x01)*2) = EV7 NUMBER

WHERE:

RACK NUMBER IS THE HIGH ORDER HALF BYTE OF THE MBM THUMB-WHEEL
MBM NUMBER IS THE LOW ORDER HALF BYTE OF THE MBM THUMB-WHEEL
CMM NUMBER IS FROM 0 TO 3 WITHIN AN MBM
EV7 NUMBER IS 0 OR 1 WITHIN A CMM

IN A SIMILAR MANNER WHEN THE x,y AXIS COORDINATED OF AN EV7 ARE KNOWN, THE THUMB-WHEEL NUMBER CAN BE DERIVED AND INSERTED INTO THE IP ADDRESS FOR THE MBM, CMM AND EV7s.

EV7 COORDINATE ADDRESSING RELATIONSHIP TO THUMBWHEEL ADDRESSING

FIG. 54

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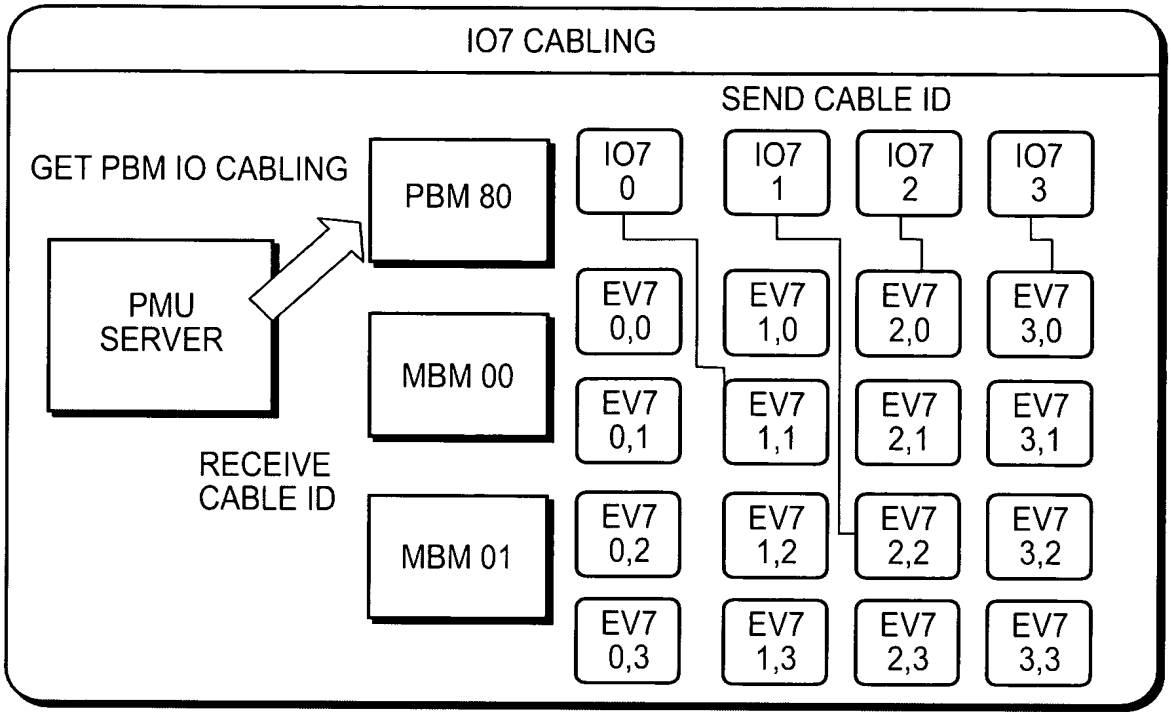
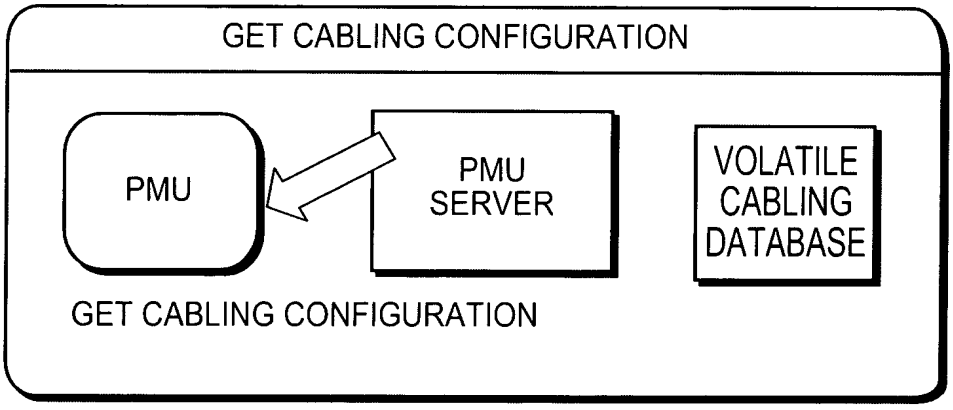


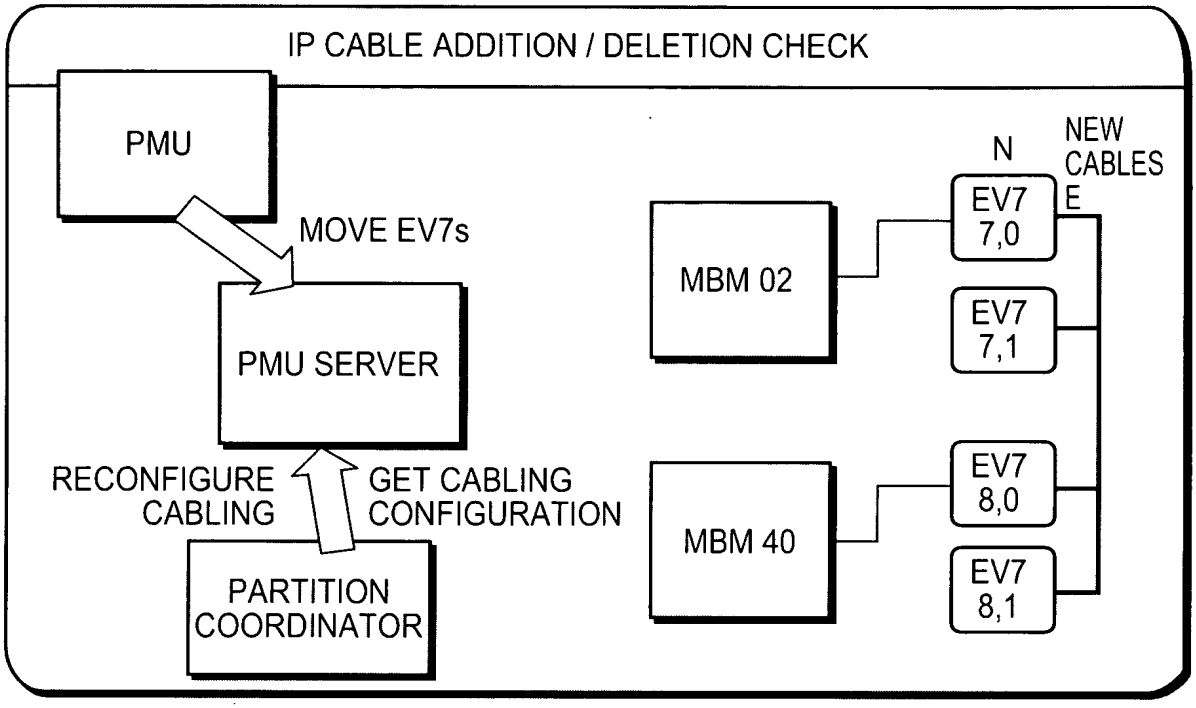
IO7 CABLING BLOCK DIAGRAM

FIG. 55



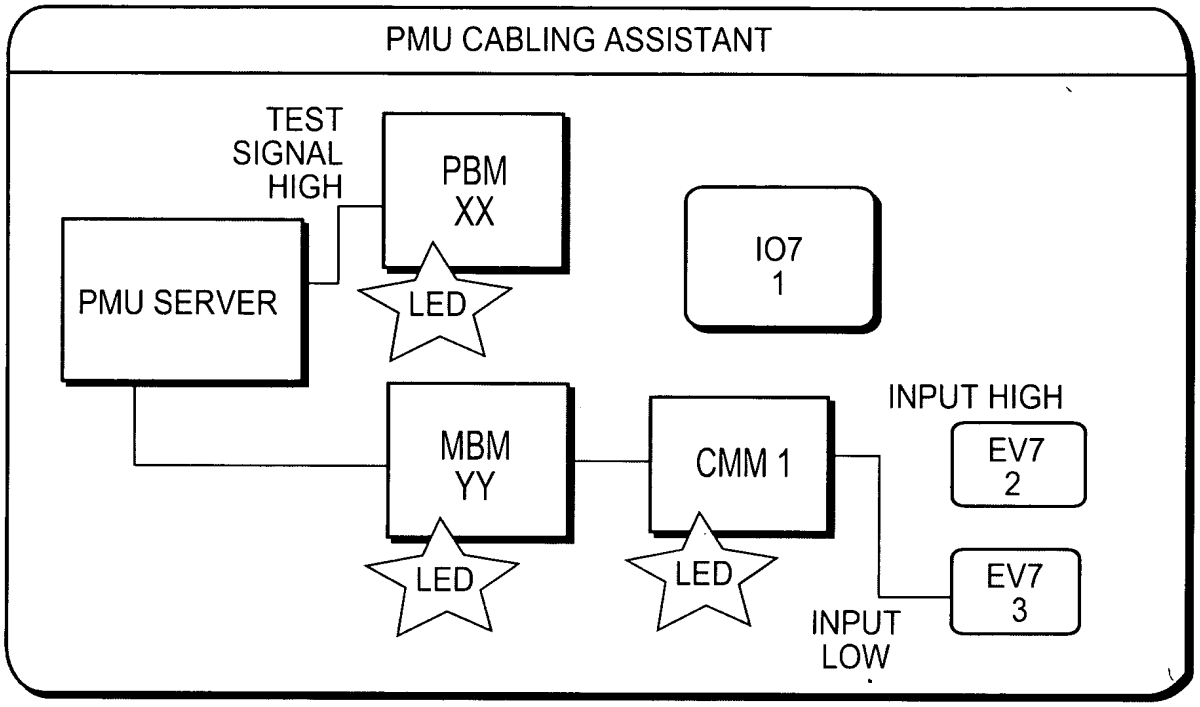
GET CABLE CONFIGURATION BLOCK DIAGRAM

FIG. 56



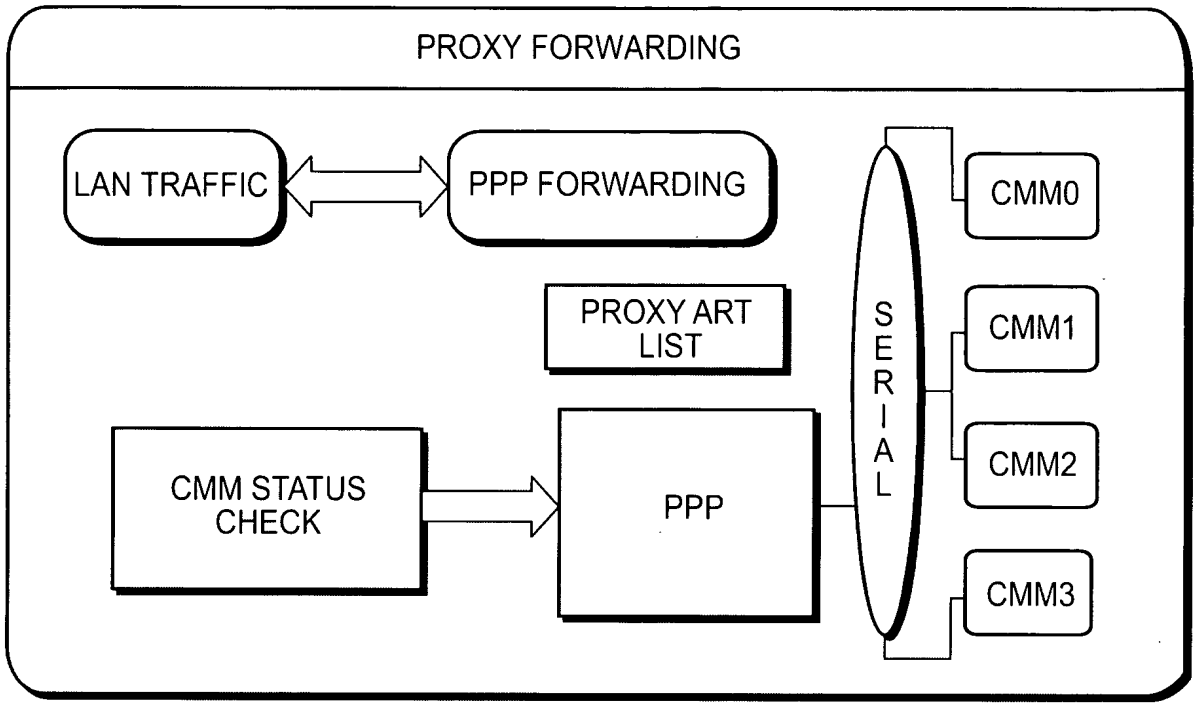
IP CABLE ADDITION/DELETION BLOCK DIAGRAM

FIG. 57



PMU CABLING ASSISTANT BLOCK DIAGRAM

FIG. 58



PROXY FORWARDING BLOCK DIAGRAM

FIG. 59

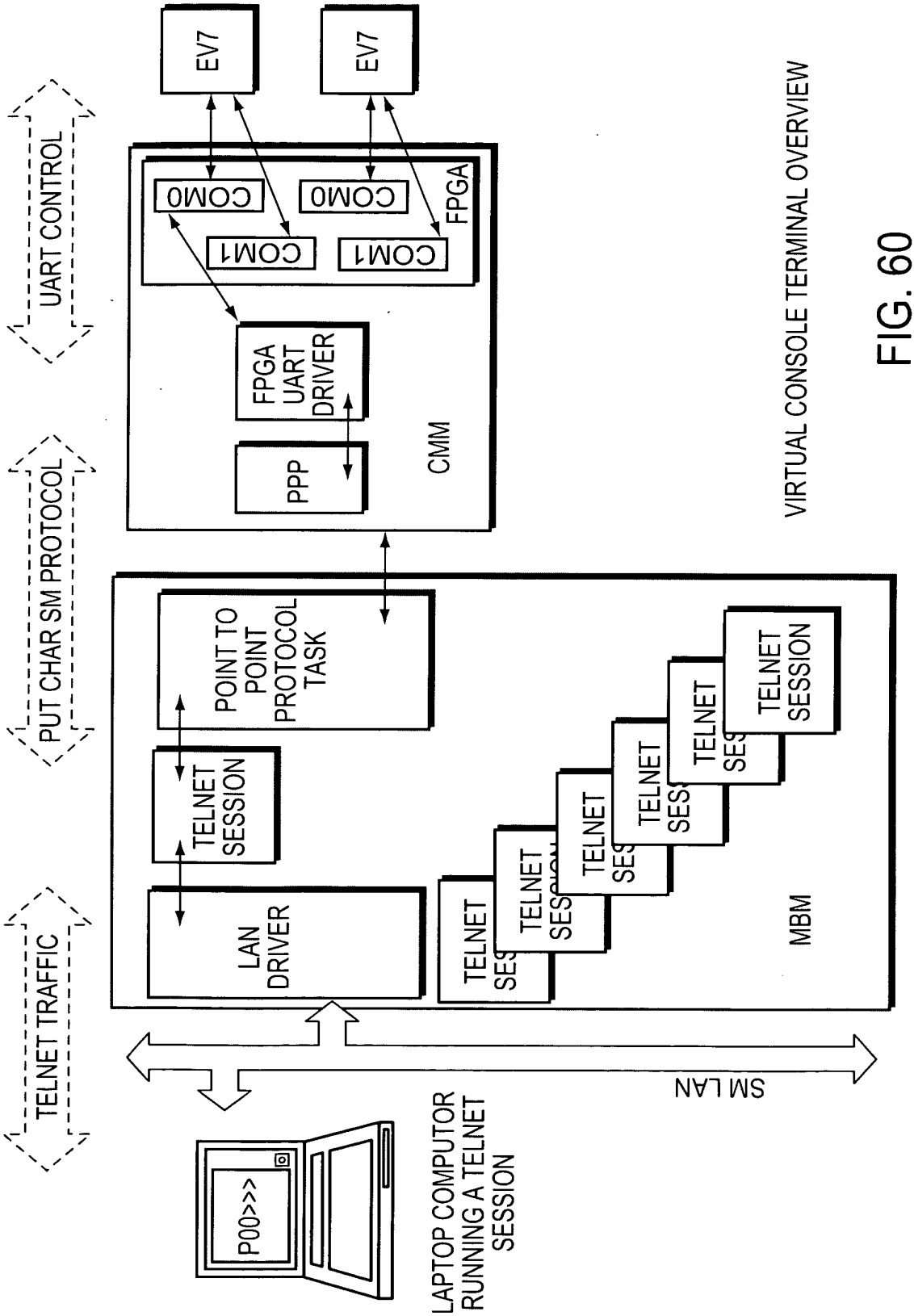

EV7
EV7
UART CONTROL
PUT CHAR SM PROTOCOL
TELNET TRAFFIC
COM0
COM0
COM1
COM1
FPGA
FPGA UART DRIVER
PPP
CMM
POINT TO POINT PROTOCOL TASK
TELNET SESSION
LAN DRIVER
TELNET SESSION
MBM
SM LAN
P00>>>
LAPTOP COMPUTOR RUNNING A TELNET SESSION
VIRTUAL CONSOLE TERMINAL OVERVIEW

FIG. 60

CMM	EV7	COM1 PORT	COM2 PORT
1	1	323	324
1	2	325	326
2	1	327	328
2	2	329	330
3	1	331	332
3	2	333	334
4	1	335	336
4	2	337	338

VIRTUAL TERMINAL TELNET

FIG. 61

VIRTUAL TERMINAL SESSION

PMU	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs	EV7s	THE PMU PLATFORM IS PLUGGED INTO AN AVAILABLE PORT ON THE SM LA
PMU →	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs	EV7s	THE PMU SENDS A GET TELNET IP ADDRESS/PORT REQUEST TO PMU SERVER FOR THE ADDRESS OF THE TELNET SERVER FOR THE TELNET SERVER FOR THE PRIMARY EV7 FOR A PARTITION.
PMU ←	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs	EV7s	PMU SERVER GIVES TO THE ADDRESS OF THE TELNET SERVER FOR THE PRIMARY EV7. (GET TELNET IP ADDRESS/PORT RESPONSE PACKET)
PMU →	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs	EV7s	USER STARTS A TELNET SESSION ON THE PMU USING THE TELNET SERVER ADDRESS FROM PREVIOUS STEP.
PMU	PMU SERVER	TELNET SVR FOR PRI EV7 →	MBMs	CMMs	EV7s	TELNET SERVER PASSES CHARACTERS TO VIRTUAL CONSOLE TASK ON MBM FOR PRIMARY EV7.
PMU	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs →	CMMs	EV7s	MBM FOR PRIMARY EV7 PASSES CHARACTERS TO CMM FOR PRIMARY EV7 USING PUT_CHAR PACKET.
PMU	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs →	EV7s	CMM STRIPS OFF IP ADDRESSING INFO AND PASSES THE CHARACTER TO PRIMARY EV7 THROUGH THE VIRTUAL CONSOLE UART REGISTERS.

VIRTUAL TERMINAL FLOW DIAGRAM

FIG. 62A

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PMU	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs ←	EV7s	PRIMARY EV7 RESPONDS WITH CHARACTERS BACK TO CMM
PMU	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs ←	CMMs	EV7s	CMM ENVELOPS CHARACTER IN PUT_CHAR PROTOCOL PACKET AND GIVES TO MBM.
PMU	PMU SERVER	TELNET SVR FOR PRI EV7 ←	MBMs	CMMs	EV7s	MBM VIRTUAL CONSOLE TASK PASSES CHARACTER TO TELNET SERVER.
PMU ←	PMU SERVER	TELNET SVR FOR PRI EV7	MBMs	CMMs	EV7s	TELNET SERVER PASSES CHARACTER TO TELNET SESSION ON PMU.

VIRTUAL TERMINAL FLOW DIAGRAM

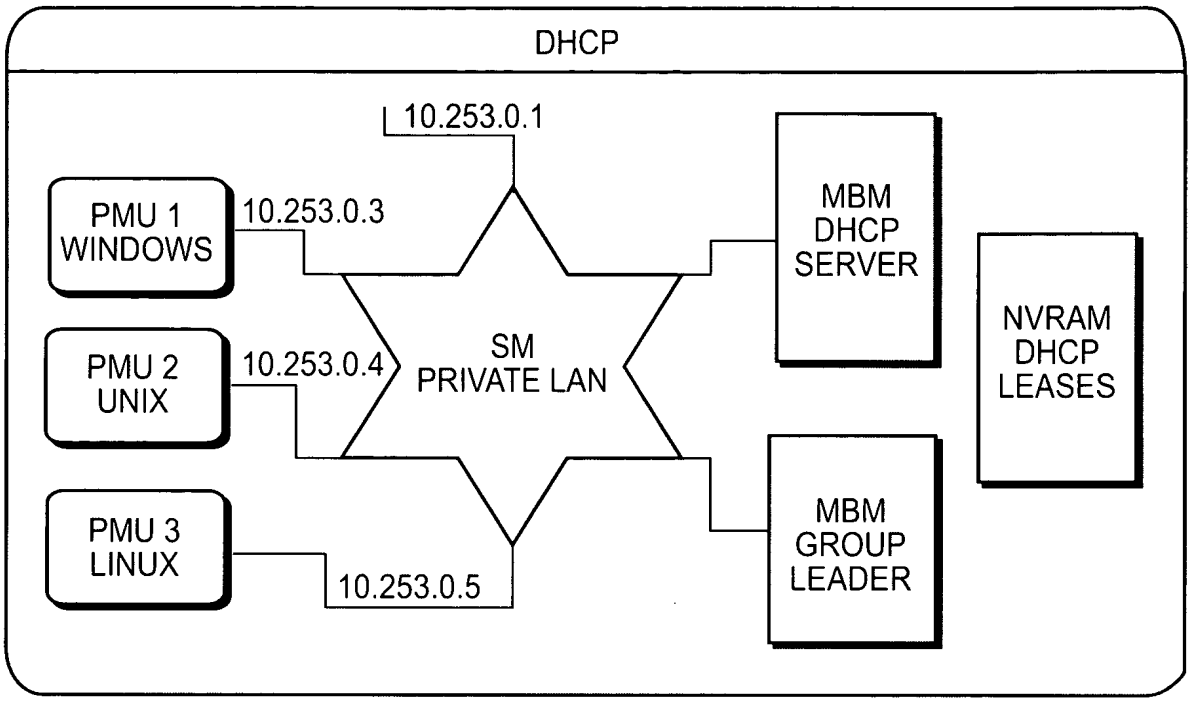
FIG. 62B

SET BASE TIME

PMU	PMU SERVER	MBMs	PBMs	THE PMU IS PLUGGED INTO AN AVAILABLE PORT ON THE SM LAN, GETS AN IP ADDRESS.
PMU →	PMU SERVER	MBMs	PBMs	THE PMU SENDS A GET BASE TIME REQUEST TO PMU SERVER
PMU ←	PMU SERVER	MBMs	PBMs	PMU SERVER GIVES TO THE CURRENT BASE TIME IN ? FORMAT.
PMU	PMU SERVER	MBMs	PBMs	USER TYPES IN NEW TIME AT PMU.
PMU →	PMU SERVER	MBMs	PBMs	PMU GIVE NEW TIME TO PMU SERVER IN SET BASE TIME PACKET.
PMU →	PMU SERVER	GROUP LEADER MBM	PBMs	PMU SERVER GIVES NEW BASE TIME TO GROUP LEADER'S TIME SERVER.
PMU	PMU SERVER	MBMs ↓	PBMs	NEW BASE TIME FRAME IS PROPAGATED AMONG MBMs AND PBM THROUGH THE BASE TIME SYNCHRONIZED TASK.

SET BASE TIME FLOW DIAGRAM .

FIG.63



DHCP BLOCK DIAGRAM

FIG.64

CONTENT	TFTP FILENAME	BLOCK SIZE	SECTOR START(OFFSET)	SECTOR END
MBM/PBM FIRMWARE	"mbmfm"', "pbmfw"	2 MB	0	7
CMM, CMM_FSL, FPGA, SROM, XSROM FIRMWARE	"cmmfw", "cmmfsl", "cmmfpga", "sromfw", "xsromfw"	O.5 MB	8(0,tbd,tbd tbd,tbd,tbd,) d)	9
ERROR LOGS		1 MB	10	13
MBM/PBM FSL FIRMWARE	"mbmfsl", "pbmfsl"	0.5 MB	14	15
NVRAM-PARTITION DATABASE		0.75 MB	16	18
SRM FIRMWARE	"srmfw"	2 MB	20	27
FPGA LOADED BY PBM ON PCI DRAWERS	"pbmfpga"	0.25 MB	28	28
MBM/PBM BOOT [IF REQUIRED BY HW]		0.25	19(0,0X30000)	19

FLASH LAYOUT

FIG.65

BYTE OFFSET	VALUE
0.. 7	0x010100005500aaff
8.. 19	IMAGE REVISION IN ASCII
20.. 23	VENDOR STRING IN ASCII (CPQ)
24.. 31	MODULE ID IN ASCII (SRMFW, MBMFW, MBMFSL, CMMFW, SROMFW, XSROMFW, CMMFPGA)
32.. 35	FIRMWARE TYPE IN ASCII (ALPH, X86)
36.. 43	0x00
44.. 47	CODE LENGTH IN BYTES
48.. 59	ROM OBJECT NAME (FW,FSL,SROM,XSROM,FPGA)
60.. 63	0x11223344

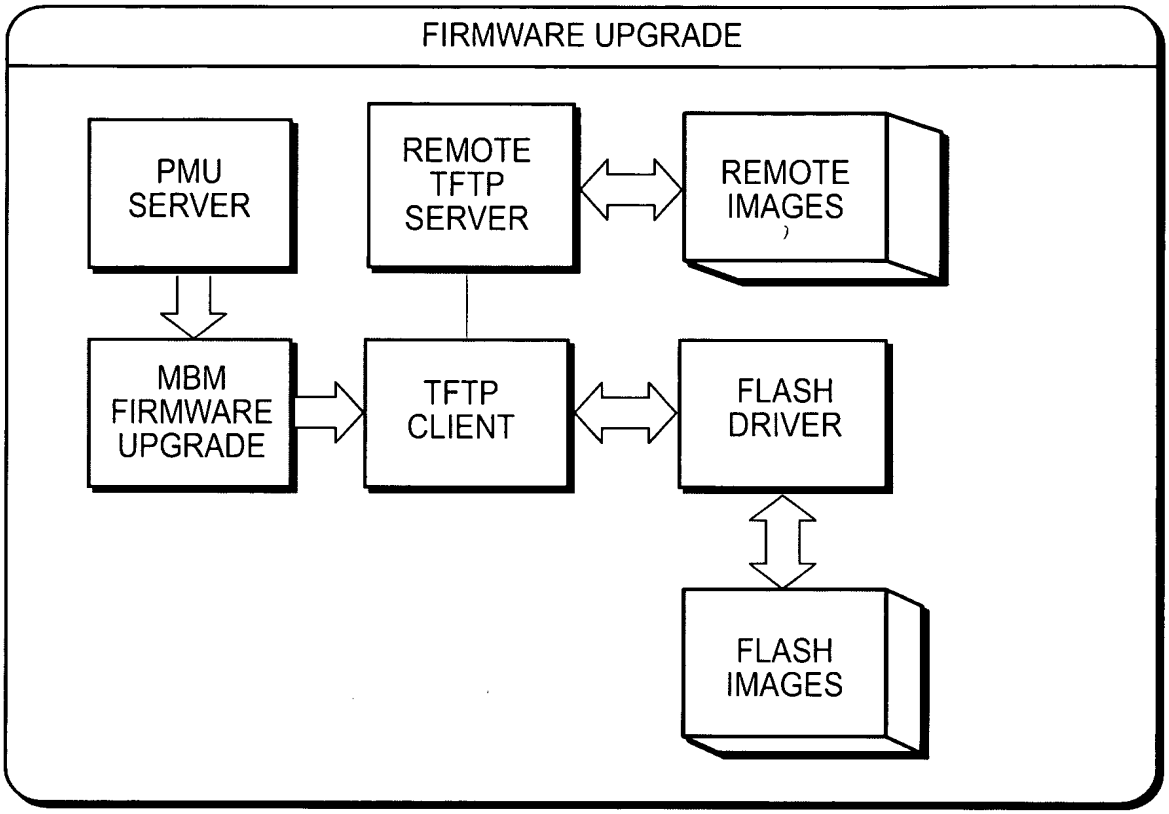
IMAGE HEADER

FIG.66

ALL MBMs PBMs	MBM	CMM	SRM	THE PRIMARY CPU IN THE PARTITION ISSUES THE COMMAND >>> SET BOOTDEF_DEV DKA 100. THIS BECOMES A STORE ENVIRONMENT VARIABLES PACKET.
ALL MBMs PBMs	MBM	CMM	SRM	MBM WAITS FOR THE TRAIN TO ARRIVE AND THEN RETRANSMITS THE FULL TRAIN MESSAGE WITH THE STORE ENVIRONMENT VARIABLES PAYLOAD.
ALL MBMs PBMs	MBM	CMM	SRM	ALL MBMs RECEIVE THE TRAIN, COPY OUT THE PAYLOAD AND HOLD THAT PAYLOAD AS A PENDING COMMAND. THE MBM PASSES ALONG THE FULL TRAIN MESSAGE, AND WAIT FOR THE EMPTY TRAIN.
ALL MBMs PBMs	MBM	CMM	SRM	THE TRAIN MAKES IT FULL CIRCLE BACK TO THE ORIGINATING MBM. IT THEN COMMITS THE SRM ENVIRONMENT VARIABLES TO THEIR FLASH. IT THEN PUTS THE FULL TRAIN MESSAGE INTO THE TRAIN PAYLOAD AND REISSUES IT
ALL MBMs PBMs	MBM	CMM	SRM	ALL MBMs COMMIT THE DATA TO THEIR FLASHES AND PASS ON THE FULL TRAIN MESSAGE.
ALL MBMs PBMs	MBM	CMM	SRM	THE TRAIN MAKES IT FULL CIRCLE BACK TO THE ORIGINATING MBM. IT NOW CAN RESPOND TO THE STORE ENVIRONMENT VARIABLES COMMAND AND SENDS OUT THE EMPTY TRAIN MESSAGE.
ALL MBMs PBMs	MBM	CMM	SRM	THE REPLY PASSES THROUGH THE CMM AND BACK TO THE EV7 RUNNING THE SRM CONSOLE WITH A SUCCESSFUL COMPLETION STATUS.

SRM ENVIRONMENT VARS FLOW DIAGRAM

FIG. 67



FIRMWARE LOAD AND UPGRADE BLOCK DIAGRAM

FIG.68

PMU	PMU SERVER	MBM	CMMx	UPGRADE FIRMWARE FOR CMM ISSUED BY PMU SERVER.
PMU	PMU SERVER	MBM	CMMx	THE PMU SERVER FORWARDS THE COMMAND TO THE MBM THAT IS PARENT TO THE CMM.
PMU TFTP SERVER	TFTP XFET	MBM TFTP CLIENT	CMMx	T HE MBM STARTS A TFTP CLIENT AND PULLS THE FILES FOR THE UPGRADE. THE MBM REQUEST THE FILES CMMFW,CMMFSL CMMFPGA,SROMFW,XSROMFW.
PMU	PMU SERVER	MBM	CMMx	THE MBM WRITES THE FILES TO ITS FLASH AND THEN SENDS AN UPGRADE FIRMWARE COMMAND TO THE CMM.
PMU	PMU SERVER	MBM TFTP SERVER	TFTP XFET CMMx TFTP CLIENT	THE CMM STARTS UP A TFTP SESSION AND PULLS THE FILES FROM THE MBM WHICH ACTS AS THE TFTP SERVER. THE CMM REQUESTS THE THE FILES CMMFW, CMMFSL, CMMFPGA, SROMFW, XSROMFW.
PMU	PMU SERVER	MBM	CMMx	THE CMM WRITES ITS FLASH AND RESPONDS TO THE UPGRADE FIRMWARE.
PMU	PMU SERVER	MBM	CMMx	THE MBM REPEATS THIS UPGRADE FOR ALL MBM THAT IT CONTROLS.
PMU	PMU SERVER	MBM	CMMx	THE MBM RESPONDS TO THE PMU SERVE'S UPGRADE FIRMWARE REQUEST.
PMU	PMU SERVER	MBM	CMMx	THE PMU SERVER RESPONDS TO THE PMU's UPGRADE FIRMWARE REQUEST.

UPGRADING CMM FIRMWARE FLOW DIAGRAM .

FIG. 69

ERROR LOG ENTRY										
SIZE (DEC)	START (HEX)	END (HEX)	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
2	0	1	ENTRY NUMBER (1-OXFFFE BEFORE WRAPPING)							
6	2	7	TIME STAMP (ssmmhhDDMMYY)							
2	8	9	ENTRY SIZE							
n	A	N+A	ENTRY DATA							

ERROR LOG ENTRY FORMAT

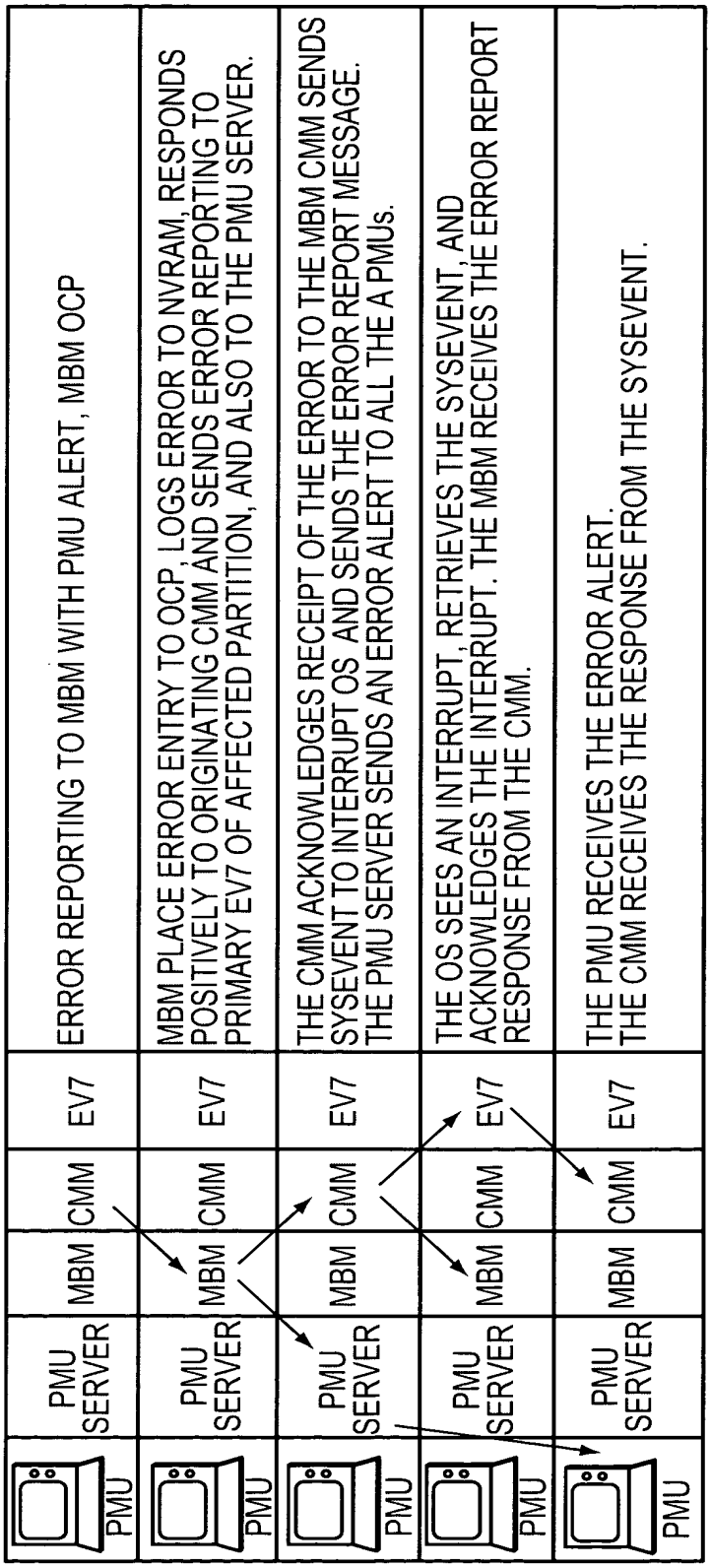
FIG. 70

ERROR ENTRY DATA										
SIZE (DEC)	START (HEX)	END (HEX)	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
4	0	3	ENTRY NUMBER							
1	4	4	SEVERITY LEVEL							
1	5	5	ENTITY IN ERROR							
1	6	6	INSTANCE							
2	7	8	ERROR CODE							
16	9	18	SERIAL NUMBER							
N	19	N+19	VARIABLE DATA							

ENTRY NUMBER	ID ADDRESS OF CAM, MOM, PAM, OR EVE THAT ENCOUNTERED THE ERROR.
SEVERITY LEVEL	INFORMATIONAL=0; WARNING=1; ERROR=2;
ENTITY IN ERROR	THE DEVICE CODE FOR THE DEVICE IN ERROR (EGG. CAM, RIM, EVE, THERMAL, VOLTAGE)
INSTANCE	THE INSTANCE OF THE ENTITY.
ERROR CODE	ERROR ENUMERATION OR INDEX INTO A SET OF TEXT MESSAGES.
SERIAL NUMBER	IDENTIFYING ADDRESS WHERE THE ERROR OCCURRED.
VARIABLE DATA	ADDITIONAL DATA SPECIFIC TO THIS ERROR CODE.

ERROR ENTRY DATA FORMAT

FIG. 71



ERROR REPORTING FLOW DIAGRAM

FIG.72

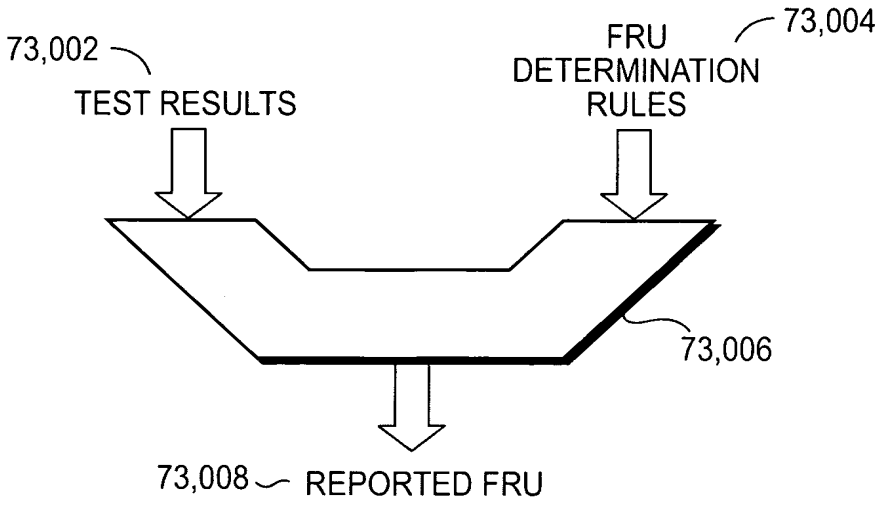
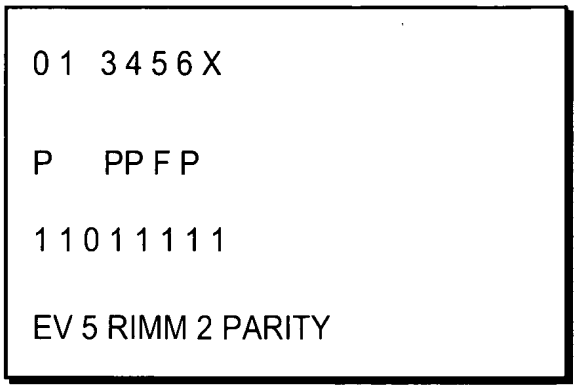

73,002
TEST RESULTS
FRU
DETERMINATION
RULES
73,004
73,006
73,008
REPORTED FRU
FIELD REPLACEABLE UNIT

FIG. 73

LINE	12345678901234567890
1	<OVERALL PROGRESS>
2	<CURRENT STATE>
3	<LOCATION WITHIN STATE>
4	<ERROR MESSAGE>

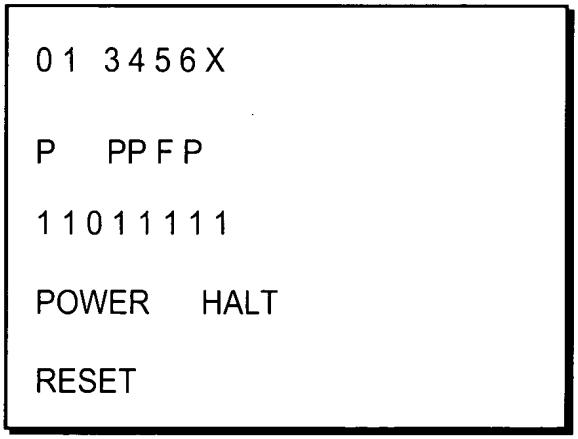
OCP TEMPLATE

FIG. 74



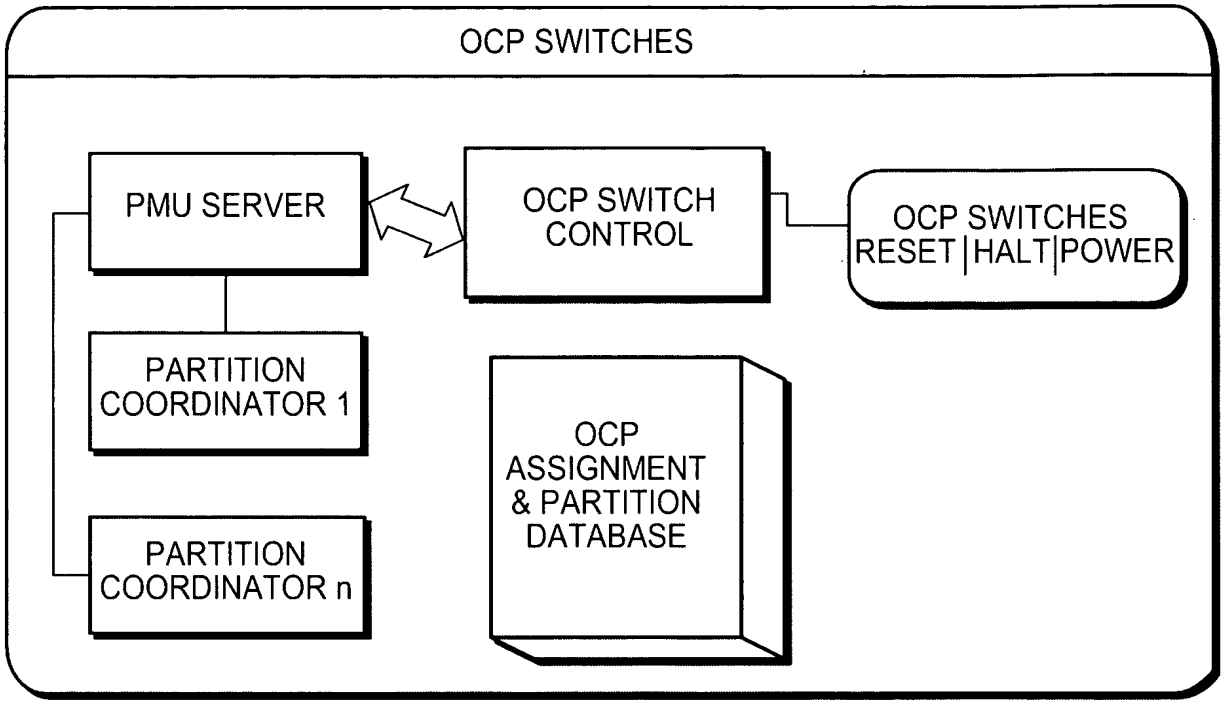
OCP 8P EXAMPLE

FIG. 75



OCP BUTTON LABLE EXAMPLE

FIG. 76



OCP SWITCHES BLOCK DIAGRAM

FIG. 77

COMMAND	PURPOSE	PROCESS
SET ATTENTION INDICATOR	LIGHT, EXTINGUISH LED AT MBM, PBM, CMM OR CABINET.	TAKE ACTION ACCORDING TO THE DESIRED STATE IN THE REQUEST.
SET KNOB	NAME/VALUE PAIR TO CONTROL SOME MBM/PBM CAPABILITIES	CODED INTO THE IMAGE IS A LIST OF VARIABLE NAMES THAT CAN BE MODIFIED
GET KNOB	THE VALUE CURRENTLY MAINTAINED FOR THE NAMED VARIABLE IS RETURNED.	CHECK TO SEE IF THE NAME IS IN THE LIST AND RETURN IT'S CURRENT VALUE IN THE RESPONSE.
READ	ALLOW DEBUG READ OF PHYSICAL MEMORY OR I/O	CHECK ON VALIDITY OF REQUEST AND IF VALID, READ PHYSICAL MEMORY SPACE OR DIRECT I/O SPACE. FOR DEBUG.
WRITE	ALLOW DEBUG WRITE OF PHYSICAL MEMORY OR I/O	CHECK ON VALIDITY OF REQUEST AND CHECK MMU PROTECTION PRIVILEGES TO WRITE IN THE SPACE SO AS NOT TO CAUSE PROTECTION VIOLATIONS. IF NOT PROTECTED, WRITE THE BLOCK. FOR DEBUG.

MISCELLANEOUS COMMAND HANDLING

FIG. 78

COMMAND	ARG COUNT	ARGUMENTS	RESULT	HANDLING
Show _Config	0		SEE SECTION "SHOW CONFIGURATION WITH FRU DATA"	MAKE REQUEST TO PMU SERVER
RESET	2	1-PARTITION NO 2-SUB PARTITION NO	RETURNS OK OR ERROR	SEND RESET PARTITION TO PMU SERVER
Power_on	2	1-PARTITION NO 2-SUB PARTITION NO	RETURNS OK OR ERROR	SEND POWER ON PARTITION TO PMU SERVER
Power_off	2	1-PARTITION NO 2-SUB PARTITION NO	RETURNS OK OR ERROR	SEND POWER OFF PARTITION TO PMU SERVER
Halt_on	2	1-PARTITION NO 2-SUB PARTITION NO	RETURNS OK OR ERROR	SEND HALT ON PARTITION TO PMU SERVER
Halt_off	2	1-PARTITION NO 2-SUB PARTITION NO	RETURNS OK OR ERROR	SEND HALT OFF PARTITION TO PMU SERVER
Prepare _EV7_List	2-16	1-MBM RACK-THUMB-WHEEL, 2-EV7 ID (0-7) UP TO 8 PAIRS	OK IF ALL ELEMENTS ARE IN THE SAME HARD PARTITION OR FREE POOL	SAVES THIS VALUE IN MBM RAM FOR USE WITH THE NEXT ADD EV7s, FREE EV7s. LASTS UNTIL NEXT PREPARE EV7 LIST.
Add_Ev7s	2	1-PARTITION NO 2-SUB PARTITION NO	TAKE THE VALUES IN THE EV7 LIST AND ADD IT TO THE PARTITION.	SEND COMMAND TO PMU SERVER.
Free_Ev7s	0		TAKE THE VALUES IN THE EV7 LIST AND REMOVE THEM FROM THE PARTITION INDICATED.	SEND COMMAND TO PMU SERVER.
Save_ Partition	2	1-PARTITION NO 2-SUB PARTITION NO	THE PARTITION DATABASE GETS STORED TO NVRAM.	SEND COMMAND TO PMU SERVER.
Destroy_ Partition	2	1-PARTITION NO 2-SUB PARTITION NO	RESET &. FREE ALL EV7S FROM PARTITION.	SEND COMMAND TO PMU SERVER.
Ev7_test	3	1-MBM RACK-THUMB-WHEEL, 2-EV7 ID (0-7), 3-TEST NUMBER	TEST STATUS	SEND A EV7 START TEST REQUEST TO THE CMM.

FIG. 79A

COMMAND	ARG COUNT	ARGUMENTS	RESULT	HANDLING
Add_Cable	5-6	1-SOURCE - MBM RACK-THUMB-WHEEL, 2-EV7 ID (0-7), 3-PORT(N,S,E,W), 4-DESTINATION - MBM/PBM RACK-THUMB-WHEEL, 5- EV7 ID(0-7) OR IO7 ID(0-3), 6-PORT(N,S,E,W) OR BLANK WHEN IO7	THIS COMMAND ASSISTS IN LOCATING THE PROPER CONNECTOR PAIR TO CONNECT THE CABLE. THE LEDS AT EACH CONNECTOR ARE LIT UNTIL THE CONNECTION IS COMPLETE.	THE COMMANDS SET CABLE TEST SIGNAL STATE AND GET CABLE TEST SIGNAL STATE ARE SENT TO THE APPROPRIATE MBM AND PBM TO CAUSE THE LEDS TO LIGHT AND CHECK THE CONNECTION ITSELF.
New_Cable	0		REDO CABLING TESTS.	SEND RECONFIGURE CABLING TO PMU SERVER.
Show_ cabling	0		DISPLAYS A LIST OF IP & IO CABLING	SEND RETRIEVE CABLING CONFIGURATION TO PMU SERVER
Virt_ console	3	1-PARTITION NO 2-SUB PARTITION NO. 3-COM PORT(1,2)	OPEN A SESSION WITH PRIMARY EV7 & INTERCEPT COMx PORT DATA.	USE PutChar STREAMS FOR BOTH DISPLAY AND KEYBOARD DATA UNTIL THE KEYBOARD DATA SEQUENCE 'ESC'ESC'S'M' IS RECOGNIZED AS AN EXIT OF THE SESSION.
Get_fans	1	1-MBM/PBM RACK -THUMB-WHEEL	RPM AND THRESHOLD THAT FANS ARE RUNNING AT.	DETERMINE APPROPRIATE IP ADDRESS FOR DESTINATION AND SEND A GET FAN RPM SPEED MESSAGE
Set_fans	3	1- MBM/PBM RACK -THUMB-WHEEL, 2-INSTANCE OF FAN, 3-RPM	ERROR OR OK	DETERMINE APPROPRIATE IP ADDRESS FOR DESTINATION AND SEND A SET FAN RPM SPEED MESSAGE.
Error_ counts	0		RETURNS A LIST OF THE ERROR COUNTS ON ALL MBM/PBM ERROR LOGS.	SEND ERROR LOG COUNT REQUEST TO EACH MBM/PBM.
Error_clear	1	1- MBM/PBM RACK -THUMB-WHEEL	OK	SEND ERROR LOG CLEAR REQUEST TO DESTINATION
Get_errors	1	1- COUNT OF THE NUMBER OF ERRORS TO BE REPORTED ON EACH DEVICE.1	A LIST OF THE LAST ERROR MESSAGES IN ENGLISH AS IT WOULD APPEAR ON THE OCP WITH ANY QUALIFYING DATA FORMATTED AS APPROPRIATE. THIS IS REPEATED FOR EACH MBM AND PBM	GET THE ERROR LOG COUNT FROM EACH MICRO. SEND ERROR LOG ENTRY RETRIEVAL REQUEST TO EACH MICRO USING THE HIGHEST NUMBER AS THE 1st REQUEST.

FIG. 79B

COMMAND	ARG COUNT	ARGUMENTS	RESULT	HANDLING
Get_time	0		DATE AND DINE IS DISPLAYED AS dd/mm/yy hh:mm:ss	USE GET BASE TIME COMMAND
Set_ time	1	1-DATE AND TIME ENTRY IN FORMAT: "dd/mm/yy-hh:l mm: ss	REDISPLAYS DATE AND TIME	SET BASE TIME AND ANNOUNCE BASE TIME CHANGE IS SENT TO ALL MBMS.
Req_knobs	2-3	1- MBM/PBM RACK -THUMBWHEEL, 2- DEVICE (MBM ", "PBM". "CMM"). 3- IFCMMIN2. THEN NUMBER (0-3)	NAMES AN VALUES OF ALL POSSIBLE KNOBS ARE LISTED.	REQUEST KNOB COMMAND FOR ALL POSSIBLE KNOBS FOR THAT DEVICE..
Set_knob	4-5	1- MBM/PBM RACK -THUMB-WHEEL, 2-DEVICE("MBM". "PBM"."CMM"). 3-IFCMMIN2,THEN NUMBER (0-3) 2-KNOB NAME, 3-KNOB VALUE	OK	SET KNOB ON REQUESTED MICRO.
Firmware_ version	3-4	1- MBM/PBM RACK -THUMB-WHEEL, 2-DEVICE('MBM", "PBM". "CMM"), 3-IFCMM IN2.THEN NUMBER (0-3) 4-MODULE('CMM" "FPGA","SROM", "XSROM","MBM" "PBM","CMM_FS L", "MBM_FSL", "PBM_ FSL", "PBM_FPGA")	RETURNS THE VERSION NUMBER	REPORT FIRMWARE VERSION COMMAND
Firmware_ upgrade	4-5	1-MBM/PBM RACK -THUMB-WHEEL. 2-DEVICE("MBM". "PBM","CMM"). 3-IFCMM IN2. THEN NUMBER (0-3) 4-MODULE("CMM". "FPGA",'SROM", "XSROM","MBM", "PBM","CMM_FSL", "MBM_PSL"," PBM_ FSL","PBM_FPGA") 5-TFTP SERVER IP ADDRESS	MAKES THE TERMINAL INTO A PPP SERIAL LINK MAKING TFTP REQUESTS UNTIL COMPLETION OF THE TRANSFER OR A TIMEOUT OCCURS. POSSIBLE RETURN VALUES ARE: "COMPLETE". "TIMEOUT". FILE TOO LONG". "PILE TOO SHORT"	SEND THE UPGRADE FIRMWARE REQUEST TO THE MBM OR PBM OR CMM. MAKE THE BACKUP COPY WHERE REQUIRED. MBM HAS A COPY OF CMM PROGRAM.

FIG. 79C

COMMAND	ARG COUNT	ARGUMENTS	RESULT	HANDLING
Load_Test_ Version.	4-5	1- MBM/PBM RACK -THUMB-WHEEL, 2-DEVICE("MBM", "PBM", "CMM"), 3-IF CMM IN2,THEN NUMBER (0-3) 4-MODULE("CMM", "FPGA","SROM" "XSROM","MBM" "PBM","CMM_FS L "MBM_FSL", " PBM_FSL","PBM_ FPGA") 5-TFTP SERVER IP ADDRESS	MAKES THE TERMINAL INTO A PPP SERIAL LINK MAKING TFTP REQUESTS UNTIL COMPLETION OF THE TRANSFER OR A TIMEOUT OCCURS. POSSIBLE RETURN VALUES ARE: "COMPLETE", "TIMEOUT", "FILE TOO LONG", "FILE TOO SHORT"	SEND THE LOAD TEST VERSION COMMAND TO THE MBM OR PBM OR CMM. WHICH MAINTAINS A COPY OF THE PROGRAM IN MBM MEMORY.
Disable_ test_ version	3-4	1- MBM/PBM RACK -THUMB-WHEEL, 2-DEVICE("MBM", "PBM","CMM"), 3-IF CMM IN 2.THEN NUMBER (0-3) 4-MODULE('CMM" "FPGA","SROM", "XSROM","MBM" "PBM","CMM_FS L", "MBM_FSL", "PBM_FSL", "PBM_FPGA")	OK	SEND Disable_test version COMMAND

CLI COMMANDS

FIG. 79D

KNOB NAME	POSSIBLE VALUES	USAGE
CLI_PORT_SPEED	1200, 2400, 4800, 9600, 19200, 38400, 57600, 115200,230400.	SPEED BETWEEN COM PORT AND MODEM (DEFAULT 57600bps)
CLI_DATA_BITS	8, 7	COM PORT UART USES 7 OR 8 DATA BITS BEFORE STOP (DEFAULT 8)
CLI_STOP_BITS	1, 1.5, 2	COM PORT UART SENDS 1, 1.5 OR 2 STOP BITS TO MODEM (DEFAULT 1)
CLI_FLOW_BITS	HW, SW, NONE	FLOW CONTROL: HW-RTS/CTS SIGNALS (DEFAULT), SW-XON/XOFF BYTES, NONE
CLI_MODEM	YES, NO	IF MODEM IS CONNECTED USE NO; OTHERWISE THE FOLLOWING SET OF CLI_MDM KNOBS ARE REQUIRED.
CLI_MDM_INIT	AT STRING FOR MODEM INITIALIZATION	ON EACH HANG-UP OR DROP OF CARRIER SIGNAL THIS COMMAND IS SENT TO THE MODEM (DEFAULT IS "ATZ"). IF MODEM DOESN'T RESPOND WITH OK, 3 RETRIES ARE ATTEMPTED.
CLI_MDM_DIAL	AT STRING WHEN WE DIAL OUT TO DROP AN ALERT MESSAGE OR DIAL-BACK.	PREFIX FOR DIALING THE NUMBER INDICATED IN THE ALERT NUMBER OR DIAL -BACK NUMBER. (DEFAULT IS "ATDT"). IF MODEM RESPONDS WITH OK, COMMUNICATION IS CONSIDERED TO BE ESTABLISHED, ELSE 3 RETRIES ARE ATTEMPTED.
CLI_DIAL_BACK	PHONE NUMBER WITH DIALING PAUSES.	FOR SECURITY PURPOSES WHENEVER A CONNECTION IS MADE, THE PROGRAM WILL HANGUP AND DIAL THE INDICATED TO ESTABLISH CONNECTION. (DEFAULT EMPTY)
CLI_DIAL_ALERT	PHONE NUMBER WITH POSSIBLE DIALING PAUSES FOR A RECEIVER OF ALERT MESSAGE.	IF AN ERROR MESSAGE HAS AN ALERT INDICATION, THE TEXT PORTION THAT WOULD BE FORMATTED FOR THE OCP IS SENT AFTER ESTABLISHING A MODEM CONNECTION WITH THE INDICATED NUMBER. NOTE: THERE IS NO PAGING SUPPORT TAPI, ALPHANUMERIC OR NUMERIC IMPLIED BY THIS OPTION.
CLI_PASSWOWRD	THE ONLY PASSWORD THAT IS ALLOWED TO AT TIME OF CONNECTION. A PASSWORD PROMPT IS USED.	THE PASSWORD PROMPT APPEARS ON A MODEM CONNECTION REQUESTING THE PASSWORD ENTRY TO CONTINUE. THE ENTRY MUST MATCH THE NULL TERMINATED STRING BELONGING TO THIS KNOB. IF NOT, A HUNG-UP COMMAND IS SENT TO THE MODEM. A DEFAULT PASSWORD WILL EXIST <TBD>, IF NONE HAS BEEN ASSIGNED.

MODEM KNOBS

FIG. 80

CAPABILITY	RAMIFICATION DURING MBM FAILURE
ROXY FORWARDING	LAN MESSAGES TO THE CMM FAIL
CMM PRESENCE WATCHDOG	THERE IS NO NOTIFICATION OF FAILURE
TFTP SERVER	CMM IS UNABLE TO BE FIRMWARE UPGRADE
ERROR REPOSITORY & DISTRIBUTION	CMM IS UNABLE TO LOG ERRORS
POWER HIERARCHY	PARTITION POWER CHANGES CANNOT OCCUR.
TIME SERVICES	CMM DOES NOT RECEIVE REGULAR TIME UPDATES
VIRTUAL CONSOLE TERMINAL	CONSOLE INPUT/OUTPUT UNAVAILABLE IF PRIMARY IN PATH
SRM ENV. VAR. REPOSITORY	ENV. WRITES FAIL; SRM WRITE CALLBACK FAIL

MODEMS KNOBS FOR CONNECTION TO CCI PORT

FIG. 81

COMMAND	OS/ SRM UP	ALL EV7s	MAJ. GRP.	COMMENT
DISTRIBUTE PARTITION DATABASE WITH EV7 CHANGE, ADD EV7, DELETE EV7.	T	T	T	PROVIDED THE NEW EV7 IS STILL IN THE MAJORITY GROUP AND ROUTABLE.
SET PARTITION DELTA TIME, STORE ENVIRONMENT VARIABLES, ASSIGN SUB PARTITION, ASSIGN MEMORY & IO7	X	X	T	THIS AFFECTS THE DATABASE; BUT WHEN JOINED BACK AGAIN MAJORITY WINS.
RESET, HALT, QUIESCE, CONTINUE	T	T	X	THIS DOESN'T AFFECT THE NON-VOLATILE DATABASE.
POWER ON, POWER OFF, CHANGE PRIMARY	X	X	X	NEVER ALLOWED ON A SPLIT PARTITION

OPERATION LIMITATIONS IN A DEGRADED SYSTEM

FIG. 82

OPERATIONS	STRUCTURES	DISTRIBUTION COMMAND	RAM	NVR
THE LAN GROUP PROTOCOL MESSAGES USE EITHER BROADCAST OR REQUEST/ RESPONSE MESSAGES IN FORMING A GROUP.	GroupID(RAM). MajGID(RAM). Micro-procesorSet(NVRAM)	NEW GROUP, ACCEPT, JOIN, SetMembership	X	X
THE GROUP LEADER, AFTER FORMING A NEW GROUP, CHECKS THE COPY OF ALL MEMBERS' PARTITION DATABASES AND DISTRIBUTED THE COPY. EV7 CHANGES NOTED BY THE PMU SERVER ARE DISTRIBUTED VIA A PARTITION DATABASE CHANGE. THE PARTITION COORDINATOR, WHEN RECONFIGURING THE PARTITION MAY NEED TO DISTRIBUTE COMMANDS THAT CHANGE THE VOLATILE COPY OF THE PARTITION DATABASE.	EV7/I07. MEMORY LOCATIONS, ASSIGNMENTS AND STATUS.	REQUEST PARTITION DATABASE, DISTRIBUTE PARTITION DATABASE	X	X
THE PMU SERVER INITIALIZATION INCLUDES THE CABLE TESTING A DISTRIBUTION OF MAT VOLATILE DATABASE INFORMATION.	CABLE CONFIGURATION	RETRIEVE CABLING CONFIGURATION	X(NOT DISTRIB UTED)	
THE DHCP SERVER DISTRIBUTES THE LIST OF DHCP LEASES AND THEIR CHANGES.	DHCP LEASES	DISTRIBUTE DHCP LEASE DATA	X	X

DATA BASE GROUPING

FIG. 83A

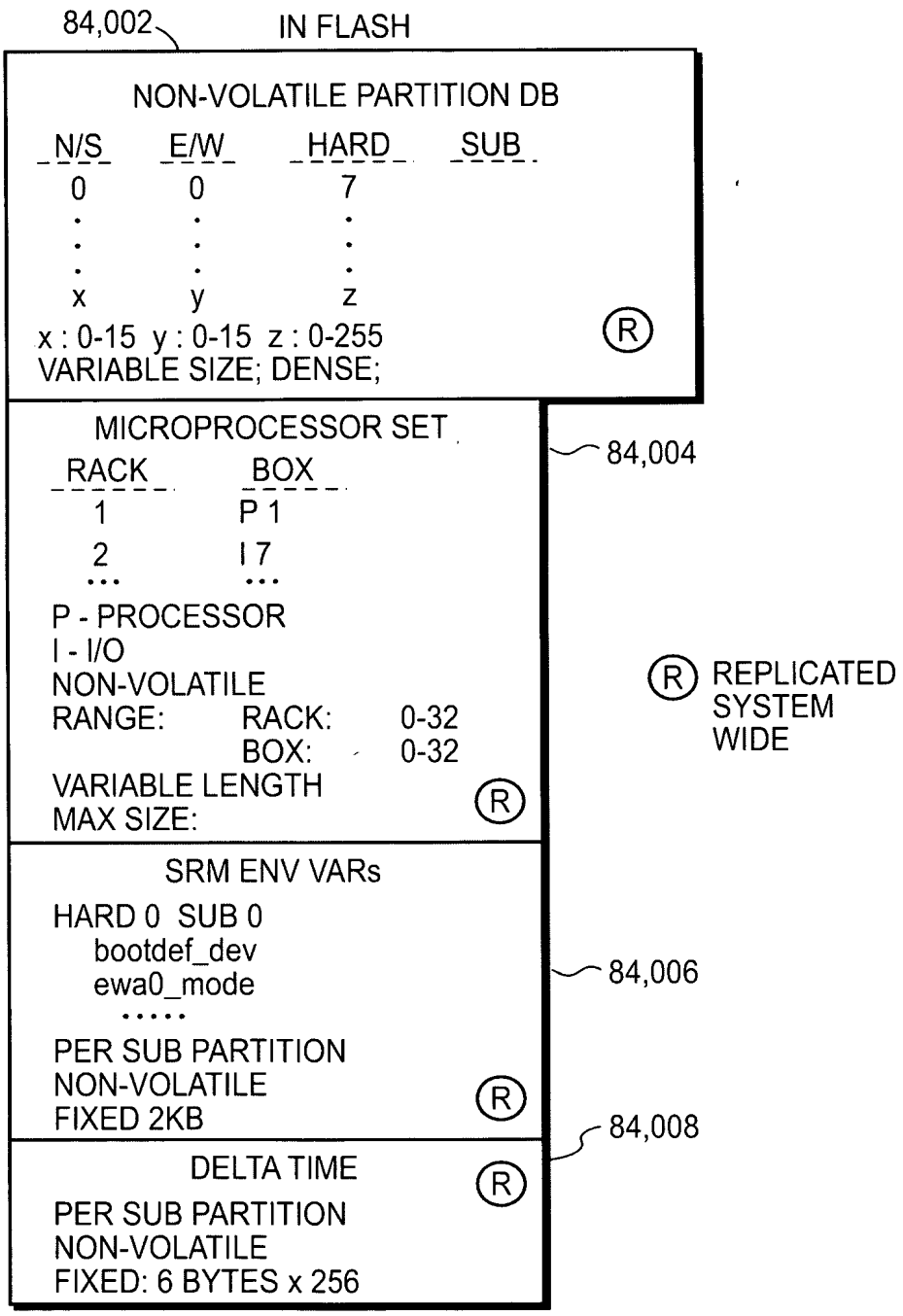
OPERATIONS	STRUCTURES	DISTRIBUTION COMMAND	RAM	NVR
THE PARTITION COORDINATOR DISTRIBUTES TO ALL MEMBERS ANY CHANGES IN THE STATUS OF THE PARTITION'S STATE AND ATTRIBUTES TO ALLOW FAIL-OVER RECOVERY TO ANOTHER PARTITION COORDINATOR. CHANGES TO MEMORY, IO7 AND COMMUNITY ASSIGNMENTS AMONG THE SUB-PARTITIONS. PARTITION STATES ARE AFFECTED BY THE ONGOING STARTING, ROUTING, LOADING, HALTING, RESETTING AND POWER CONTROLS ON THE ENTIRE PARTITION. ATTRIBUTE FIELDS ARE: 1) OS WATCHDOG INTERVAL AND ACTION MASK, 2) BBWATCH DELTA TIME, 3) SRM ENVIRONMENT VARIABLES.	PARTITION STATE AND ATTRIBUTE FTELDS(RAM) THE STATE AND OS WATCHDOG IS KEPT IN VOLATILE RAM AND ALL OTHER ATTRIBUTES ARE STORED IN NVRAM LOCATION FOR THAT PARTITION	SET PARTITION STATE AND ATTRIBUTES OR DISTRIBUTE ONE PARTITION'S DATABASE	X	X
MBM MAINTAINS IN RAM STATUS ON IT'S OWN CMMs, EV7s AND MEMORY. I2C DATA, ERROR LOG COUNT. KNOBS AND OCP DATA IS MAINTAINED IN RAM.	CMM EV7 STATUS. EC SENSOR AND EEROM VALUES, VOLATILE KNOBS.	MBM REPORT CONFIGURATION	X(NOT DISTRIB UTED)	
MBM MAINTAINS THE CURRENT USE OF THE OCP SWITCH SETTINGS AND SOME KNOBS. DEFAULT IS ENTIRE SYSTEM ENCLOSURE.	OCP SWITCH CONTROL, PERMANENT KNOBS.	OCP TO PARTITION ASSIGNMENT	X(NOT DISTRIB UTED)	X(NOT DISTRIB UTED)
PBM MAINTAINS A RAM COPY OF IT'S OWN 107 IDS, I2C DATA AND ERROR LOG COUNT.	107 RISER IDS. I2C SENSOR AND EEROM VALUES.	PBM REPORT CONFIGURATION, RECEIVE CABLE ID	X(NOT DISTRIB UTED)	
PBM RECEIVES FROM SRM THE PCI CONFIGURATION DATA AND RETRIEVES IT ON REQUEST. MBM, PBM AND CMM KNOB DATA ARE KEPT IN NVRAM TO TAILOR BEHAVIOR.	PCI CONFIG SPACE (RAM), KNOB DATA.	STORE PCI SLOT INFO, READ PCI SLOT INFO. SET KNOB DATA, GET KNOB DATA.	X(NOT DISTRIB UTED)	X(NOT DISTRIB UTED)

DATA BASE GROUPING

FIG. 83B

200301883-1
US Serial No. 09/652,458
David Golden, et al

84,000



REPLICATED DATABASE

FIG. 84A

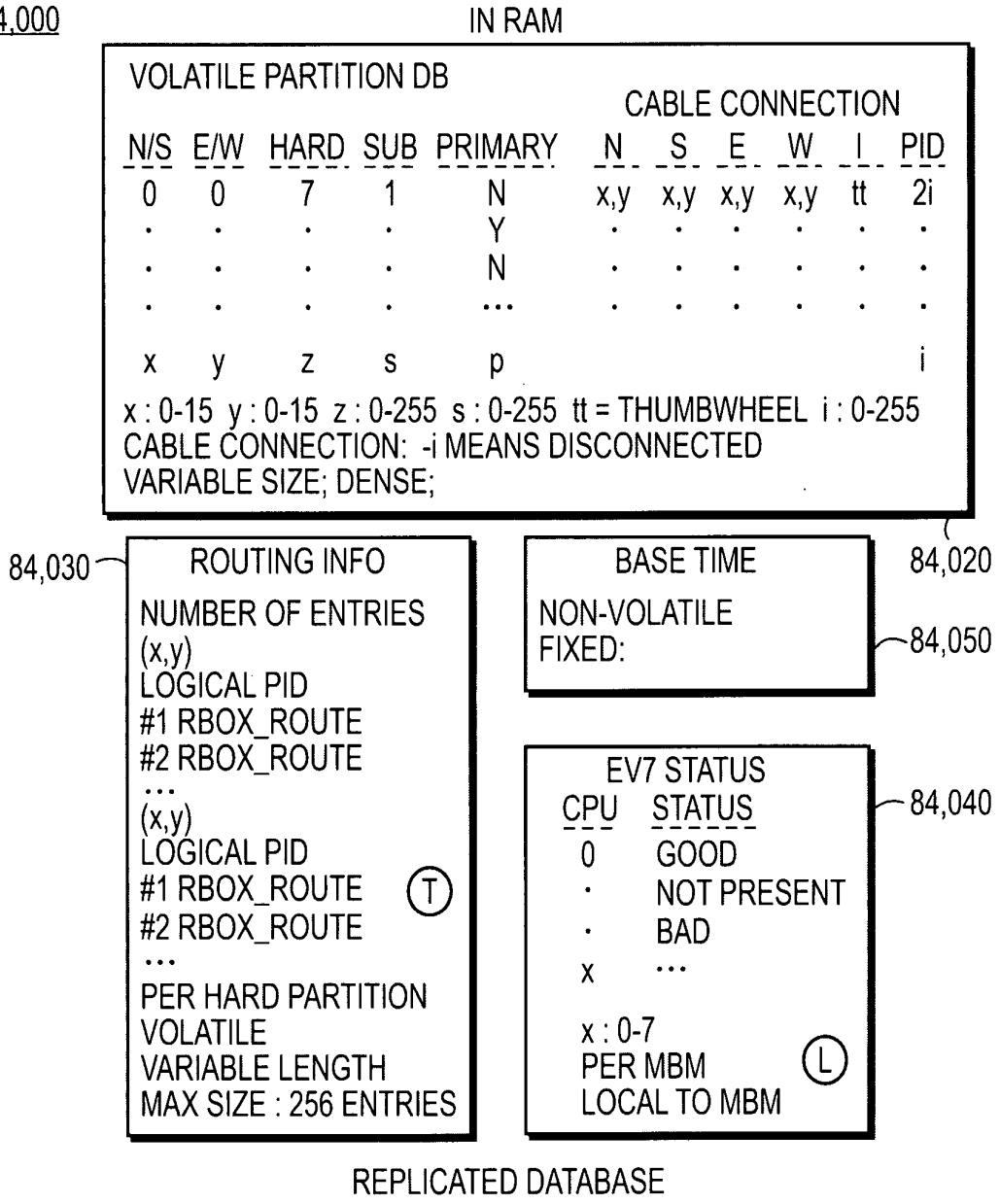

84,000
IN RAM
VOLATILE PARTITION DB
CABLE CONNECTION
N/S E/W HARD SUB PRIMARY N S E W I PID
0 0 7 1 N x,y x,y x,y x,y tt 2i
Y
N
x y z s p i
x : 0-15 y : 0-15 z : 0-255 s : 0-255 tt = THUMBWHEEL i : 0-255
CABLE CONNECTION: -i MEANS DISCONNECTED
VARIABLE SIZE; DENSE;
84,020
84,030
ROUTING INFO
NUMBER OF ENTRIES
(x,y)
LOGICAL PID
#1 RBOX_ROUTE
#2 RBOX_ROUTE
(x,y)
LOGICAL PID
#1 RBOX_ROUTE
#2 RBOX_ROUTE
PER HARD PARTITION
VOLATILE
VARIABLE LENGTH
MAX SIZE : 256 ENTRIES
T
BASE TIME
NON-VOLATILE
FIXED:
84,050
EV7 STATUS
CPU STATUS
0 GOOD
NOT PRESENT
BAD
x
x : 0-7
PER MBM
LOCAL TO MBM
84,040
L
L LOCAL TO MBM
T TEMPORARY/ SHORT LIVED
REPLICATED DATABASE

FIG. 84B

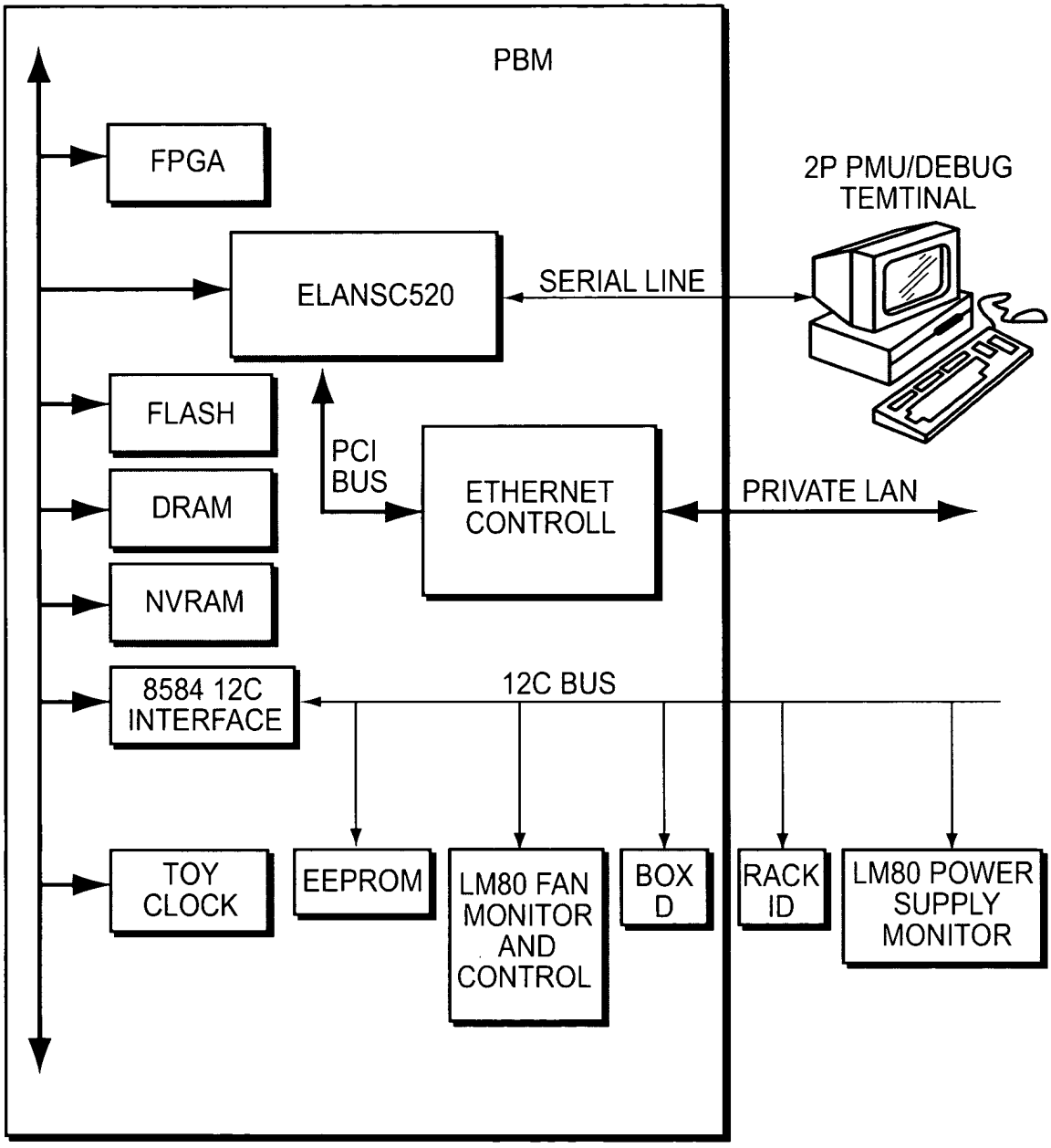
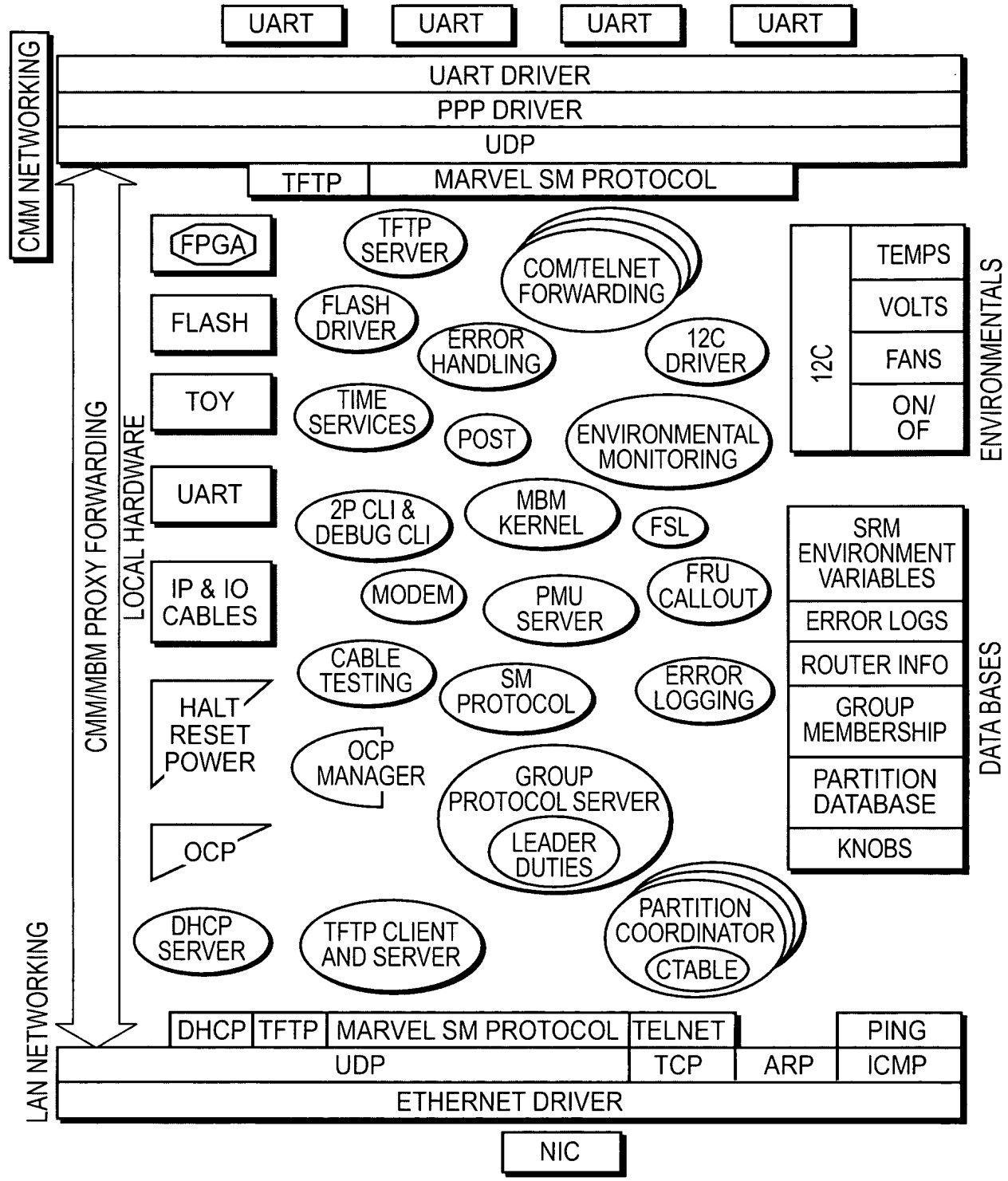

PBM
FPGA
2P PMU/DEBUG
TEMTINAL
ELANSC520
SERIAL LINE
FLASH
PCI
BUS
DRAM
ETHERNET
CONTROLL
PRIVATE LAN
NVRAM
8584 12C
INTERFACE
12C BUS
TOY
CLOCK
EEPROM
LM80 FAN
MONITOR
AND
CONTROL
BOX
D
RACK
ID
LM80 POWER
SUPPLY
MONITOR

FIG. 85



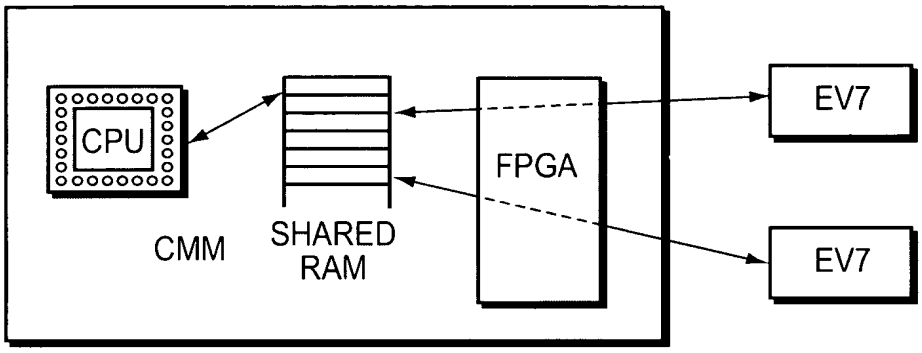
PBM HARDWARE OVERVIEW

FIG. 86

ERROR CODE	DEVICE TYPE	REPOR-TER	OCP/CLI TEXT	LOG	ALERT	SYSEVENT	EEROM	LOC DATA	OCP
1	NONE	ANYONE	FREE TEXT	X	?	?		X	?
2	NONE	ANYONE	BINARY WITH NO TEXT	X	?	?		X	?
1	EV7	CMM	EV7 XX FAILURE CODE XX ON RUNNING OS	X	X	X		X	X
2	EV7	CMM	EV7 XX FAILURE IN ROUTING TO EV7 XX	X	X				
3	EV7	CMM	EV7 XX POWER ON FAILED	X	X				X
4	EV7	CMM	EV7 XX FAILED ON TEST XX WITH STATUS XX	X	X				
5	EV7	CMM	EV7 XX OVERHEATED AT XXX FAHR.	X	X	X			X
6	EV7	CMM	EV7 XX XX.X POWER AT XX.X	X	X	X			X
7	RIMM	CMM	MEMORY TEST XX FAILURE FOR RIMM XX	X	X			X	X
8	FPGA	CMM	CMM X FPGA FAILURE XX	X	X				X
9	CMM	CMM	CMM X POST ERROR XX	X	X			X	X
10	CMM	CMM	CMM X FAILSAFE REQUIRED	X	X		X		X
11	CMM	CMM	CMM X FAILED TO START TEST XX	X	X		X	X	X
12	12C	CMM, MBM	12C FAILURE ON MBM XX CMM X	X	X	X			X
13	MBM	MBM	MBM POST FAILURE XX	X	X			X	X
14	POWER SUPPLY	MBM	POWER SUPPLY X CAN'T POWER ON	X	X	X		X	X
15	LAN	MBM	NO PEER COMMUNICATION ON LAN	X		X			X
16	UART	MBM	COM PORT X FAILURE	X	X				X
17	MBM/ PBM	MBM/ PBM	FAIL SAFE LOAD REQUIRED	X	X				X
18	LAN	MBM/PBM	IN ISOLATED GROUP ON LAN	X	X	X			X
19	MEMORY	MBM/PBM	SINGLE/MULTI BIT ECC ERROR	X	X			X	X
20	WDT	MBM/PBM	MBM WATCHDOG RESET	X	X	X			X
21	WDT	MBM	WATCHDOG EXPIRED ON PARTITION XX	X	X	X			X
22	IO7	PBM	IO7 XX DRAWER NOT ACCESSIBLE	X	X	X			X
23	TEMP	MBM/PBM	TEMPERATURE TOO HIGH XXX FAHR	X	X	X	X		X
24	EEROM	CMM, MBM/PBM	EEROM XX NOT ACCESSIBLE	X	X				X
25	OCP	MBM	EV7 XX POWER ON FAILED	X	X				
26	OCP	MBM	EV7 XX NOT CONNECTED TO IO7 XX	X		X			X
27	EV7	MBM	VIRTUAL CONSOLE AT EV7 XX BUSY	X					X

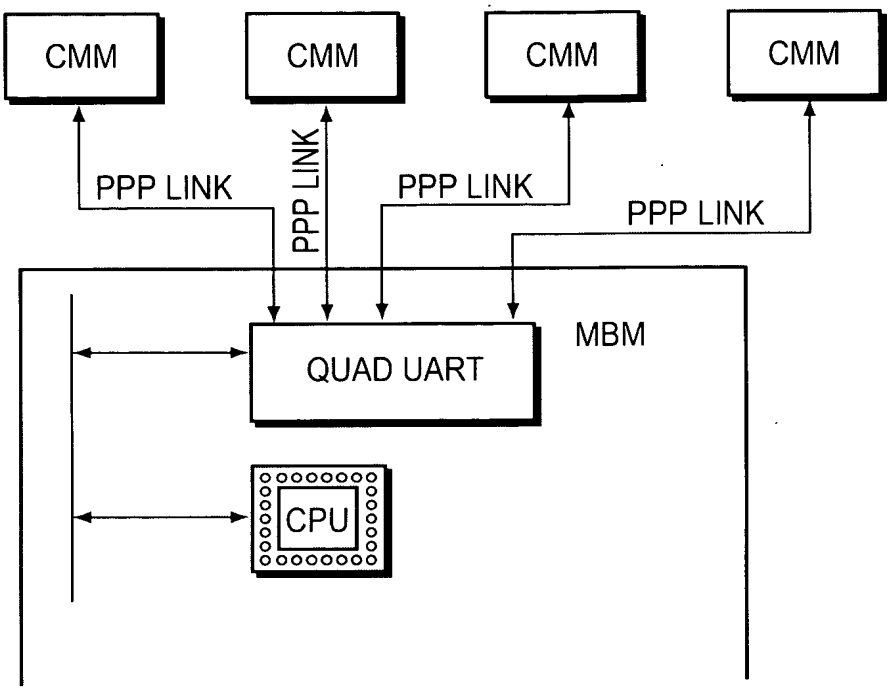
ERROR CODES

FIG. 87



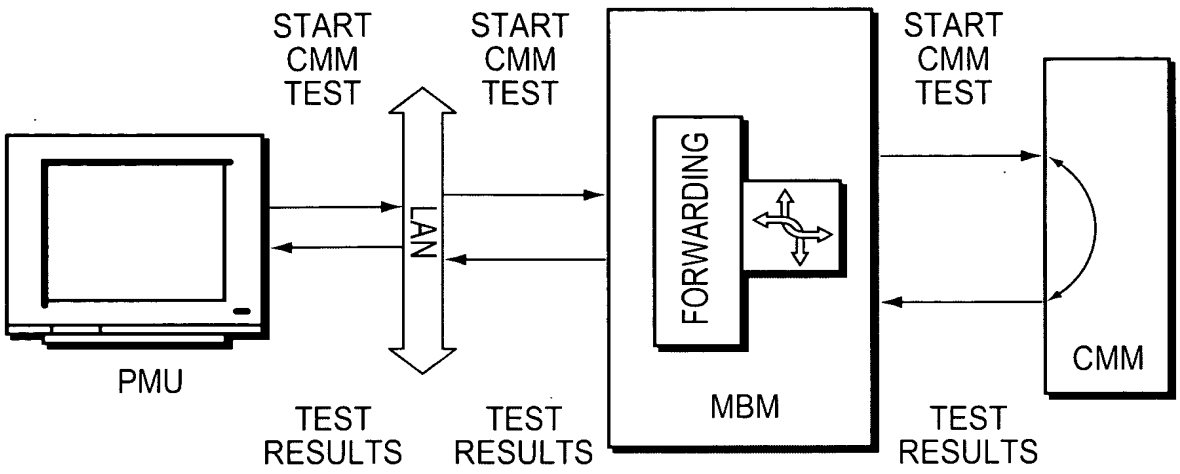
SHARED RAM COMMUNICATION

FIG. 88



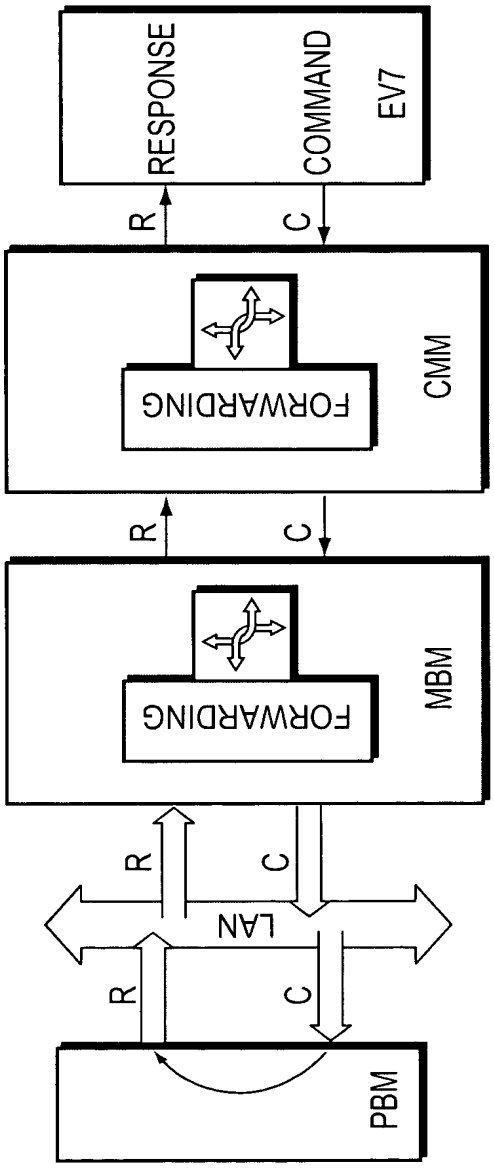
MBM TO CMM COMMUNIICATION

FIG. 89



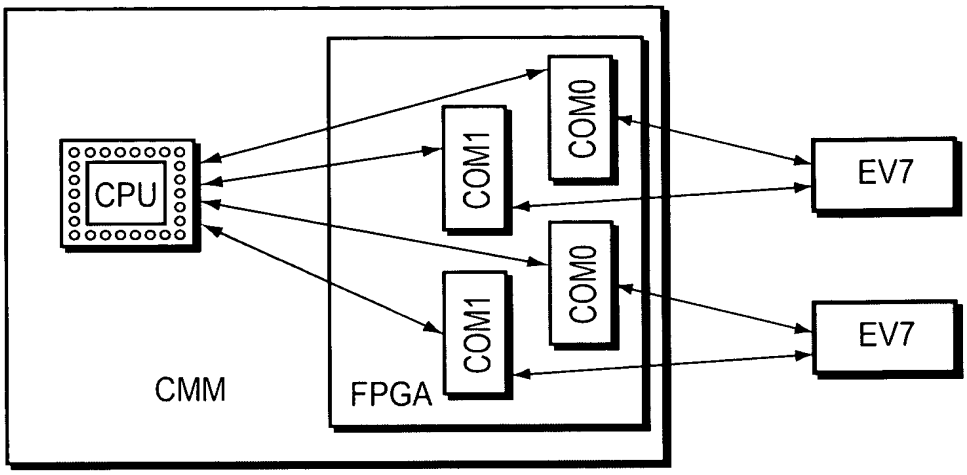
EXAMPLE OF MBM FORWARDING

FIG. 90



EXAMPLE OF CMM FORWARDING

FIG. 91



CMM COM PORT CONNECTION

FIG. 92

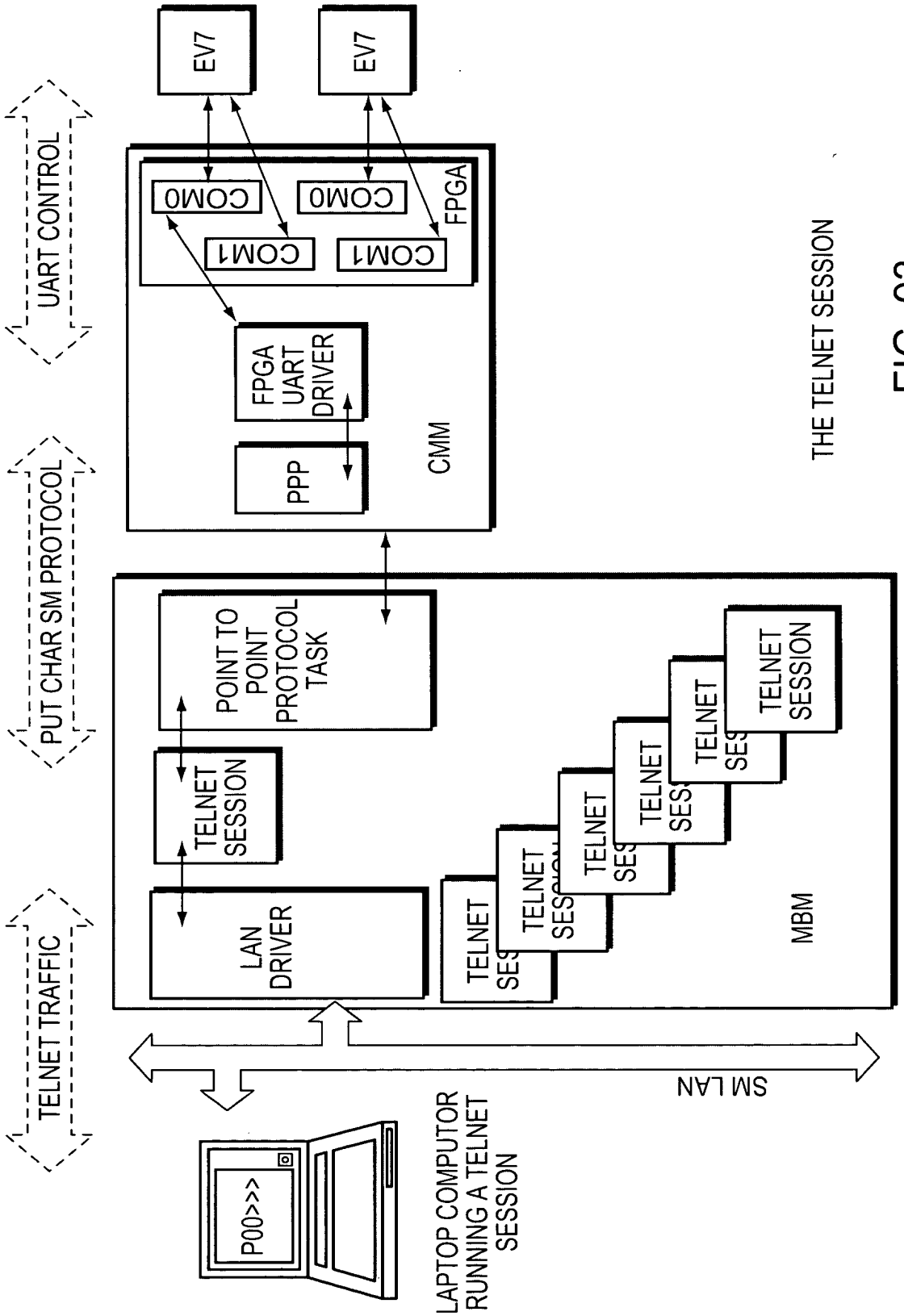

EV7
EV7
UART CONTROL
COM0
COM0
COM1
COM1
FPGA
FPGA UART DRIVER
PPP
CMM
PUT CHAR SM PROTOCOL
POINT TO POINT PROTOCOL TASK
TELNET SESSION
LAN DRIVER
TELNET SESSION
MBM
THE TELNET SESSION
TELNET TRAFFIC
SM LAN
P00>>>
LAPTOP COMPUTOR RUNNING A TELNET SESSION

FIG. 93

REQUEST FORMAT

SIZE (DEC)	START (HEX)	END (HEX)	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
4	0	3	ORIGINATOR IP ADDRESS							
4	4	7	DESTINATION IP ADDRESS							
4	8	B	IDENTIFIER							
2	C	D	COMMAND CODE							
n	E	n+E	DATA (OPTIONAL)							

REQUEST FORMAT

FIG. 94

RESPONSE FORMAT

SIZE (DEC)	START (HEX)	END (HEX)	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
4	0	3	ORIGINATOR IP ADDRESS							
4	4	7	DESTINATION IP ADDRESS							
4	8	B	IDENTIFIER							
2	C	D	RESPONSE CODE							
2	E	F	STATUS (SEE APP. A)							
n	10	n+10	DATA (OPTIONAL)							

RESPONSE FORMAT

FIG. 95

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
ORIGINATOR IP ADDRESS (0)							
DESTINATION IP ADDRESS (FFFFFFFFh)							
IDENTIFIER (GroupID)							
COMMAND CODE							
SM LAN MESSAGE							

TRAIN MESSAGE HEADER FORMAT

FIG. 96

COMMAND DESCRIPTOR	CODE
NEW GROUP	0101h
ACCEPT GROUP OFFER	0102h
REJECT GROUP OFFER	0103h
JOIN GROUP	0104h
PROBE MICROPROCESSOR	0105h
I-am-alive	0106h
REPORT CONFLICTING ADDRESS	0107h
SET MEMBERSHIP CONFIGURATION	0108h

LAN FORMATION GROUP

FIG. 97

COMMAND DESCRIPTOR	CODE
FULL TRAIN MESSAGE	0201h
EMPTY TRAIN MESSAGE	0202h

RELIABLE MESSAGE GROUP

FIG. 98

COMMAND DESCRIPTOR	CODE
GET CMM STATE	0310h
GET MBM CONFIGURATION	0321h
GET PBM CONFIGURATION	0322h
GET PARTITION DATABASE	0323h
DISTRIBUTE PARTITION DATABASE	0324h
GET SYSTEM TOPOLOGY	0330h
STORE PCI SLOT INFO	0331h
GET PCI SLOT INFO	0332h
GET OWN PARTITION NUMBER	0333h

SYSTEM DISCOVERY GROUP

FIG. 99

COMMAND DESCRIPTOR	CODE
CREATE PARTITION	0401h
SET PARTITION ATTRIBUTES	0402h
MOVE EV7S TO PARTITION	0403h
REMOVE EV7S FROM PARTITION	0404h
SAVE PARTITION ASSIGNMENT	0405h
START PARTITION	0406h
RESET PARTITION	0407h
POWER ON PARTITION	0408h
POWER OFF PARTITION	0409h
HALT PARTITION	040Ah
ADD EV7S TO RUNNING PARTITION	040Bh
DELETE EV7S FROM RUNNING PARTITION	040Ch
SWITCH PRIMARY EV7	040Dh
DESTROY PARTITION	040Eh
CONTINUE PARTITION	040Fh
COMPUTE ROUTING	0410h
CONFIGURE RBOX/CBOX	0411h
SET PARTITION STATE	0412h
GET STATE OF OCP SWITCHES	0413h
OCP SWITCH ASSIGNMENT	0414h
POWER ON/OFF	0415h
SYSTEM EVENT	0416h
ASSIGN SUB PARTITIONS TO COMMUNITY	0417h[1]
GET HARD PARTITION MEMORY ASSIGNMENTS	0418h
ASSIGN MEMORY BLOCK TO SUB PARTITION	0419h
ASSIGN 107 TO SUB PARTITION	041Ah
STORE ENVIRONMENT VARIABLES	041Bh
GET ENVIRONMENT VARIABLES	041Ch

PARTITION CONTROL GROUP

[1] COMMUNITIES ARE TO BE IMPLEMENTED AT A LATER PHASE OF DEVELOPMENT

FIG. 100

COMMAND DESCRIPTOR	CODE
EV7 RESET ON/OFF	0501h
EV7 PULSED RESET	0502h
EV7 HALT ON/OFF	0503h
EV7 QUIESCE	0504h
EV7 RBOX/CBOX CONFIG.	0505h
REQUEST EV7 START TEST	0506h
LOAD IMAGE	0507h
LOAD & RUN SRM	0508h

EV7 SETUP GROUP

FIG. 101

COMMAND DESCRIPTOR	CODE
SET CABLE TEST SIGNAL STATE	0601h
GET CABLE TEST SIGNAL STATE	0602h
SEND CABLE ID	0603h
RECEIVE CABLE ID	0604h
GET MBM IP CABLING	0605h
GET PBM IO CABLING	0606h
GET CABLING CONFIGURATION	0607h
RECONFIGURE CABLING	0608h

CABLE TEST GROUP

FIG. 102

COMMAND DESCRIPTOR	CODE
GET TELNET IP ADDRESS/PORT	0701h
PUT CHARS FROM KEYBOARD TO VIRTUAL CONS	0702h

VIRTUAL CONSOLE GROUP

FIG. 103

COMMAND DESCRIPTOR	CODE
GET FIRMWARE VERSION	0801h
UPGRADE FIRMWARE	0802h
LOAD TEST VERSION	0803h
DISABLE TEST VERSION	0804h

FIRMWARE LOAD AND UPGRADE GROUP

FIG. 104

COMMAND DESCRIPTOR	CODE
GET VOLTAGE READINGS	0901h
GET TEMPERATURE READINGS	0902h
GET FAN RPM READINGS	0903h
SET FAN RPM SPEED	0904h
SET OCP DISPLAY DATA	0905h
SET ATTENTION INDICATOR	0906h
GET SWITCH STATE	0907h
GET POWER SUPPLY STATE	0908h

ENVIRONMENTAL RETRIEVAL GROUP

FIG. 105

COMMAND DESCRIPTOR	CODE
GET EEROM DATA	0A01h
SET EEROM DATA	0A02h

FRU DATA GROUP

FIG. 106

COMMAND DESCRIPTOR	CODE
ERROR REPORTING	0B01h
GET ERROR LOG COUNT	0B02h
ERROR LOG CLEAR	0B03h
GET ERROR LOG ENTRY	0B04h

ERROR LOGGING GROUP

FIG. 107

COMMAND DESCRIPTOR	CODE
START OS WATCH DOG	0C01h
KEEP ALIVE	0C02h
STOP OS WATCHDOG	0C03h

OS WATCH DOG TIMER

FIG. 108

COMMAND DESCRIPTOR	CODE
GET BASE TIME	0D01h
SET BASE TIME	0D02h
DISTRIBUTE BASE TIME CHANGE	0D03h
SET PARTITION DELTA TIME	0D04h
GET PARTITION DELTA TIME	0D05h

DATE/TIME GROUP

FIG. 109

COMMAND DESCRIPTOR	CODE
GET KNOB	0E01h
SET KNOB	0E02h
UNRECOGNIZED RESPONSE	0E03h
DISTRIBUTE DHCP LEASE DATA	0E04h
READ	0E05h
WRITE	0E06h

MISCELLANEOUS GROUP

FIG. 110